

IDENTIFICATION

PRODUCT CODE: MAINDEC-15-DØBØ-D
PRODUCT NAME: INSTRUCTION TEST (PART 1 and 1A)
DATE REVISED: MAY 14, 1970
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: EARL HAIGHT

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1. ABSTRACT

This program test all PDP-15 operate group instructions and the following memory reference instructions: LAC, ADD, TAD, SAD, and XOR. The program is divided into two parts with a binary tape for each part. Part 1 contains all the operate group, LAC, AND, and XOR tests and must run properly before Part 1A is attempted. Part 1A contains the TAD, ADD, and SAD tests.

2. REQUIREMENTS

Equipment

PDP-15

Storage

4K core storage minimum

Preliminary Programs

Previous run of memory and memory address tests.

3. LOADING PROCEDURE

Method

Set PDP-15 to BANK MODE
Program object tape(s) - punched in hardware read-in format (HRI) used, make sure the teletype is on-line and that the reader switch is in the Start position. Set console address switches to 0200, press I/O Reset key and then press Read-In key. The program will now load.

4. STARTING PROCEDURE

Starting and/or Restarting Addresses

Part 1 = 2000₈

Part 1A = 2000₈

Program and/or Operator Action

AC Switch Settings

Program will ring bell every 1000₈ passes.

AC SW 0 = 1 will suppress bell

5. OPERATING PROCEDURE

- a. Load programs
- b. Depress I/O Reset, then depress Start.
Program will now run without any further operator action as long as no errors occur. Every 100 passes thru the program the teletype bell will ring (unless AC SW $\emptyset = 1$).

6. ERRORS

Error Halts and Description

Reference symbolic listing for the address of the individual error halt location.

<u>TEST</u>	<u>ERROR</u>	<u>TAGS</u>	<u>TEST LOCATIONS</u>
OPERATE	H1	thru H289	Part 1
LAC	H288	H367	Part 1
AND	H368A	H521	Part 1
XOR	H522	H698	Part 1
TAD	H647-	H879	Part 1A 201-2174
ADD	H880-	H977	Part 1A 2175-3515
SAD	H978-	H1105	Part 1A 3516-4431

Error Recovery

Insert a JUM instruction at the error halt address (E-XXXX) back to the start of the test.

6. RESTRICTIONS

Part 1 must run in its entirety before attempting to run Part 1A.

.TITLE 151T1
 /PDP-15 INSTRUCTION TEST
 /COPYRIGHT 1968, DIGITAL EQUIPMENT CORP., MAYNARD, MASS.
 /OPERATE GROUP
 ,FULL

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00200          .LOC 200
          /PRESS IO RESET CLEAR AC AND LINK
          /
00200 607300          JMP BEGIN          /TEST SKP
00201 741000          PART1 SKP
00202 740040          H1 HALT          /ERRORISKP FAILED TO SKIP
          /TEST SPA
00203 750000          CLA          /AC=0
00204 741100          SPA
00205 740040          H2 HALT          /ERRORISPA FAILED TO SKIP
          /TEST SNA
00206 750000          CLA          /AC=0
00207 741200          SNA
00210 741000          SKP
00211 740040          H3 HALT          /ERRORISPA SKIPPED
          /TEST SZL
00212 754000          CLA!CLL          /AC=0 LINK=0
00213 741400          SZL
00214 740040          H4 HALT          /ERRORISZL FAILED TO SKIP
          /TEST SNL
00215 754000          CLA!CLL          /AC=0 LINK=0
00216 740400          SNL
00217 741000          SKP
00220 740040          H5 HALT          /ERRORISNL SKIPPED
          /TEST SZA
00221 750000          CLA          /AC=0
00222 740200          SZA
00223 740040          H6 HALT          /ERRORISZA FAILED TO SKIP
          /TEST SMA
00224 750000          CLA          /AC=0
00225 740100          SMA
00226 741000          SKP
00227 740040          H7 HALT          /ERRORISMA SKIPPED
          /TEST SNA=CMA
00230 750001          CLA!CMA          /AC=ALL ONES
00231 741200          SNA
00232 740040          H8 HALT          /ERRORISNA=CMA FAILED
          /TEST SNL=CML
00233 754000          CLA!CLL          /AC=0 LINK=0
00234 740002          CML          /LINK=1
00235 740400          SNL
00236 740040          H9 HALT          /ERRORISNL=CML FAILED
          /TEST SPA
00237 750001          CLA!CMA          /AC=ALL ONES
00240 741100          SPA
00241 741000          SKP
00242 740040          H10 HALT          /ERRORI SPA SHIPPED
          .EJECT

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00243	744000		CLL	/TEST SZL	
00244	740002		CML	/LINK=0	
00245	741400		SZL	/LINK=1	
00246	741000		SKP		
00247	740040	H11	HALT	/ERROR: SZL SKIPPED	
				/TEST SZA	
00250	750000		CLA	/AC=0	
00251	740200		SZA		
00252	740040	H12	HALT	/ERROR: SZA SKIPPED	
				/TEST SMA	
00253	750001		CLA CMA	/AC=ALL ONES	
00254	740100		SMA		
00255	740040	H13	HALT	/ERROR: SMA FAILED TO SKIP	
				/TEST SPA	
00256	750000		CLA	/AC=0	
00257	741100		SPA		
00260	740040	H14	HALT	/ERROR: SPA FAILED TO SKIP	
				/TEST SPA	
00261	750001		CLA CMA	/AC=ALL ONES	
00262	741100		SPA		
00263	741000		SKP		
00264	740040	H15	HALT	/ERROR: SPA FAILED TO SKIP	
				/TEST SNA	
00265	750000		CLA	/AC=0	
00266	741200		SNA		
00267	741000		SKP		
00270	740040	H16	HALT	/ERROR: SNA FAILED TO SKIP	
				/TEST SNA	
00271	750001		CLA CMA	/AC=ALL ONES	
00272	741200		SNA		
00273	740040	H17	HALT	/ERROR: SNA FAILED TO SKIP	
				/TEST SZL	
00274	744000		CLL	/LINK=0	
00275	741400		SZL		
00276	740040	H18	HALT	/ERROR: SZL FAILED TO SKIP	
				/TEST SZL	
00277	744000		CLL	/LINK=0	
00300	740002		CML	/LINK=1	
00301	741400		SKP SZL		
00302	741000		SKP		
00303	740040	H19	HALT	/ERROR:	
				/TEST SNA=SPA?	
00304	750000		CLA	/AC=0	
00305	741300		SNA SPA		
00306	741000		SKP		
00307	740040	H20	HALT	/ERROR: SNA CLA SKIPPED	
				/TEST SPA=SZL	
00310	754000		CLA CLL	/AC=0 LINK=0	
00311	741500		SPA SZL		
00312	740040	H21	HALT	/ERROR: SPA SZL FAILED TO SKIP	
				/TEST SPA=SZL	
00313	754000		CLA CLL	/AC=0 LINK=0	
00314	740002		CML		
00315	741500		SPA SZL		

00316	741000		SKP	
00317	740040	H22	HALT	/ERROR=SPAISZL SKIPPED
				/TEST SNA=SZL
00320	754000		CLAICLL	/AC=0 LINK=0
00321	741600		SNAISZL	
00322	741000		SKP	
00323	740040	H23	HALT	/ERRORISNAISZL SKIPPED
				/TEST SNA=SZL
00324	754000		CLAICLL	/AC=0 LINK=0
00325	740003		CMAICML	/AC=ALL ONES LINK=1
00326	741600		SNAISZL	
00327	741000		SKP	
00330	740040	H24	HALT	/ERRORISNAISZL SKIPPED
				/TEST SNA=SZL
00331	750000		CLA	/AC=0
00332	740001		CMA	/AC=ALL ONES
00333	744000		CLL	/LINK=0
00334	741600		SNAISZL	
00335	740040	H25	HALT	/ERRORISNAISZL FAILED TO SKIP
				/TEST SNA=SZL=SNA
00336	754000		CLAICLL	/AC=0 LINK=0
00337	741600		SNAISZL	
00340	741000		SKP	
00341	740040	H26	HALT	/ERRORISPAISNA SKIPPED
				/TEST SKP=SPA=SZL
00342	754000		CLAICLL	/AC=0 LINK=0
00343	741500		SPAISZL	
00344	740040	H27	HALT	/ERRORISKPISPAISZL FAILED TO SKIP
				/TEST =SNA=SZL
00345	754000		CLAICLL	/AC=0 LINK=0
00346	741600		SNAISZL	
00347	741000		SKP	
00350	740040	H28	HALT	/ERRORISNAISZL SKIPPED
				/TEST SKP=SNA=SZL
00351	754000		CLAICLL	/AC=0 LINK=0
00352	740001		CMA	/AC=ALL ONES
00353	741600		SNAISZL	
00354	740040	H29	HALT	/ERRORISNAISZL FAILED TO SKIP
				/TEST SMA=SNL
00355	754000		CLAICLL	/AC=0 LINK=0
00356	740003		CMAICML	/AC=ALL ONES LINK=1
00357	740500		SMAISNL	
00360	740040	H30	HALT	/ERRORISMAISNL FAILED TO SKIP
				/TEST SMA=SNL
00361	754000		CLAICLL	/AC=0 LINK=0
00362	740002		CML	/LINK=1
00363	740500		SMAISNL	
00364	740040	H31	HALT	/ERRORISMAISNL FAILED TO SKIP
				/TEST SMA=SNL
00365	754000		CLAICLL	/AC=0 LINK=0
00366	740002		CML	/AC=ALL ONES
00367	740500		SMAISNL	
00370	740040	H32	HALT	/ERRORISMAISNL FAILED TO SKIP
				/TEST SMA=SNL
00371	754000		CLAICLL	/AC=0 LINK=0

00372	740500		SMA:SNL	
00373	741000		SKP	
00374	740040	H33	HALT	/ERRORISMA:SNL SKIPPED
				/TEST SZA=SNL
00375	754000		CLA:CLL	/AC=0 LINK=0
00376	740600		SZA:SNL	
00377	740040	H34	HALT	/ERRORISZA:SNL FAILED TO SKIP
				/TEST SZA=SNL
00400	754000		CLA:CLL	/AC=0 LINK=0
00401	740001		CMA	/AC=ALL ONES
00402	740600		SZA:SNL	
00403	741000		SKP	
00404	740040	H35	HALT	/ERRORISZA:SNL SKIPPED
				/TEST SZA=SNL
00405	754000		CLA:CLL	/AC=0 LINK=0
00406	740003		CMA:CML	/AC=ALL ONES LINK=1
00407	740600		SZA:SNL	
00410	740040	H36	HALT	/ERRORISZA:SNL FAILED TO SKIP
				/TEST SZA=SMA=SNL
00411	754000		CLA:CLL	/AC=0 LINK=0
00412	740700		SZA:SMA:SNL	
00413	740040	H37	HALT	/ERRORISZA:SMA:SNL FAILED TO SKIP
				/TEST SZA=SMA=SNL
00414	754000		CLA:CLL	/AC=0 LINK=0
00415	740001		CMA	/AC=ALL ONES
00416	740700		SZA:SMA:SNL	
00417	740040	H38	HALT	/ERRORISZA:SMA:SNL FAILED TO SKIP
				/TEST SMA=SZA=SNL
00420	754000		CLA:CLL	/AC=0 LINK=0
00421	740003		CMA:CML	/AC=ALL ONES LINK=1
00422	740700		SZA:SMA:SNL	
00423	740040	H39	HALT	/ERRORISZA:SMA:SNL FAILED TO SKIP
				/TEST CLA=CMA
00424	750000		CLA	/AC=0
00425	740001		CMA	/AC=LL ONES
00426	740001		CMA	/AC=0
00427	740001		CMA	/AC=ALL ONES
00430	740001		CMA	/AC=0
00431	740200		SZA	
00432	740040	H40	HALT	/ERRORICMA FAILED TO COMPLEMENT AC
				/TEST CLA=CMA
00433	750001		CLA:CMA	/AC=ALL ONES
00434	750001		CLA:CMA	/AC=ALL ONES
00435	750001		CLA:CMA	/AC=ALL ONES
00436	740100		SMA	
00437	740040	H41	HALT	/ERRORICLA:CMA FAILED
				/TEST CLL=CML
00440	744000		CLL	/LINK=0
00441	740002		CML	/LINK=1
00442	740002		CML	/LINK=0
00443	740002		CML	/LINK=1
00444	740400		SNL	
00445	740040	H42	HALT	/ERRORICML FAILED TO COMPLEMENT LINK
				/TEST CLL=CML
00446	744002		CLL:CML	/LINK=1

00447	744002		CLL!CML	/LINK=1
00450	740400		SNL	
00451	740040	H43	HALT	/ERRORICLL!CML FAILED
				/TEST CLA=CMA=CLL=CML
00452	754003		CLA!CMA!CLL!CML	/AC=ALL ONES LINK=1
00453	741600		SNA!SZL	
00454	741000		SKP	
00455	740040	H44	HALT	/ERRORICLA!CMA!CLL!CML FAILED
				/TEST SZA=CLA
00456	750001		CLA!CMA	/AC=ALL ONES
00457	750200		SZA!CLA	
00460	741000		SKP	
00461	740040	H45	HALT	/ERRORISZA!CLA FAILED
				/TEST SNA=CLA
00462	750001		CLA!CMA	/AC=ALL ONES
00463	751200		SNA!CLA	
00464	740040	H46	HALT	/ERRORISNA!CLA FAILED
				/TEST SZA=CMA
00465	750001		CLA!CMA	/AC=ALL ONES
00466	740201		SZA!CMA	
00467	741000		SKP	
00470	740040	H47	HALT	/ERRORISZA!CMA FAILED
				/TEST SNA=CMA
00471	750001		CLA!CMA	/AC=ALL ONES
00472	741201		SNA!CMA	
00473	740040	H48	HALT	/ERRORISNA!CMA FAILED
				/TEST SZL=CLL
00474	744002		CLL!CML	/LINK=1
00475	745400		SZL!CLL	
00476	741000		SKP	
00477	740040	H49	HALT	/ERRORISZL!CLL FAILED
				/TEST SZL=CML
00500	744000		CLL	/LINK=0
00501	741402		SZL!CML	
00502	740040	H50	HALT	/ERRORISZL!CML FAILED
				/TEST SNL=CLL
00503	744002		CLL!CML	/LINK=1
00504	744400		SNL!CLL	
00505	740040	H51	HALT	/ERRORISNL!CML FAILED
				/TEST SNL=CML
00506	744000		CLL	/LINK=0
00507	740402		SNL!CML	
00510	741000		SKP	
00511	740040	H52	HALT	/ERRORISNL!CML FAILED
				/TEST NOP
00512	750000		CLA	/AC=0
00513	740000		NOP	
00514	740200		SZA	
00515	740040	H53	HALT	/ERRORINOP ALTERED THE AC
				/TEST NOP
00516	750001		CLA!CMA	/AC=ALL ONES
00517	740000		NOP	
00520	740200		SZA	
00521	741000		SKP	
00522	740040	H54	HALT	/ERRORINOP CLEARED THE AC

00523	754000		CLAICLL	/TEST NOP
00524	740000		NOP	/AC=0 LINK=0
00525	741400		SZL	
00526	740040	H55	HALT	/ERRORINOP SET THE LINK
00527	744002		CLLICML	/TEST NOP
00530	740000		NOP	/LINK=1
00531	740400		SNL	
00532	740040	H56	HALT	/ERRORINOP CLEARED LINK
00533	750000		CLA	/TEST IAC
00534	740030		IAC	/AC=0
00535	741200		SNA	/ADD 1
00536	740040	H57	HALT	/ERRORIIAC FAILED
00537	750001		CLAICMA	/TEST IAC=CMA
00540	740030		IAC	/AC=ALL ONES
00541	740200		SZA	/ADD 1
00542	740040	H58	HALT	/ERRORIIAC FAILED
00543	750031		CLAICMAIIAC	/TEST CLA=CMA=IAC
00544	740200		SZA	/AC=0
00545	740040	H59	HALT	/ERRORICLAICMAIIAC FAILED
00546	754001		CLAICMAICLL	/TEST IAC (COMPLEMENT LINK)
00547	740030		IAC	/AC=ALL ONES LINK=0
00550	740400		SNL	
00551	740040	H60	HALT	/ERRORIIAC CARRYOUT FAILED
00552	754031		CLAICMAICLLIIAC	/TEST CLA=CMA=CLL=IAC
00553	740400		SNL	/AC=ALL ONES
00554	740040	H61	HALT	/ERRORIIAC FAILED
00555	754031		CLAICMAICLLIIAC	/TEST CLA=CMA=CLL=IAC
00556	741400		SZL	
00557	741000		SKP	
00560	740040	H62	HALT	/ERRORICLAICMAICLLICMLIIAC FAILED
00561	750000		CLA	/TEST SKP=IAC
00562	741030		SKP!IAC	/AC=0
00563	740040	H63	HALT	/ERRORISKPIIAC FAILED
00564	750000		CLA	/TEST SPA=IAC
00565	740030		IAC	/AC=0
00566	741130		SPA!IAC	
00567	740040	H64	HALT	/ERRORISPAIIAC FAILED
00570	750001		CLAICMA	/TEST SNA=IAC
00571	741230		SNA!IAC	/AC=ALL ONES
00572	740040	H65	HALT	/ERRORISNAIIAC FAILED
00573	754001		CLAICMAICLL	/TEST SZL=IAC
00574	741430		SZL!IAC	/AC=ALL ONES LINK=0
00575	740040	H66	HALT	/ERRORISZLIIAC FAILED

00576	754001		/TEST SNL=IAC
00577	740430		CLA!CMAICLL /AC=ALL ONES LINK=0
00600	741000		SNL!IAC
00601	740040	H67	SKP HALT /ERRORISNLIAC FAILED
00602	750000		/TEST SZA=IAC
00603	740230		CLA /AC=0
00604	740040	H68	SZA!IAC HALT /ERRORISZAIAC FAILED
00605	750001		/TEST SMA=IAC
00606	740130		CLA!CMA /AC=ALL ONES
00607	740040	H69	SMA!IAC HALT /ERRORISMAIAC FAILED
00610	750000		/TEST SKP=SPA=IAC
00611	741130		CLA /AC=0
00612	740040	H70	SKP!SPA!IAC HALT /ERRORISKP!SPA!IAC
00613	750001		/TEST SKP=SNA=IAC
00614	741230		CLA!CMA /AC=0
00615	740040	H71	SNA!IAC HALT /ERRORISNAIAC FAILED TO SKIP
00616	754001		/TEST SKP=SNA=SZL=IAC
00617	741630		CLA!CMAICLL /AC=ALL ONES LINK=0
00620	740040	H72	SKP!SNA!SZL!IAC HALT /ERRORISKP!SNA!SZL!IAC FAILED
00621	754001		/TEST SNL=SZA=IAC
00622	740630		CLA!CMAICLL /AC=ALL ONES LINK=0
00623	741000		SNL!SZA!IAC
00624	740040	H73	SKP HALT /ERRORISNLSZAIAC FAILED
00625	754001		/TEST SNL=SMA=IAC
00626	740530		CLA!CMAICLL /AC=ALL ONES LINK=0
00627	740040	H74	SNL!SMA!IAC HALT /ERRORISNLSMAIAC FAILED
00630	754001		/TEST SZA=SMA=IAC
00631	740330		CLA!CMAICLL /AC=ALL ONES LINK=0
00632	740040	H75	SZA!SMA!IAC HALT /ERRORISZA!SMA!IAC FAILED
00633	754000		/TEST SNHA
00634	742030		CLA!ICLL /AC=0 LINK=0
00635	740200		SWHA
00636	740040	H76	SZA HALT /ERRORI SWHA ALTERED AC
00637	754000		/TEST SNHA
00640	742030		CLA!ICLL /AC=0 LINK=0
00641	741400		SWHA
00642	740040	H77	SZL HALT /ERRORI SWHA ALTERED THE LINK
00643	750001		/TEST SWHA
00644	742030		CLA!CMA /AC=ALL ONES
00645	741200		SWHA
00646	740040	H78	SNA HALT /ERRORI SWHA ZEROED AC
00647	754003		/TEST SWHA
			CLA!CMAICLL!ICML /AC=ALL ONES LINK=1

00650	742030		SWHA	
00651	740400		SNL	
00652	740040	H79	HALT	/ERROR! SWHA ZEROED LINK
				/TEST SWHA=SPA
				/AC=0
00653	750000		CLA	
00654	742030		SWHA	
00655	741100		SPA	
00656	740040	H80	HALT	/ERROR! AC0=1
				/TEST SWHA=SMA
				/AC=ALL ONES WHEN SWAPPED
00657	750001		CLA!CMA	
00660	742030		SWHA	
00661	740100		SMA	
00662	740040	H81	HALT	/ERROR! AC0=0 WHEN SWAPPED
				/TEST SWHA=IAC
				/AC=0
00663	750031		CLA!CMA!IAC	
00664	742030		SWHA	
00665	741100		SPA	
00666	740040	H82	HALT	/ERROR! SWHA=IAC FAILED
				/TEST SWHA=SMA=SNL
00667	754001		CLL!CLA!CMA	
00670	743630		SPL!SWHA!SNA	
00671	740040	H83	HALT	/ERROR! SMA!SWHA!SNL FAILED TO SKIP
				/TEST SZA=SNL=SWHA
				/AC=0 LINK=1
00672	754002		CLA!CLL!CML	
00673	742630		SZA!SNL!SWHA	
00674	740040	H84	HALT	/ERROR! SMA!SNL!SWHA FAILED TO SKIP
				/TEST SKP=SWHA
				/AC=0
00675	750000		CLA	
00676	743030		SKP!SWHA	
00677	740040	H85	HALT	/ERROR! SKP!SWHA FAILED TO SKIP
				/TEST SPA=SWHA
				/AC=ALL ONES
00700	750001		CLA!CMA	
00701	743130		SPA!SWHA	
00702	741000		SKP	
00703	740040	H86	HALT	/ERROR! SPA!SWHA SKIPPED
				/TEST SNA=SWHA
				/AC=ALL ONES
00704	750001		CLA!CMA	
00705	743230		SNA!SWHA	
00706	740040	H87	HALT	/ERROR! SNA!SWHA FAILED TO SKIP
				/TEST SZL=SNHA
				/AC=0 LINK=0
00707	754000		CLA!CLL	
00710	743430		SZL!SWHA	
00711	740040	H88	HALT	/ERROR! SZL!SWHA FAILED TO SKIP
				/TEST SPA=SWHA
				/AC=0
00712	754000		CLA!CLL	
00713	743130		SPA!SWHA	
00714	740040	H89	HALT	/ERROR! SKP!SPA!SWHA FAILED TO SKIP
				/TEST SNA=SZL=SWHA
				/AC=ALL ONES LINK=0
00715	754001		CLL!CLA!CMA	
00716	743630		SNA!SZL!SWHA	
00717	740040	H90	HALT	/ERROR! SNA!SZL!SWHA FAILED TO SKIP
				/TEST SZL=SNHA
				/AC=0 LINK=1
00720	754002		CLA!CLL!CML	
00721	743430		SZL!SWHA	
00722	741000		SKP	

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00723 740040 H91 HALT /ERROR: SKP:SZL:SWHA SKIPPED
/TEST SPA=SNA=SWHA
00724 750001 CLA:ICMA /AC=ALL ONES
00725 743330 SPA:SN:SWHA
00726 741000 SKP
00727 740040 H92 HALT /ERROR: SPA:SN:SWHA SKIPPED
/TEST SPA=SZL=SWHA
00730 754000 CLA:ICLL /AC=0 LINK=0
00731 743230 SPA:SZL:SWHA
00732 740040 H93 HALT /ERROR: SPA:SZL:SWHA FAILED TO SKIP
/TEST SKP=SNA=SWHA
00733 750001 CLA:ICMA /AC=ALL ONES
00734 743230 SKP:SN:SWHA
00735 740040 H94 HALT /ERROR: SKP:SN:SWHA FAILED TO SKIP
/TEST SKP=SPA=SZL=SWHA
00736 754000 CLA:ICLL /LINK=0 AC=0
00737 743530 SKP:SPA:SZL:SWHA
00740 740040 H95 HALT /ERROR: SKP:SPA:SZL:SWHA FAILED TO SKIP
/TEST SKP=SNA=SZL=SWHA
00741 754003 CLA:ICLL:ICML:ICMA /AC=ALL ONES LINK=1
00742 743630 SKP:SN:SZL:SWHA
00743 741000 SKP
00744 740040 H96 HALT /ERROR: SWHA FAILED
00745 740020 RAR
00746 740100 SMA
00747 740040 H97 HALT /ERROR: AC NOT 0 RAL FAILED
00750 740400 SNL
00751 740040 H98 HALT /ERROR: LINK NOT SET
/TEST RAR =AC
00752 754000 CLA:ICLL /AC=0, L=0
00753 740020 RAR
00754 740200 SZL
00755 740040 H99 HALT /ERROR: AC NOT 0 RAR FAILED
00756 741400 SZL
00757 740040 H100 HALT /ERROR: LINK NOT 0 RAR FAILED
/TEST RTL=AC
00760 754000 CLA:ICLL /AC=0, L=0
00761 742010 RTL
00762 740200 SZL
00763 740040 H101 HALT /ERROR: AC NOT 0 RTL FAILED
00764 741400 SZL
00765 740040 H102 HALT /ERROR: LINK NOT 0 RTL FAILED
/TEST RTR = AC
00766 754000 CLA:ICLL /AC=0, L=0
00767 742020 RTR
00770 740200 SZL
00771 740040 H103 HALT /ERROR: AC NOT 0 RTR FAILED
00772 741400 SZL
00773 740040 H104 HALT /ERROR: LINK NOT 0 RTR FAILED
/TEST RAR=SMA
00774 754002 CLA:ICLL:ICML /AC=0, L=1
00775 740020 RAR
00776 740100 SMA
00777 740040 H105 HALT /ERROR: AC BIT 0 NOT 1 RAR OR SMA
/FAILED

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01000 754002          /TEST RAR=SZL
01001 740020          CLA:CLLICML /AC=0, L=1
01002 741400          RAR
01003 740040          SZL
H100  HALT /ERROR; LINK NOT 0 RAR FAILED
          /TEST RAL = SNA
01004 754002          CLA:CLLICML /AC=0, L=1
01005 740010          RAL
01006 741200          SNA
01007 740040          H107 HALT /ERROR; AC BIT17 NOT 1 RAL OR SNA FAILED
          /TEST RAL=SZL
01010 754002          CLA:CLLICML /AC=0, L=1
01011 740010          RAL
01012 741400          SZL
01013 740040          H108 HALT /ERROR; LINK NOT 0 RAL FAILED
          /TEST RTR=SNA
01014 754002          CLA:CLLICML /AC=0, L=1
01015 742020          RTR
01016 741200          SNA
01017 740040          H109 HALT /ERROR; AC BIT 1 NOT 1 RTR OR SNA FAILED
          /TEST RTR=SZL
01020 754002          CLA:CLLICML /AC=0, L=1
01021 742020          RTR
01022 741400          SZL
01023 740040          H110 HALT /ERROR; LINK NOT 0 RTR FAILED
          /TEST RTL=SNA
01024 754002          CLA:CLLICML /AC=0, L=1
01025 742010          RTL
01026 741200          SNA
01027 740040          H111 HALT /ERROR; AC BIT 16 NOT 1 RTL OR SNA
          /FAILED
01030 754002          /TEST RTL=SZL
01031 742010          CLA:CLLICML /AC=0, L=1
01032 741400          RTL
01033 740040          H112 SZL
          HALT /ERROR; LINK NOT 0 RTL FAILED
          /TEST RAR = RAL
01034 754002          CLA:CLLICML /AC=0, L=1
01035 740020          RAR
01036 740010          RAL
01037 740400          SNL
01040 740040          H113 HALT /ERROR; LINK BIT NOT 1 AFTER ROTATES
          /TEST RTR=RTL
01041 754002          CLA:CLLICML /AC=0, L=1
01042 742020          RTR
01043 742010          RTL
01044 740400          SNL
01045 740040          H114 HALT /ERROR; LINK BIT NOT 1 AFTER ROTATES
          /TEST RAL=RAR
01046 754002          CLA:CLLICML /AC=0, L=1
01047 740010          RAL
01050 740020          RAR
01051 740400          SNL
01052 740040          H115 HALT /ERROR; LINK BIT NOT 1 AFTER ROTATES
          /TEST RTL=RTR

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01053	754002	CLA:CLLICML	/AC=0, L=1
01054	742010	RTL	
01055	742020	RTR	
01056	740400	SNL	
01057	740040	H116	HALT /ERROR: LINK BIT NOT 1 AFTER ROTATES
			/TEST RAR SERIES AND LINK
01060	754002	CLA:CLLICML	/AC=0, L=1
01061	740020	RAR	
01062	740020	RAR	
01063	740020	RAR	
01064	740020	RAR	
01065	740020	RAR	
01066	740020	RAR	
01067	740020	RAR	
01070	740020	RAR	
01071	740020	RAR	
01072	740020	RAR	
01073	740020	RAR	
01074	740020	RAR	
01075	740020	RAR	
01076	740020	RAR	
01077	740020	RAR	
01100	740020	RAR	
01101	740020	RAR	
01102	740020	RAR	
01103	741600	SNA:SZL	
01104	740040	H117	HALT /ERROR: AC BIT 17 NOT 1, OR L=1 AFTER
			/ROTATE SERIES
			/TEST RAL SERIES AND LINK
01105	754002	CLA:CLLICML	/AC=0, L=1
01106	740010	RAL	
01107	740010	RAL	
01110	740010	RAL	
01111	740010	RAL	
01112	740010	RAL	
01113	740010	RAL	
01114	740010	RAL	
01115	740010	RAL	
01116	740010	RAL	
01117	740010	RAL	
01120	740010	RAL	
01121	740010	RAL	
01122	740010	RAL	
01123	740010	RAL	
01124	740010	RAL	
01125	740010	RAL	
01126	740010	RAL	
01127	740010	RAL	
01130	741600	SNA:SZL	
01131	740040	H118	HALT /ERROR: AC BIT 0 NOT 1, OR L=1 AFTER
			/ROTATE SERIES
			/TEST RTL SERIES AND LINK
01132	754002	CLA:CLLICML	/AC=0, L=1
01133	742010	RTL	
01134	742010	RTL	

01135	742010	RTL	
01136	742010	RTL	
01137	742010	RTL	
01140	742010	RTL	
01141	742010	RTL	
01142	742010	RTL	
01143	742010	RTL	
01144	741600	SNA!SZL	
01145	740040	HALT	H119 /ERROR; AC BIT 0 NOT 1, OR L=1 AFTER
			/ROTATE SERIES
			/TEST RTR SERIES AND LINK
01146	754002	CLA!CLLICML	/AC=0, LINK=1
01147	742020	RTR	
01150	742020	RTR	
01151	742020	RTR	
01152	742020	RTR	
01153	742020	RTR	
01154	742020	RTR	
01155	742020	RTR	
01156	742020	RTR	
01157	742020	RTR	
01160	741600	SNA!SZL	
01161	740040	HALT	H120 /ERROR; AC BIT 17 NOT 1, OR L=1 AFTER
			/ROTATE SERIES
			/TEST RAR
01162	754003	CLA!CMAICLLICML	/AC=ONES, L=1
01163	740020	RAR	
01164	740001	CMA	/AC=0
01165	740200	SZA	
01166	740040	HALT	H121 /ERROR; AC NOT ONES RAR FAILED
01167	740400	SNL	
01170	740040	HALT	H122 /ERROR; LINK NOT 1 RAR FAILED
			/TEST RAL
01171	754003	CLA!CMAICLLICML	/AC=ONES, L=1
01172	740010	RAL	
01173	740001	CMA	/AC=0
01174	740200	SZA	
01175	740040	HALT	H123 /ERROR; AC NOT ONES RAL FAILED
01176	740400	SNL	
01177	740040	HALT	H124 /ERROR; LINK NOT 1 RAL FAILED
			/TEST RTR
01200	754003	CLA!CMAICLLICML	/AC=ONES, L=1
01201	742020	RTR	
01202	740001	CMA	/AC=0
01203	740200	SZA	
01204	740040	HALT	H125 /ERROR; AC NOT ONES RTR FAILED
01205	740400	SNL	
01206	740040	HALT	H126 /ERROR; LINK NOT 1 RTR FAILED
			/TEST RTL
01207	754003	CLA!CMAICLLICML	/AC=ONES, L=1
01210	742010	RTL	
01211	740001	CMA	/AC=0
01212	740200	SZA	
01213	740040	HALT	H127 /ERROR; AC NOT ONES RTL FAILED
01214	740400	SNL	

01215	740040	H128	HALT	/ERROR; LINK NOT 1 RTL FAILED
				/TEST RAR
01216	754001		CLA:CMACLL	/AC=ONES, L=0
01217	740020		RAR	
01220	740001		CMA	/AC=10000
01221	741200		SNA	
01222	740040	H129	HALT	/ERROR; AC BIT 0 NOT 0 RAR FAILED
01223	740400		SNL	
01224	740040	H130	HALT	/ERROR; LINK NOT 1 RAR FAILED
				/TEST RAL
01225	754001		CLA:CMACLL	/AC=ONES, L=0
01226	740010		RAL	
01227	740001		CMA	/AC=000001
01230	741200		SNA	
01231	740040	H131	HALT	/ERROR; AC BIT 17 NOT 0, RAL FAILED
01232	740400		SNL	
01233	740040	H132	HALT	/ERROR; LINK NOT RAL FAILED
				/TEST RTL
01234	754001		CLA:CMACLL	/AC=ONES, L=0
01235	742010		RTL	
01236	740001		CMA	/AC=000002
01237	741200		SNA	
01240	740040	H133	HALT	/ERROR; AC BIT 16 NOT 0 RTL FAILED
01241	740400		SNL	
01242	740040	H134	HALT	/ERROR; LINK NOT 1 RTL FAILED
				/TEST RTR
01243	754001		CLA:CMACLL	/AC=ONES, L=0
01244	742020		RTR	
01245	740001		CMA	/AC=200000
01246	741200		SNA	
01247	740040	H135	HALT	/ERROR; AC BIT 1 NOT 0 RTR FAILED
01250	740400		SNL	
01251	740040	H136	HALT	/ERROR; LINK NOO 1 RTR FAILED
				/TEST RAR=RAL
01252	754001		CLA:CMACLL	/AC=ONES, L=0
01253	740020		RAR	
01254	740010		RAL	
01255	740001		CMA	/AC=0
01256	740200		SZA	
01257	740040	H137	HALT	/ERROR; AC NOT ONES AFTER ROTATES
01260	741400		SZL	
01261	740040	H138	HALT	/ERROR; LINK NOT 0 AFTER ROTATES
				/TEST RTR=RTL
01262	754001		CLA:CMACLL	/AC=ONES, L=0
01263	742020		RTR	
01264	742010		RTL	
01265	740001		CMA	/AC=0
01266	740200		SZA	
01267	740040	H139	HALT	/ERROR; AC NOT ONES AFTER ROTATES
01270	741400		SZL	
01271	740040	H140	HALT	/ERROR; LINK NOT 0 AFTER ROTATES
				/TEST RAL=RAR
01272	754001		CLA:CMACLL	/AC=ONES, L=0
01273	740010		RAL	
01274	740020		RAR	

01275	740001		CMA	/AC=0
01276	740200		SZA	
01277	740040	H141	HALT	/ERROR; AC NOT ONES AFTER ROTATES
01300	741400		SZL	
01301	740040	H142	HALT	/ERROR; LINK NOT 0 AFTER ROTATES
				/TEST RTL=RTR
01302	754001		CLA!CMAICLL	/AC=ONES, L=0
01303	742010		RTL	
01304	742020		RTR	
01305	740001		CMA	/AC=0
01306	740200		SZA	
01307	740040	H143	HALT	/ERROR; AC NOT ONES AFTER ROTATES
				/PDP-15 INSTRUCTION TEST TAPE 2
				/RAR SERIES
01310	754001		CLA!CMAICLL	/AC=ONES, L=0
01311	740020		RAR	
01312	740020		RAR	
01313	740020		RAR	
01314	740020		RAR	
01315	740020		RAR	
01316	740020		RAR	
01317	740020		RAR	
01320	740020		RAR	
01321	740020		RAR	
01322	740020		RAR	
01323	740020		RAR	
01324	740020		RAR	
01325	740020		RAR	
01326	740020		RAR	
01327	740020		RAR	
01330	740020		RAR	
01331	740020		RAR	
01332	740020		RAR	
01333	740003		CMA!CML	/AC=000001, L=0
01334	741600		SNA!SZL	
01335	740040	H144	HALT	/ERROR; AC BIT 17 NOT 1, OR L=0 AFTER
				/ROTATE SERIES
				/TEST RAL SERIES TEST
01336	754001		CLA!CMAICLL	/AC=ONES, L=0
01337	740010		RAL	
01340	740010		RAL	
01341	740010		RAL	
01342	740010		RAL	
01343	740010		RAL	
01344	740010		RAL	
01345	740010		RAL	
01346	740010		RAL	
01347	740010		RAL	
01350	740010		RAL	
01351	740010		RAL	
01352	740010		RAL	
01353	740010		RAL	
01354	740010		RAL	
01355	740010		RAL	
01356	740010		RAL	

01357	740010	RAL	
01360	740010	RAL	
01361	740002	CML	/AC=377777, L=0
01362	741500	SPA:SZL	
01363	740040	H145 HALT	/ERROR: AC BIT 0 NOT 0, OR L=0 AFTER
			/ROTATE SERIES
			/TEST RTL SERIES
01364	754001	CLA:CMACLL	/AC=ONES, L=0
01365	742010	RTL	
01366	742010	RTL	
01367	742010	RTL	
01370	742010	RTL	
01371	742010	RTL	
01372	742010	RTL	
01373	742010	RTL	
01374	742010	RTL	
01375	742010	RTL	
01376	740002	CML	/L=0
01377	741500	SPA:SZL	
01400	740040	H146 HALT	/ERROR: AC BIT 0 NOT 0, OR L=0 AFTER
			/ROTATE SERIES
			/TEST RTR SERIES
01401	754001	CLA:CMACLL	/AC=ONES, L=0
01402	742020	RTR	
01403	742020	RTR	
01404	742020	RTR	
01405	742020	RTR	
01406	742020	PTR	
01407	742020	RTR	
01410	742020	RTR	
01411	742020	RTR	
01412	742020	RTR	
01413	740003	CMA:CML	/AC=000001, L=0
01414	741600	SNA:SZL	
01415	740040	H147 HALT	/ERROR: AC BIT 17 NOT 1, OR L=0
			/AFTER ROTATE SERIES
			/TEST CLAIRAL
01416	754001	CLA:CMACLL	/AC=ONES, L=0
01417	750010	CLA:RAL	/AC=0
01420	740200	SEA	
01421	740040	H148 HALT	/ERROR: AC NOT 0, CLAIRAL
			/FAILED
			/TEST CLAIRAR
01422	754001	CLA:CMACLL	/AC=ONES, L=0
01423	750020	CLA:RAR	/AC=0
01424	740200	SEA	
01425	740040	H149 HALT	/ERROR: AC NOT 0, CLAIRAR FAILED
			/TEST CLL:RAL
01426	754002	CLA:CLL:CMCML	/AC=0, L=1
01427	744010	CLL:RAL	/AC=0, L=0
01430	740200	SEA	
01431	740040	H150 HALT	/ERROR: AC NOT 0, CLL:RAL FAILED
			/TEST CLL:RAR
01432	754002	CLA:CLL:CMCML	/AC=0, L=1
01433	744020	CLL:RAR	/AC=0, L=0

01434	740200		SZA	
01435	740040	H151	HALT	/ERROR: AC NOT 0, CLLIRAR FAILED
				/TEST CLLICLAIRAL
01436	754003		CLA:CMALICML	/AC=0NES, L=1
01437	754010		CLL:CLAIRAL	/AC=0, L=0
01440	740200		SZA	
01441	740040	H152	HALT	/ERROR: AC NOT 0, L NOT 0
				/CLL:CLAIRAL FAILED
01442	741400		SZL	
01443	740040	H153	HALT	/ERROR: LINK NOT 0
				/CLL:CLAIRAL FAILED
				/TEST CLLICLAIRAR
01444	754003		CLA:CMALICML	/AC=0NES, L=1
01445	754020		CLL:CLAIRAR	/AC=0, L=0
01446	740200		SZA	
01447	740040	H154	HALT	/ERROR: AC NOT 0
				/CLL:CLAIRAR FAILED
01450	741400		SZL	
01451	740040	H155	HALT	/ERROR: L NOT 0
				/CLL:CLAIRAR FAILED
				/TEST RALISKP
01452	754000		CLA:CLL	/AC=0, L=0
01453	741010		RAL:SKP	
01454	740040	H156	HALT	/ERROR: SKP FAILED TO SKIP
				/TEST RALISPA
01455	754002		CLL:CMALICLA	/AC=0, L=1
01456	742020		RTR	/AC=200000, L=0
01457	741110		SPA:RAL	
01460	740040	H157	HALT	/ERROR: SPA FAILED TO SKIP
				/TEST RARISPA
01461	754002		CLL:CMALICLA	/AC=0, L=1
01462	741120		SPA:RAR	
01463	740040	H158	HALT	/ERROR: SPA FAILED TO SKIP
				/TESS RTLISPA
01464	754002		CLL:CMALICLA	/AC=0, L=1
01465	742020		RTR	/AC=200000
01466	740020		RAR	/AC=400000
01467	743110		SPA:RTL	
01470	740040	H159	HALT	/ERROR: SPA FAILED TO SKIP
				/TEST RTRISPA
01471	754002		CLL:CMALICLA	/AC=0, L=1
01472	740010		RAL	/AC=200001, L=0
01473	743120		SPA:RTR	
01474	740040	H160	HALT	/ERROR: SPA FAILED TO SKIP
				/TEST SPAIRAL
01475	754002		CLA:CLL:ICML	/AC=0, L=1
01476	740020		RAR	/AC=400000
01477	741110		RAL:SPA	
01500	741000		SKP	
01501	740040	H161	HALT	/ERROR: SPA SKIPPED
				/TEST SPAIRAR
01502	754002		CLA:CLL:ICML	/AC=0, L=1
01503	740020		RAR	/AC=400000
01504	741120		RAL:SPA	
01505	741000		SKP	

01506	740040	H162	HALT	/ERROR: SPA SKIPPED
				/TEST SPAIRTL
01507	754002		CLA:CLLICML	/AC=0, L=1
01510	740020		RAR	/AC=400000
01511	743120		RTL:SPA	
01512	741000		SKP	
01513	740040	H163	HALT	/ERROR: SPA SKIPPED
				/TEST SPAIRTR
01514	754002		CLA:CLLICML	/AC=0, L=1
01515	740020		RAR	/AC=400000
01516	743120		RTR:SPA	
01517	741000		SKP	
01520	740040	H164	HALT	/ERROR: SPA SKIPPED
				/TEST CLASPAIRAR
01521	754002		CLA:CLLICML	/AC=0, L=1
01522	740020		RAR	/AC=400000
01523	751120		CLA:SPAIRAR	
01524	741000		SKP	
01525	740040	H165	HALT	/ERROR: SPA SKIPPED
				/TEST RALISNA
01526	754002		CLA:CLLICML	/AC=0, L=1
01527	741210		RAL:SNA	
01530	741000		SKP	
01531	740040	H166	HALT	/ERROR: SNA SKIPPED
				/TEST RARISNA
01532	754002		CLA:CLLICML	/AC=0, L=1
01533	741220		RAR:SNA	
01534	741000		SKP	
01535	740040	H167	HALT	/ERROR: SNA SKIPPED
				/TEST RTLISNA
01536	754002		CLA:CLLICML	/AC=0, L=1
01537	743210		RTL:SNA	
01540	741000		SKP	
01541	740040	H168	HALT	/ERROR: SNA SKIPPED
				/TEST RTRISNA
01542	754002		CLA:CLLICML	/AC=0, L=1
01543	743220		RTR:SNA	
01544	741000		SKP	
01545	740040	H169	HALT	/ERROR: SNA SKIPPED
				/TEST RALISNA
01546	754002		CLA:CLLICML	/AC=0, L=1
01547	740020		RAR	/AC=400000
01550	741210		SNA:RAL	
01551	740040	H170	HALT	/ERROR: SNA FAILED TO SKIP
				/TEST RARISNA
01552	754002		CLA:CLLICML	/AC=0, L=1
01553	740010		RAL	/AC=000001
01554	741220		SNA:RAR	
01555	740040	H171	HALT	/ERROR: SNA FAILED TO SKIP
				/TEST RTLISNA
01556	754002		CLA:CLLICML	/AC=0, L=1
01557	742020		RTR	/AC=200000
01560	743210		SNA:RTL	
01561	740040	H172	HALT	/ERROR: SNA FAILED TO SKIP
				/TEST RTRISNA

01562	754002		CLA:CLLICML	/AC=0, L=1
01563	742010		RTL	
01564	743220		SNA:RTR	
01565	740040	H173	HALT	/ERROR: SNA FAILED TO SKIP
				/TEST CLLISNA:RAR
01566	754001		CLA:CMALICLL	/AC=ONES, L=0
01567	751220		CLA:SNA:RAR	
01570	740040	H174	HALT	/ERROR: SNA FAILED TO SKIP
				/TEST RALISZL
01571	754002		CLA:CLLICML	/AC=0, L=1
01572	741410		RALISZL	
01573	741000		SKP	
01574	740040	H175	HALT	/ERROR: SZL SKIPPED
				/TEST RARISZL
01575	754002		CLA:CLLICML	/AC=0, L=1
01576	741420		RARISZL	
01577	741000		SKP	
01600	740040	H176	HALT	/ERROR: SZL SKIPPED
				/TEST RTLISZL
01601	754002		CLA:CLLICML	/AC=0, L=1
01602	743410		RTLISZL	
01603	741000		SKP	
01604	740040	H177	HALT	/ERROR: SZL SKIPPED
				/TEST RTRISZL
01605	754002		CLA:CLLICML	/AC=0, L=1
01606	743420		RTRISZL	
01607	741000		SKP	
01610	740040	H178	HALT	/ERROR: SZL SKIPPED
				/TEST RALISZL
01611	754001		CLA:CMALICLL	/AC=ONES, L=0
01612	741410		RALISZL	
01613	740040	H179	HALT	/ERROR: SZL FAILED TO SKIP
				/TEST RARISZL
01614	754001		CLA:CMALICLL	/AC=ONES, L=0
01615	741420		RARISZL	
01616	740040	H180	HALT	/ERROR: SZL FAILED TO SKIP
				/TEST RTRISZL
01617	754001		CLA:CMALICLL	/AC=ONES, L=0
01620	743420		RTRISZL	
01621	740040	H181	HALT	/ERROR: SZL FAILED TO SKIP
				/TEST CLLISZL:RAL
01622	754002		CLA:CLLICML	/AC=0, L=1
01623	745410		CLLISZL:RAL	
01624	741000		SKP	
01625	740040	H182	HALT	/ERROR: SZL SKIPPED
				/TEST SMA:RAL
01626	754002		CLA:CLLICML	/AC=0, L=1
01627	742020		RTR	/AC=200000
01630	740110		RAL:SMA	
01631	741000		SKP	
01632	740040	H183	HALT	/ERROR: SMA SKIPPED
				/TEST SMA:RAR
01633	754002		CLA:CLLICML	/AC=0, L=1
01634	740120		RAR:SMA	
01635	741000		SKP	

01636	740040	H184	HALT	/ERROR; SMA SKIPPED
				/TEST SMAIRTL
01637	754002		CLAICLLICML	/AC=0, L=1
01640	742020		RTR	/AC=200000
01641	740020		RAR	/AC=100000
01642	742110		RTLISMA	
01643	741000		SKP	
01644	740040	H185	HALT	
				/TEST SMAIRTR
01645	754002		CLAICLLICML	/AC=0, L=1
01646	740010		RAL	/AC=000001
01647	742120		RTRISMA	
01650	741000		SKP	
01651	740040	H186	HALT	/ERROR; SMA SKIPPED
				/TEST SMAIRAL
01652	754002		CLAICLLICML	/AC=0, L=1
01653	740020		RAR	/AC=400000, L=0
01654	740110		RALISMA	
01655	740040	H187	HALT	/ERROR; SMA FAILED TO SKIP
				/TEST SMAIRAR
01656	754001		CLAICMAICLL	/AC=ONES, L=0
01657	740120		RARISMA	
01660	740040	H188	HALT	/ERROR; SMA FAILED TO SKIP
				/TEST SMAIRTL
01661	754002		CLAICLLICML	/AC=0, L=1
01662	740020		RAR	/AC=400000, L=0
01663	742110		RTLISMA	
01664	740040	H189	HALT	/ERROR; SMA FAILED TO SKIP
				/TEST SMAIRTR
01665	754002		CLAICLLICML	/AC=0, L=1
01666	740020		RAR	/AC=400000, L=0
01667	742120		RTRISMA	
01670	740040	H190	HALT	/ERROR; SMA FAILED TO SKIP
				/TEST SMAIRARICLAICLL
01671	754002		CLAICLLICML	/AC=0, L=1
01672	740020		RAR	/AC=400000, L=0
01673	754120		CLAICLLIRARISMA	
01674	740040	H192	HALT	/ERROR; SMA FAILED TO SKIP
				/TEST SZAIRAL
01675	754002		CLAICLLICML	/AC=0, L=1
01676	740010		RAL	/AC=000001, L=0
01677	740210		RALISZA	
01700	741000		SKP	
01701	740040	H193	HALT	/ERROR; SZA SKIPPED
				/TEST SZAIRAR
01702	754002		CLAICLLICML	/AC=0, L=1
01703	740010		RAL	/AC=000001, L=0
01704	740220		RARISZA	
01705	741000		SKP	
01706	740040	H194	HALT	/ERROR; SZA SKIPPED
				/TEST SZAIRTL
01707	754002		CLAICLLICML	/AC=0, L=1
01710	742020		RTR	/AC=200000, L=0
01711	742210		RTLISZA	
01712	741000		SKP	

01713	740040	H195	HALT	/ERROR; SZA SKIPPED
				/TEST SZAIRTR
01714	754002		CLA:CLLICML	/AC=0, L=1
01715	742010		RTL	/AC=000002
01716	742220		RTR:SZA	
01717	741000		SKP	
01720	740040	H196	HALT	/ERROR; SZA SKIPPED
				/TEST SZAIRAL
01721	754002		CLA:CLLICML	/AC=0, L=1
01722	740210		RAL:SZA	
01723	740040	H197	HALT	/ERROR; SZA FAILED TO SKIP
				/TEST SZAIRAR
01724	754002		CLA:CLLICML	/AC=0, L=1
01725	740220		RAR:SZA	
01726	740040	H198	HALT	/ERROR; SZA FAILED TO SKIP
				/TEST SZAIRTL
01727	754002		CLA:CLLICML	/AC=0, L=1
01730	742210		RTL:SZA	
01731	740040	H199	HALT	/ERROR; SZA FAILED TO SKIP
				/TEST SZAIRTR
01732	754002		CLA:CLLICML	/AC=0, L=1
01733	742220		RTR:SZA	
01734	740040	H200	HALT	/ERROR; SZA FAILED TO SKIP
				/TEST CLLIRARICLAISZA
01735	754002		CLA:CLLICML	/AC=0, L=1
01736	740020		RAR	/AC=400000
01737	754220		CLL:RARICLAISZA	
01740	741000		SKP	
01741	740040	H201	HALT	/ERROR; SZA SKIPPED
				/TEST SNLIRAL
01742	754002		CLA:CLLICML	/AC=0, L=1
01743	740410		RAL:SNL	
01744	740040	H202	HALT	/ERROR; SNL FAILED TO SKIP
				/TEST SNLIRAR
01745	754002		CLA:CLLICML	/AC=0, L=1
01746	740420		RAR:SNL	
01747	740040	H203	HALT	/ERROR; SNL FAILED TO SKIP
				/TEST SNLIRTL
01750	754002		CLA:CLLICML	
01751	742410		RTL:SNL	
01752	740040	H204	HALT	/ERROR; SNL FAILED TO
				/TEST SNLIRTR
01753	754002		CLA:CLLICML	/AC=0, L=1
01754	742420		RTR:SNL	
01755	740040	H205	HALT	/ERROR; SNL FAILED TO SKIP
				/TEST SNLIRAL
01756	754001		CLA:CMACLL	/AC=ONES, L=0
01757	740410		RAL:SNL	
01760	741000		SKP	
01761	740040	H206	HALT	/ERROR; SNL SKIPPED
				/TEST SNLIRAR
01762	754001		CLA:CMACLL	/AC=ONES, L=0
01763	740420		RAR:SNL	
01764	741000		SKP	
01765	740040	H207	HALT	/ERROR; SNL SKIPPED

			/TEST SNLIRTL
01766	754001	CLAICMAICLL	/AC=ONES, L=0
01767	742410	RTLISNL	
01770	741000	SKP	
01771	740040	H208 HALT	/ERROR: SNL SKIPPED
			/TEST SNLIRTR
01772	754001	CLAICMAICLL	/AC=ONES, L=0
01773	742420	RTRISNL	
01774	741000	SKP	
01775	740040	H210 HALT	/ERROR: SNL SKIPPED
			/TEST CLAICLLIRALISNL
01776	754003	CLAICMAICLLICML	/AC=ONES, L=1
01777	754410	CLAICLLIRALISNL	
02000	740040	H211 HALT	/ERROR: SNL FAILED TO SKIP
			/INSTRUCTION TEST TAPE 3
			/TEST RTL=IAC=RAL=SMA
02001	754000	CLAICLL	/AC=0 LINK=0
02002	740030	IAC	/+1 AC
02003	742010	RTL	
02004	742010	RTL	
02005	742010	RTL	
02006	742010	RTL	
02007	742010	RTL	
02010	742010	RTL	
02011	742010	RTL	
02012	742010	RTL	
02013	740010	RAL	
02014	740100	SMA	
02015	740040	H212 HALT	/ERROR: AC1 NOT SET
			/TEST RTL=IAC=SNL=SZA
02016	754000	CLAICLL	/AC=0 LINK=0
02017	740030	IAC	/+1 AC
02020	742010	RTL	
02021	742010	RTL	
02022	742010	RTL	
02023	742010	RTL	
02024	742010	RTL	
02025	742010	RTL	
02026	742010	RTL	
02027	742010	RTL	
02030	742010	RTL	
02031	740400	SNL	
02032	740040	H213 HALT	/ERROR: LINK NOT SET
02033	740200	SZA	
02034	740040	H214 HALT	/ERROR: AC NOT CLEARED
			/TEST RTR=IAC
02035	754000	CLAICLL	/AC=0 LINK=0
02036	740030	IAC	/+1 AC
02037	742020	RTR	
02040	740100	SMA	
02041	740040	H215 HALT	/ERROR: AC CLEARED BY RTR
			/TEST RAR=IAC=SNL
02042	754000	CLAICLL	/AC=0 LINK=0
02043	740030	IAC	/+1 AC
02044	740020	RAR	

02045	740400		SNL	
02046	740400	H216	HALT	/ERROR! LINK NOT SET BY RAR
02047	740200		SEA	
02050	740400	H217	HALT	/ERROR! AC NOT RESET BY ROTATE
				/TEST IAC=RAL-SWHA
02051	754000		CLAICLL	/AC=0 LINK=0
02052	740030		IAC	/*1 AC
02053	740010		RAL	
02054	740010		RAL	
02055	740010		RAL	
02056	740010		RAL	
02057	740010		RAL	
02060	740010		RAL	
02061	740010		RAL	
02062	740010		RAL	
02063	742030		SWHA	
02064	740100		SMA	
02065	740040	H218	HALT	/ERROR! SWHA FAILED
				/TEST IAC=ROTATES-SWHA
02066	754002		CLAICLLICML	/AC=0 LINK=0
02067	740020		RAR	
02070	740030		IAC	/*1 AC
02071	742010		RTL	
02072	742010		RTL	
02073	742010		RTL	
02074	742010		RTL	
02075	742030		SWHA	
02076	742010		RTL	
02077	740100		SMA	
02100	740040	H219	HALT	/ERROR! AC0 NOT SET
02101	740010		RAL	
02102	740400		SNL	
02103	740040	H220	HALT	/ERROR! SWHA FAILED
				/TEST LAW 760000, AC=0
02104	754000		CLAICLL	/AC=0
02105	760000		LAW 00000	/AC=760000
02106	740010		RAL	/AC=740000
02107	744400		SNLICLL	/L=1
02110	740040	H221	HALT	/ERROR! AC BIT 0 NOT A 1, LAW 760000
				/FAILED
02111	740010		RAL	/AC=700000
02112	744400		SNLICLL	/L=1
02113	740040	H222	HALT	/ERROR! AC BIT 1 NOT A 1, LAW 760000
				/FAILED
02114	740010		RAL	/AC=600000
02115	744400		SNLICLL	/L=1
02116	740040	H223	HALT	/ERROR! AC BIT 2 NOT A 1, LAW 760000
				/FAILED
02117	740010		RAL	/AC=400000
02120	744400		SNLICLL	/L=1
02121	740040	H224	HALT	/ERROR! AC BIT 3 NOT A 1, LAW 760000
				/FAILED
02122	740010		RAL	/AC=000000
02123	744400		SNLICLL	/L=1
02124	740040	H225	HALT	/ERROR! AC BIT 4 NOT A 1, LAW 760000

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                                /FAILED
02125 740200          H226  SZA          /AC=000000
02126 740040          HALT          /ERROR; AC BITS 5-17 NOT 0
                                /LAW 760000 FAILED
                                /TEST LAW 760000, AC=ONES
02127 754001          CLAICMAICLL /AC=ONES, L=0
02130 760000          LAW 00000    /AC=760000
02131 741400          SZL
02132 740040          H227  HALT          /ERROR; LINK NOT A 0, LAW SET LINC
02133 740010          RAL          /AC=740000
02134 744400          SNLICLL    /L=1
02135 740040          H228  HALT          /ERROR; AC BIT 0 NOT A 1, LAW 760000 FAILED
02136 740010          RAL          /AC=700000
02137 744400          SNLICLL    /L=1
02140 740040          H229  HALT          /ERROR; AC BIT 1 NOT A 1, LAW 760000 FAILED
02141 740010          RAL          /AC =600000
02142 744400          SNLICLL    /L=1
02143 740040          H230  HALT          /ERROR; AC BIT 2 NOT A 1, LAW 76000 FAILED
02144 740010          RAL          /AC=400000
02145 744400          SNLICLL    /L=1
02146 740040          H231  HALT          /ERROR; AC BIT 3 NOT A 1, LAW 760000 FAILED
02147 740010          RAL          /AC=000000
02150 744400          SNLICLL    /L=1
02151 740040          H232  HALT          /ERROR; AC BIT 4 NOT A 1, LAW 760000 FAILED
02152 740200          SZA          /AC=000000
02153 740040          H233  HALT          /ERROR; AC BIT S 5-17 NOT 0
                                /LAW 760000 FAILED
                                /TEST LAW 770000, AC BIT 5
02154 754000          CLAICLL    /AC=0
02155 770000          LAW 10000    /AC=770000
02156 742010          RTL          /AC=740001
02157 742010          RTL          /AC=600007
02160 742010          RTL          /AC=000037
02161 740400          SNL
02162 740040          H234  HALT          /ERROR; AC BIT 5 NOT A1, LAW 770000 FAILED
                                /TEST LAW 764000, AC BIT 6
02163 754000          CLAICLL    /AC=0
02164 764000          LAW 4000    /AC=764000
02165 742010          RTL          /AC=720001
02166 742010          RTL          /AC=500007
02167 742010          RTL          /AC=100037
02170 740100          SMA
02171 740040          H235  HALT          /ERROR; AC BIT 6 NOT A1, LAW 764000 FAILED
                                /TEST LAW 762000, AC BIT7
02172 754000          CLAICLL    /AC=0
02173 762000          LAW 2000    /AC=762000
02174 740010          RAL          /AC=741000
02175 742010          RTL          /AC=620003
02176 742010          RTL          /AC=100017
02177 742010          RTL          /AC=400076
02200 740100          SMA
02201 740040          H236  HALT          /ERROR -AC BIT7 NOT A 1, LAW 762000 FAILED
                                /TEST LAW 761000, AC BIT 8
02202 754000          CLAICLL    /AC=0
02203 761000          LAW 1000    /AC=761000

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02204	742010	RTL	/AC=704001
02205	742010	RTL	/AC=420007
02206	742010	RTL	/AC=100037
02207	742010	RTL	/AC=400174
02210	740100	SMA	
02211	740040	H237 HALT	/ERROR; AC BIT 8 NOT A 1, LAW 761000 FAILED
			/TEST LAW 760400, AC BIT 9
02212	754000	CLA:CLL	/AC=0
02213	760400	LAW 400	/AC=760400
02214	740010	RAL	/AC=741000
02215	742010	RTL	/AC=604003
02216	742010	RTL	/AC=020017
02217	742010	RTL	/AC=100076
02220	742010	RTL	/AC=400370
02221	740100	SMA	
02222	740040	H238 HALT	/ERROR; AC BIT 9 NOT A 1, LAW 760400 FAILED
			/TEST LAW 760200, AC BIT 10
02223	754000	CLA:CLL	/AC=0
02224	760200	LAW 200	/AC=760200
02225	742010	RTL	/AC=701001
02226	742010	RTL	/AC=404007
02227	742010	RTL	/AC=020037
02230	742010	RTL	/AC=100174
02231	742010	RTL	/AC=400760
02232	740100	SMA	
02233	740040	H239 HALT	/ERROR; AC BIT 10 NOT A 1, LAW 760200 FAILED
			/TEST LAW 760100, AC BIT 11
02234	754000	CLA:CLL	/AC=0
02235	760100	LAW 100	/AC=760100
02236	740010	RAL	/AC=740200
02237	742010	RTL	/AC=601003
02240	742010	RTL	/AC=004017
02241	742010	RTL	/AC=020076
02242	742010	RTL	/AC=100370
02243	742010	RTL	/AC=401740
02244	740100	SMA	
02245	740040	H240 HALT	/ERROR; AC BIT 11 NOT A 1, LAW 760100 FAILED
			/TEST LAW 760040, AC BIT 12
02246	754000	CLA:CLL	/AC=0
02247	760040	LAW 40	/AC=760040
02250	742010	RTL	/AC=700201
02251	742010	RTL	/AC=401007
02252	742010	RTL	/AC=004037
02253	742010	RTL	/AC=020174
02254	742010	RTL	/AC=100760
02255	742010	RTL	/AC=403700
02256	740100	SMA	
02257	740040	H241 HALT	/ERROR; AC BIT 12 NOT A 1, LAW 760040 FAILED
			/TEST LAW 760020, AC BIT 13
02260	754000	CLA:CLL	/AC=0
02261	760020	LAW 20	/AC=760020
02262	740010	RAL	/AC=740040
02263	742010	RTL	/AC=600203
02264	742010	RTL	/AC=001017
02265	742010	RTL	/AC=004076

02266	742010	RTL	/AC=020370
02267	742010	RTL	/AC=101740
02270	742010	RTL	/AC=407600
02271	740100	SMA	
02272	740040	H242 HALT	/ERROR! AC BIT 13 NOT A 1, LAW 760020 FAILED /TEST LAW 760010, AC BIT 14
02273	754000	CLA:CLL	/AC=0
02274	760010	LAW 10	/AC=760010
02275	742010	RTL	/AC=700041
02276	742010	RTL	/AC=400207
02277	742010	RTL	/AC=001037
02300	742010	RTL	/AC=004174
02301	742010	RTL	/AC=020760
02302	742010	RTL	/AC=103700
02303	742010	RTL	/AC=417400
02304	740100	SMA	
02305	740040	H243 HALT	/ERROR! AC BIT 14 NOT A 1, LAW 760010 FAILED /TEST LAW 760004, AC BIT 15
02306	754000	CLA:CLL	/AC=0
02307	760004	LAW 4	/AC=760004
02310	740010	RAL	/AC=740010
02311	742010	RTL	/AC=600043
02312	742010	RTL	/AC=000217
02313	742010	RTL	/AC=001076
02314	742010	RTL	/AC=004370
02315	742010	RTL	/AC=021740
02316	742010	RTL	/AC=107600
02317	742010	RTL	/AC=437000
02320	740100	SMA	
02321	740040	H244 HALT	/ERROR! AC BIT 15 NOT A 1, LAW 760004 FAILED /TEST LAW 760002, AC BIT 16
02322	754000	CLA:CLL	/AC=0
02323	760002	LAW 2	/AC=760002
02324	742010	RTL	/AC=700011
02325	742010	RTL	/AC=400047
02326	742010	RTL	/AC=000237
02327	742010	RTL	/AC=001174
02330	742010	RTL	/AC=004760
02331	742010	RTL	/AC=023700
02332	742010	RTL	/AC=117400
02333	742010	RTL	/AC=476000
02334	740100	SMA	
02335	740040	H245 HALT	/ERROR! AC BIT 16 NOT A 1, LAW 760002 FAILED /TEST LAW 760001, AC BIT 17
02336	754000	CLA:CLL	/AC=0
02337	760001	LAW 1	/AC=760001
02340	740010	RAL	/AC=740002
02341	742010	RTL	/AC=600013
02342	742010	RTL	/AC=000057
02343	742010	RTL	/AC=000276
02344	742010	RTL	/AC=001370
02345	742010	RTL	/AC=005740
02346	742010	RTL	/AC=027600
02347	742010	RTL	/AC=137000
02350	742010	RTL	/AC=574000

02351	740100		SMA	
02352	740040	H246	HALT	/ERROR; AC BIT 17 NOT A 1, LAW 60001 FAILED
				/TEST LAW 767777, AC BIT 5
02353	754002		CLA!CLLICML	/AC=0, L=1
02354	742020		RTR	/AC=200K
02355	742020		RTR	/AC=40K
02356	742020		RTR	/C=10K
02357	744002		CLLICML	
02360	767777		LAW 7777	/AC=767777
02361	742010		RTL	/AC=737777
02362	742010		RTL	/AC=577777
02363	742010		RTL	/L=0
02364	741401		SZL!CMA	/AC=0
02365	740040	H247	HALT	/ERROR; AC BIT 5 NOT A 0, LAW 767777 FAILED
02366	740200		SZA	/AC=0
02367	740040	H248	HALT	/ERROR; AC DROPPED A BIT, LAW 767777 FAILED
				/TEST LAW 773777, AC BIT 6
02370	754002		CLA!CLLICML	/AC=0, L=1
02371	740020		RAR	/AC=400K
02372	742020		RTR	/AC=100K
02373	742020		RTR	/AC=20K
02374	742020		RTR	/AC=4K
02375	744002		CLLICML	
02376	773777		LAW 13777	/AC=773777
02377	740010		RAL	/AC=767777
02400	742010		RTL	/AC=737777
02401	742010		RTL	/AC=577777
02402	742010		RTL	/L=0
02403	741401		SZL!CMA	/AC=0
02404	740040	H249	HALT	/ERROR; AC BIT 6 NOT A 0, LAW 773777 FAILED
02405	740200		SZA	
02406	740040	H250	HALT	/ERROR; AC DROPPED A BIT, LAW 773777 FAILED
				/TEST LAW 775777, AC BIT 7
02407	754002		CLA!CLLICML	/AC=0, L=1
02410	742020		RTR	/AC=200K
02411	742020		RTR	/AC=40K
02412	742020		RTR	/AC=10K
02413	742020		RTR	/AC=2K
02414	744002		CLLICML	
02415	775777		LAW 15777	/AC=775777
02416	742010		RTL	/AC=767777
02417	742010		RTL	/AC=737777
02420	742010		RTL	/AC=577777
02421	742010		RTL	/L=0
02422	741401		SZL!CMA	/AC=0
02423	740040	H251	HALT	/ERROR; AC BIT 7 NOT A 0, LAW 775777 FAILED
02424	740200		SZA	
02425	740040	H252	HALT	/ERROR; AC DROPPED A BIT, LAW 775777 FAILED
				/TEST LAW 776777, AC BIT 8
02426	754002		CLA!CLLICML	/AC=0, L=1
02427	740020		RAR	/AC=400K
02430	742020		RTR	/AC=100K
02431	742020		RTR	/AC=20K
02432	742020		RTR	/AC=4K
02433	742020		RTR	/AC=1K

02434	744002	CLL!CML	
02435	776777	LAW 16777	/AC=776777
02436	740010	RAL	/AC=775777
02437	742010	RTL	/AC=767777
02440	742010	RTL	/AC=737777
02441	742010	RTL	/AC=577777
02442	742010	RTL	/L=0
02443	741401	SZLICMA	/AC=0
02444	740040	H253 HALT	/ERROR; AC BIT 8 NOT A 0, LAW 776777 FAILED
02445	740200	SZA	
02446	740040	H254 HALT	/ERROR; AC DROPPED A BIT, LAW 776777 FAILED
			/TEST LAW 777377, AC BIT 9
02447	754002	CLA!CLL!CML	/AC=0, L=1
02450	742020	RTR	/AC=200K
02451	742020	RTR	/AC=40K
02452	742020	RTR	/AC=10K
02453	742020	RTR	/AC=2K
02454	742020	RTR	/AC=400
02455	744002	CLL!CML	
02456	777377	LAW 17377	/AC=777377
02457	742010	RTL	/AC=775777
02460	742010	RTL	/AC=767777
02461	742010	RTL	/AC=737777
02462	742010	RTL	/AC=577777
02463	742010	RTL	/L=0
02464	741401	SZLICMA	/AC=0
02465	740040	H255 HALT	/ERROR; AC BIT 9 NOT A 0, LAW 777377 FAILED
02466	740200	SZA	
02467	740040	H256 HALT	/ERROR; AC DROPPED A BIT, LAW 777377 FAILED
			/TEST LAW 777577, AC BIT 10
02470	754002	CLA!CLL!CML	/AC=0, L=1
02471	740020	RAR	/AC=400K
02472	742020	RTR	/AC=100K
02473	742020	RTR	/AC=20K
02474	742020	RTR	/AC=4K
02475	742020	RTR	/AC=1K
02476	742020	RTR	/AC=200
02477	744002	CLL!CML	
02500	777577	LAW 17577	/AC=777577
02501	740010	RAL	/AC=777377
02502	742010	RTL	/AC=775777
02503	742010	RTL	/AC=767777
02504	742010	RTL	/AC=737777
02505	742010	RTL	/AC=577777
02506	742010	RTL	/L=0
02507	741401	SZLICMA	/AC=0
02510	740040	H257 HALT	/ERROR; AC BIT 10 NOT A 0, LAW 777577 FAILED
02511	740200	SZA	/ERROR; AC DROPPED A BIT, LAW 777577 FAILED
02512	740040	H258 HALT	
			/TEST LAW 777677, AC BIT 11
02513	754002	CLA!CLL!CML	/AC=0, L=1
02514	742020	RTR	/AC=200K
02515	742020	RTR	/AC=40K
02516	742020	RTR	/AC=10K
02517	742020	RTR	/AC=2K

02520	742020	RTR	/AC=400
02521	742020	RTR	/AC=100
02522	744002	CLL!CML	
02523	777677	LAW 17677	/AC=777677
02524	742010	RTL	/AC=777377
02525	742010	RTL	/AC=775777
02526	742010	RTL	/AC=767777
02527	742010	RTL	/AC=737777
02530	742010	RTL	/AC=577777
02531	742010	RTL	/L=0
02532	741401	SZL!CMA	/AC=0
02533	740040	H25Y HALT	/ERROR! AC BIT 11 NOT A 0, LAW 777677 FAILED
02534	740200	SZA	
02535	740040	H260 HALT	/ERROR! AC DROPPED A BIT, LAW 777677 FAILED
			/TEST LAW 777737, AC BIT 12
02536	754002	CLA!CLL!CML	/AC=0, L=1
02537	740020	RAR	/AC=400K
02540	742020	RTR	/AC=100K
02541	742020	RTR	/AC=20K
02542	742020	RTR	/AC=4K
02543	742020	RTR	/AC=1K
02544	742020	RTR	/AC=200
02545	742020	RTR	/AC=40
02546	744002	CLL!CML	
02547	777737	LAW 17737	/AC=777737
02550	740010	RAL	/AC=777677
02551	742010	RTL	/AC=777377
02552	742010	RTL	/AC=775777
02553	742010	RTL	/AC=767777
02554	742010	RTL	/AC=737777
02555	742010	RTL	/AC=577777
02556	742010	RTL	/L=0
02557	741401	SZL!CMA	/AC=0
02560	740040	H261 HALT	/ERROR! AC BIT 12 NOT A 0, LAW 777737 FAILED
02561	740200	SZA	
02562	740040	H262 HALT	/ERROR! AC DROPPED A BIT, LAW 777737 FAILED
			/TEST LAW 777757, AC BIT 13
02563	754002	CLA!CLL!CML	/AC=0, L=1
02564	742020	RTR	/AC=200K
02565	742020	RTR	/AC=40K
02566	742020	RTR	/AC=10K
02567	742020	RTR	/AC=2K
02570	742020	RTR	/AC=400
02571	742020	RTR	/AC=100
02572	742020	RTR	/AC=20
02573	744002	CLL!CML	
02574	777757	LAW 17757	/AC=777757
02575	742010	RTL	/AC=777677
02576	742010	RTL	/AC=777377
02577	742010	RTL	/AC=775777
02600	742010	RTL	/AC=767777
02601	742010	RTL	/AC=737777
02602	742010	RTL	/AC=577777
02603	742010	RTL	/L=0
02604	741401	SZL!CMA	/AC=0

02605	740040	H265	HALT	/ERROR; AC BIT 13 NOT A0, LAW 77757 FAILED
02606	740200		SZA	
02607	740040	H266	HALT	/ERROR; AC DROPPED A BIT, LAW 77757 FAILED
				/TEST LAW 77767, AC BIT 14
02610	754002		CLAICLLICML	/AC=0, L=1
02611	740020		RAR	/AC=400K
02612	742020		RTR	/AC=100K
02613	742020		RTR	/AC=20K
02614	742020		RTR	/AC=4K
02615	742020		RTR	/AC=1K
02616	742020		RTR	/AC=200
02617	742020		RTR	/AC=40
02620	742020		RTR	/AC=10
02621	744002		CLLICML	
02622	777767		LAW 17767	/AC=77767
02623	740010		RAL	/AC=77757
02624	742010		RTL	/AC=77767
02625	742010		RTL	/AC=777377
02626	742010		RTL	/AC=775777
02627	742010		RTL	/AC=767777
02630	742010		RTL	/AC=737777
02631	742010		RTL	/AC=577777
02632	742010		RTL	/L=0
02633	741401		SZLICMA	/AC=0
02634	740040	H265	HALT	/ERROR; AC BIT 14 NOT 10, LAW 77767 FAILED
02635	740200		SZA	
02636	740040	H266	HALT	/ERROR; AC DROPPED A BIT, LAW 77767 FAILED
				/TEST LAW 77773, AC BIT 15
02637	754002		CLAICLLICML	/AC=0, L=1
02640	742020		RTR	/AC=200K
02641	742020		RTR	/AC=40K
02642	742020		RTR	/AC=10K
02643	742020		RTR	/AC=2K
02644	742020		RTR	/AC=400
02645	742020		RTR	/AC=100
02646	742020		RTR	/AC=20
02647	742020		RTR	/AC=4
02650	744002		CLLICML	
02651	777773		LAW 17773	/AC=77773
02652	742010		RTL	/AC=77757
02653	742010		RTL	/AC=777677
02654	742010		RTL	/AC=777377
02655	742010		RTL	/AC=775777
02656	742010		RTL	/AC=767777
02657	742010		RTL	/AC=737777
02660	742010		RTL	/AC=577777
02661	742010		RTL	/L=0
02662	741401		SZLICMA	/AC=0
02663	740040	H267	HALT	/ERROR; AC BIT 15 NOT A 0, LAW 77773 FAILED
02664	740200		SZA	
02665	740040	H268	HALT	/ERROR; AC DROPPED A BIT, LAW 77773 FAILED
				/TEST LAW 77775, AC BIT 16
02666	754002		CLAICLLICML	/AC=0, L=1
02667	740020		RAR	/AC=400K
02670	742020		RTR	/AC=100K

02671	742020	RTR	/AC=20K
02672	742020	RTR	/AC=4K
02673	742020	RTR	/AC=1K
02674	742020	RTR	/AC=200
02675	742020	RTR	/AC=40
02676	742020	RTR	/AC=10
02677	742020	RTR	/AC=2
02700	744002	CLL!CML	
02701	777775	LAW 17775	/AC=777775
02702	740010	RAL	/AC=777773
02703	742010	RTL	/AC=777757
02704	742010	RTL	/AC=777677
02705	742010	RTL	/AC=777377
02706	742010	RTL	/AC=77577
02707	742010	RTL	/AC=767777
02710	742010	RTL	/AC=737777
02711	742010	RTL	/AC=577777
02712	742010	RTL	/L=0
02713	741401	SZL!CMA	/AC=0
02714	740040	H269 HALT	/ERROR; AC BIT 16 NOT A 0, LAW 777775 FAILED
02715	740200	SZA	
02716	740040	H270 HALT	/ERROR; AC DROPPED A BIT, LAW 777775 FAILED
			/TEST LAW 777776, AC BIT 17
02717	754002	CLA!CLL!CML	/AC=0, L=1
02720	742020	RTR	/AC=200K
02721	742020	RTR	/AC=40K
02722	742020	RTR	/AC=10K
02723	742020	RTR	/AC=2K
02724	742020	RTR	/AC=400
02725	742020	RTR	/AC=100
02726	742020	RTR	/AC=20
02727	742020	RTR	/AC=4
02730	742020	RTR	/AC=1
02731	744002	CLL!CML	
02732	777776	LAW 17776	/AC=777776
02733	742010	RTL	/AC=777773
02734	742010	RTL	/AC=777757
02735	742010	RTL	/AC=777677
02736	742010	RTL	/AC=777377
02737	742010	RTL	/AC=775777
02740	742010	RTL	/AC=767777
02741	742010	RTL	/AC=737777
02742	742010	RTL	/AC=577777
02743	742010	RTL	/L=0
02744	741401	SZL!CMA	/AC=0
02745	740040	H271 HALT	/ERROR; AC BIT 17 NOT A 0, LAW 777776 FAILED
02746	740200	SZA	
02747	740040	H272 HALT	/ERROR; AC DROPPED A BIT, LAW 777776 FAILED
			/PDP-9 INST TEST - PART 1 TAPE 4
			/TEST LAW = IAC
02750	754000	CLA!CLL	/AC=0 LINK=0
02751	777773	LAW 17773	
02752	740030	IAC	/AC=777774
02753	740030	IAC	/AC=777775
02754	740030	IAC	/AC=777776

02755	740030	IAC	/AC=777777
02756	740030	IAC	/AC=0
02757	740400	SNL	
02760	740040	H275 HALT	/ERROR: AC DID NOT OVERFLO
02761	740200	SZA	
02762	740040	H274 HALT	/ERROR: AC NOT ZERO
			/TEST LAW - IAC
02763	754000	CLA:CLL	/AC=0 LINK=0
02764	760400	LAW 400	/AC=760400
02765	742030	SWHA	
02766	740100	SMA	
02767	740040	H275 HALT	/ERROR: LAW OR SWHA FAILED
			/TEST LAW = IAC = SWHA
02770	754000	CLA:CLL	/AC=0 LINK=0
02771	760377	LAW 377	/AC=760377
02772	740030	IAC	/AC=760400
02773	742030	SWHA	
02774	740100	SMA	
02775	740040	H276 HALT	/ERROR: AC0 NOT SET
			/TEST LAC 0
02776	754002	CLA:CLL:ICML	/AC=0
02777	207303	LAC K0	/000000
03000	740200	SZA	
03001	740040	H277A HALT	/ERROR: AC NOT 0 LAC
			/FAILED
03002	740400	SNL	
03003	740040	H277B HALT	/ERROR: LINK NOT 1 AFTER
			/A LAC K0
			/TEST LAC K0, AC BIT 0
03004	754002	CLA:CLL:ICML	/AC=0, L=1
03005	740020	RAR	/AC=400K
03006	207303	LAC K0	/000000
03007	740200	SZA	
03010	740040	H277C HALT	/ERROR: AC BIT 0 NOT 0
			/AFTER LAC K0
			/TEST LAC K0, AC BIT 1
03011	754002	CLA:CLL:ICML	/AC=0, L=1
03012	742020	RTR	/AC=200K
03013	207303	LAC K0	/000000
03014	740200	SZA	
03015	740040	H277D HALT	/ERROR: AC BIT 1 NOT 0
			/AFTER LAC K0
			/TEST LAC K0, AC BIT 2
03016	754002	CLA:CLL:ICML	/AC=0, L=1
03017	742020	RTR	/AC=200K
03020	740020	RAR	/AC=100K
03021	207303	LAC K0	/000000
03022	740200	SZA	
03023	740040	H277E HALT	/ERROR: AC BIT 2 NOT 0
			/AFTER LAC K0
			/TEST LAC K0, AC BITS
03024	754002	CLA:CLL:ICML	/AC=0, L=1
03025	742020	RTR	/AC=200K
03026	742020	RTR	/AC=400K
03027	207303	LAC K0	/000000

03030	740200		SZA	
03031	740040	H278	HALT	/ERROR: AC BIT 3 NOT 0, LAC K0 FAILED
				/TEST LAC K0, AC BIT4
03032	754002		CLA:CLL:ICML	/AC=2, L=1
03033	742020		RTR	/AC=200K
03034	742020		RTR	/AC=40K
03035	740020		RAR	/AC=20K
03036	207303		LAC K0	/000000
03037	740200		SZA	
03040	740040	H279	HALT	/ERROR: AC BIT 4 NOT 0, LAC K0 FAILED
				/TEST LAC K0, AC BIT5
03041	754002		CLA:CLL:ICML	/AC=2, L=1
03042	742020		RTR	/AC=200K
03043	742020		RTR	/AC=40K
03044	742020		RTR	/AC=10K
03045	207303		LAC K0	/000000
03046	740200		SZA	
03047	740040	H280	HALT	/ERROR: AC BIT 5 NOT 0
				/LAC K0 FAILED
				/TEST LAC K0, AC BIT 6
03050	754002		CLA:CLL:ICML	/AC=2, L=1
03051	742020		RTR	/AC=200K
03052	742020		RTR	/AC=40K
03053	742020		RTR	/AC=10K
03054	740020		RAR	/AC=4K
03055	207303		LAC K0	/000000
03056	742020		SZA	
03057	740040	H281	HALT	/ERROR: AC BIT 6 NOT 0
				/LAC K0 FAILED
				/TEST LAC K0, AC BIT7
03060	754002		CLA:CLL:ICML	/AC=2, L=1
03061	742020		RTR	/AC=200K
03062	742020		RTR	/AC=40K
03063	742020		RTR	/AC=10K
03064	742020		RTR	/AC=2K
03065	207303		LAC K0	/000000
03066	742020		SZA	
03067	740040	H282	HALT	/ERROR: AC BIT 7 NOT 0, LAC K0 FAILED
				/TEST LAC K0, AC BIT 8
03070	754002		CLA:CLL:ICML	/AC=2, L=1
03071	742020		RTR	/AC=200K
03072	742020		RTR	/AC=40K
03073	742020		RTR	/AC=10K
03074	742020		RTR	/AC=2K
03075	740020		RAR	/AC=1K
03076	207303		LAC K0	/000000
03077	740200		SZA	
03100	740040	H283	HALT	/ERROR: AC BIT 8 NOT 0, AC K0 FAILED
				/TEST LAC K0, AC BIT9
03101	754002		CLA:CLL:ICML	/AC=2, L=1
03102	742020		RTR	/AC=200K
03103	742020		RTR	/AC=40K
03104	742020		RTR	/AC=10K
03105	742020		RTR	/AC=2K
03106	742020		RTR	/AC=400

03107	207303	LAC K0	/000000
03110	740200	SZA	
03111	740040	H284	HALT /ERROR; AC BIT 9 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT10
03112	754002	CLA:CLLICML	/AC=0, L=1
03113	742020	RTR	/AC=200K
03114	742020	RTR	/AC=40K
03115	742020	RTR	/AC=10K
03116	742020	RTR	/AC=2K
03117	742020	RTR	/AC=400
03120	740020	RAR	/AC=200
03121	207303	LAC K0	/000000
03122	740200	SZA	
03123	740040	H285	HALT /ERROR; AC BIT 10 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT11
03124	754002	CLA:CLLICML	/AC=0, L=1
03125	742020	RTR	/AC=200K
03126	742020	RTR	/AC=40K
03127	742020	RTR	/AC=10K
03130	742020	RTR	/AC=2K
03131	742020	RTR	/AC=400
03132	742020	RTR	/AC=100
03133	207303	LAC K0	/000000
03134	740200	SZA	
03135	740040	H286	HALT /ERROR; AC BIT 11 NOT 0 LAC K0 FAILED
			/TEST LAC K0, AC BIT 12
03136	754002	CLA:CLLICML	/AC=0, L=1
03137	742020	RTR	/AC=200K
03140	742020	RTR	/AC=40K
03141	742020	RTR	/AC=10K
03142	742020	RTR	/AC=2K
03143	742020	RTR	/AC=400
03144	742020	RTR	/AC=100
03145	740020	RAR	/AC=40
03146	207303	LAC K0	/000000
03147	740200	SZA	
03150	740040	H287	HALT /ERROR; AC BIT 12 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT13
03151	754002	CLA:CLLICML	/AC=0, L=1
03152	742020	RTR	/AC=200K
03153	742020	RTR	/AC=40K
03154	742020	RTR	/AC=10K
03155	742020	RTR	/AC=2K
03156	742020	RTR	/AC=400
03157	742020	RTR	/AC=100
03160	742020	RTR	/AC=20
03161	207303	LAC K0	/000000
03162	740200	SZA	
03163	740040	H288	HALT /ERROR; AC BIT 13 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT 14
03164	754002	CLA:CLLICML	/AC=0, L=1
03165	742020	RTR	/AC=200K
03166	742020	RTR	/AC=40K
03167	742020	RTR	/AC=10K
03170	742020	RTR	/AC=2K

03171	742020	RTR	/AC=400
03172	742020	RTR	/AC=100
03173	742020	RTR	/AC=20
03174	740020	RAR	/AC=10
03175	207303	LAC K0	/000000
03176	740200	SZA	
03177	740040	H289 HALT	/ERROR; AC BIT 14 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT 15
03200	754002	CLA!CLLICML	/AC=0, L=1
03201	742020	RTR	/AC=200K
03202	742020	RTR	/AC=40K
03203	742020	RTR	/AC=10K
03204	742020	RTR	/AC=2K
03205	742020	RTR	/AC=400
03206	742020	RTR	/AC=100
03207	742020	RTR	/AC=20
03210	742020	RTR	/AC=4
03211	207303	LAC K0	/000000
03212	740200	SZA	
03213	740040	H290 HALT	/ERROR; AC BIT 5 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT 16
03214	754002	CLA!CLLICML	/AC=0, L=1
03215	742020	RTR	/AC=200K
03216	742020	RTR	/AC=40K
03217	742020	RTR	/AC=10K
03220	742020	RTR	/AC=2K
03221	742020	RTR	/AC=400
03222	742020	RTR	/AC=100
03223	742020	RTR	/AC=20
03224	742020	RTR	/AC=4
03225	740020	RAR	/AC=2
03226	207303	LAC K0	/000000
03227	740200	SZA	
03230	740040	H291 HALT	/ERROR; AC BIT 16 NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC BIT 17
03231	754002	CLA!CLLICML	/AC=0, L=1
03232	742020	RTR	/AC=200K
03233	742020	RTR	/AC=40K
03234	742020	RTR	/AC=10K
03235	742020	RTR	/AC=2K
03236	742020	RTR	/AC=400
03237	742020	RTR	/AC=100
03240	742020	RTR	/AC=20
03241	742020	RTR	/AC=4
03242	742020	RTR	/AC=1
03243	207303	LAC K0	/000000
03244	740200	SZA	
03245	740040	H292 HALT	/ERROR; AC BIT 17 NOT 0, LAC K0 FAILED
			/TEST LAC K0 AC=ONES
03246	754001	CLA!CMAICLL	/AC=ONES, L=0
03247	207303	LAC K0	/000000
03250	740200	SZA	
03251	740040	H293 HALT	/ERROR; AC NOT 0, LAC K0 FAILED
			/TEST LAC K0, AC=ONES, L=1
03252	754003	CLA!CMAICLLICML	/AC=ONES, L=1

03253	207303	LAC K0	/000000
03254	740400	SNL	
03255	740040	H294	HALT /ERROR; LINK NOT 1, LAC K0 FAILED
			/TEST LAC K0, AC=ONES, L=0
03256	754001	CLA:CLL	/AC=ONES, L=0
03257	207303	LAC K0	
03260	741400	SZL	
03261	740040	H295	HALT /ERROR; LINK NOT 1, LAC K0 FAILED
			/TEST LAC K400K, AC BIT0
03262	754000	CLA:CLL	/AC=0, L=0
03263	207304	LAC K400K	/400K
03264	740010	RAL	/L=1
03265	740400	SNL	
03266	740040	H296	HALT /ERROR; AC BIT0 NOT 1, LAC K400K FAILED
03267	740200	SZA	
03270	740040	H297	HALT /ERROR; AC PICKED A BIT, LAC K400K FAILED
			/TEST LAC K200K; AC BIT1
03271	754000	CLA:CLL	/AC=0, L=0
03272	207305	LAC K200K	/200K
03273	742010	RTL	/L=1
03274	740400	SNL	
03275	740040	H298	HALT /ERROR; AC BIT 1 NOT 1, LAC K200K FAILED
03276	740200	SZA	
03277	740040	H299	HALT /ERROR; AC PICKED A BIT, LAC K200K, FAILED
			/TEST LAC K100K, AC BIT 2
03300	754000	CLA:CLL	/AC=0, L=0
03301	207306	LAC K100K	/100K
03302	742010	RTL	/AC=400K
03303	740010	RAL	/L=1
03304	740400	SNL	
03305	740040	H300	HALT /ERROR; AC BIT 2 NOT 1, LAC K100K, FAILED
03306	740200	SZA	
03307	740040	H301	HALT /ERROR; AC PICKED A BIT, LAC K100K FAILED
			/TEST LAC K40K, AC BIT3
03310	754000	CLA:CLL	/AC=0, L=0
03311	207307	LAC K40K	/40K
03312	742010	RTL	/AC=200K
03313	742010	RTL	/L=1
03314	740200	SZA	
03315	740040	H302	HALT /ERROR; AC PICKED A BIT, LAC K40K FAILED
03316	740400	SNL	
03317	740040	H303	HALT /ERROR; AC BIT 3 NOT 1, LAC K40K FAILED
			/TEST LAC K20K, AC BIT4
03320	754000	CLA:CLL	/AC=0, L=0
03321	207310	LAC K20K	/20K
03322	742010	RTL	/AC=100K
03323	742010	RTL	/AC=400K
03324	740010	RAL	/L=1
03325	740400	SNL	
03326	740040	H304	HALT /ERROR; AC BIT 4 NOT 1, LAC K20K FAILED
03327	740200	SZA	
03330	740040	H305	HALT /ERROR; AC PICKED A BIT, LAC K20K
			/TEST LAC K10K, AC BIT5
03331	754000	CLA:CLL	/AC=0, L=0
03332	207311	LAC K10K	/10K

03333	742010	RTL	/AC=40K
03334	742010	RTL	/AC=200K
03335	742010	RTL	/L=1
03336	740400	SNL	
03337	740040	H306	HALT /ERROR: AC BIT 5 NOT 1, LAC K10K FAILED
03340	740200	SZA	
03341	740040	H307	HALT /ERROR: AC PICKED A BIT, LAC K10K FAILED
			/TEST LAC K4K, AC BIT6
03342	754000	CLA!CLL	/AC=0, L=0
03343	207312	LAC K4K	/4K
03344	742010	RTL	/AC=20K
03345	742010	RTL	/AC=100K
03346	742010	RTL	/AC=400K
03347	740010	RAL	/L=1
03350	740400	SNL	
03351	740040	H308	HALT /ERROR: AC BIT 6 NOT 1, LAC K4K FAILED
03352	740200	SZA	
03353	740040	H309	HALT /ERROR: AC PICKED A BIT, LAC K4K FAILED
			/TEST LAC K2K, AC BIT7
03354	754000	CLA!CLL	/AC=0, L=0
03355	207313	LAC K2K	/2K
03356	742010	RTL	/AC=10K
03357	742010	RTL	/AC=40K
03360	742010	RTL	/AC=200K
03361	742010	RTL	/L=1
03362	740400	SNL	
03363	740040	H310	HALT /ERROR: AC BIT 7 NOT 1 LAC K2K FAILED
03364	740200	SZA	
03365	740040	H311	HALT /ERROR: AC PICKED A BIT, LAC K2K FAILED
			/TEST LAC K1K, AC BIT8
03366	754000	CLA!CLL	/AC=0, L=0
03367	207314	LAC K1K	/1K
03370	742010	RTL	/AC=4K
03371	742010	RTL	/AC=20K
03372	742010	RTL	/AC=100K
03373	742010	RTL	/AC=400K
03374	740010	RAL	/L=1
03375	740400	SNL	
03376	740040	H312	HALT /ERROR: AC PICKED A BIT, LAC K1K
03377	740200	SZA	
03400	740040	H313	HALT /ERROR: AC PICKED A BIT, LAC K1K
			/FAILED
			/TEST LAC K400, AC BIT 9
03401	754000	CLA!CLL	/AC=0, L=0
03402	207315	LAC K400	/400
03403	742010	RTL	/AC=2K
03404	742010	RTL	/AC=10K
03405	742010	RTL	/AC=40K
03406	742010	RTL	/AC=200K
03407	742010	RTL	/L=1
03410	740400	SNL	
03411	740040	H314	HALT /ERROR: AC BIT 9 NOT 1, LAC K400 FAILED
03412	740200	SZA	
03413	740040	H315	HALT /ERROR: AC PICKED A BIT, LAC K400
			/FAILED

03476	754000		CLA!CLL	/AC=0, L=0
03477	207322		LAC K10	/10
03500	742010		RTL	/AC=40
03501	742010		RTL	/AC=200
03502	742010		RTL	/AC=1K
03503	742010		RTL	/AC=4K
03504	742010		RTL	/AC=20K
03505	742010		RTL	/AC=100K
03506	742010		RTL	/AC=400K
03507	740010		RAL	/L=1
03510	740400		SNL	
03511	740040	H324	HALT	/ERROR; AC BIT 14 NOT 1, LAC K10 FAILED
03512	740200		SZA	
03513	740040	H325	HALT	/ERROR; AC PICKED A BIT, LAC K10 FAILED
			/TEST LAC K4, AC BIT 15	
03514	754000		CLA!CLL	/AC=0, L=0
03515	207323		LAC K4	/4
03516	742010		RTL	/AC=20
03517	742010		RTL	/AC=100
03520	742010		RTL	/AC=400
03521	742010		RTL	/AC=2K
03522	742010		RTL	/AC=10K
03523	742010		RTL	/AC=40K
03524	742010		RTL	/AC=200K
03525	742010		RTL	/L=1
03526	740400		SNL	
03527	740040	H326	HALT	/ERROR; AC BIT 15 NOT 1, LAC K4 FAILED
03530	740200		SZA	
03531	740040	H327	HALT	/ERROR; AC PICKED A BIT, LAC K4 FAILED
			/TEST LAC K2, AC BIT 16	
03532	754000		CLA!CLL	/AC=0, L=0
03533	207324		LAC K2	/2
03534	742010		RTL	/AC=10
03535	742010		RTL	/AC=40
03536	742010		RTL	/AC=200
03537	742010		RTL	/AC=1K
03540	742010		RTL	/AC=4K
03541	742010		RTL	/AC=20K
03542	742010		RTL	/AC=100K
03543	742010		RTL	/AC=400K
03544	740010		RAL	/L=1
03545	740400		SNL	
03546	740040	H328	HALT	/ERROR; AC BIT 16 NOT 1, LAC K2 FAILED
03547	740200		SZA	
03550	740040	H329	HALT	/ERROR; AC PICKED A BIT, LAC K2 FAILED
			/TEST LAC K1, AC BIT 17	
03551	754000		CLA!CLL	/AC=0, L=0
03552	207325		LAC K1	/1
03553	742010		RTL	/4
03554	742010		RTL	/20
03555	742010		RTL	/100
03556	742010		RTL	/400
03557	742010		RTL	/2K
03560	742010		RTL	/10K
03561	742010		RTL	/40K

03562	742010		RTL	/200K
03563	742010		RTL	/L=1
03564	740400		SNL	
03565	740040	H330	HALT	/ERROR; AC BIT 17 NOT 1, LAC F1 FAILED
03566	740200		SZA	
03567	740040	H331	HALT	/ERROR; AC PICKED A BIT, LAC K1 FAILED
				/TEST LAC M400K, AC BIT 0
03570	754002		CLA:CLL:ICML	/L=1
03571	740020		RAR	/AC=400K
03572	744002		CLL:ICML	
03573	207335		LAC M400K	/AC=377777
03574	740010		RAL	/L=0
03575	741401		SZL:ICMA	
03576	740040	H332	HALT	/ERROR; AC BIT 0 NOT 0, LAC M400K FAILED
03577	740200		SZA	
03600	740040	H335	HALT	/ERROR; AC DROPPED A BIT, LAC M400 K1
				/FAILED
				/TEST LAC M200K AC BIT 1
03601	754002		CLL:ICML:ICLA	/L=1
03602	742020		RTR	/AC=200K
03603	744002		CLL:ICML	
03604	207336		LAC M200K	/AC=577777
03605	742010		RTL	/L=0
03606	741401		SZL:ICMA	
03607	740040	H334	HALT	/ERROR; AC BIT 1 NOT 0 LAC M200K FAILED
03610	740200		SZA	
03611	740040	H335	HALT	/ERROR; AC DROPPED A BIT, LAC M200K FAILED
				/TEST LAC M100K, AC BIT 2
03612	754002		CLL:ICML:ICLA	/L=1
03613	742020		RTR	/AC=200K
03614	740020		RAR	/AC=100K
03615	744002		CLL:ICML	
03616	207337		LAC M100K	/AC=677777
03617	742010		RTL	/AC=377777
03620	740010		RAL	/L=0
03621	741401		SZL:ICMA	
03622	740040	H336	HALT	/ERROR; AC BIT 2 NOT 0, LAC M100K FAILED
03623	740200		SZA	
03624	740040	H337	HALT	/ERROR; AC DROPPED A BIT, LAC M100K
				/FAILED
				/TEST LAC M40K, AC BIT 3
03625	754002		CLL:ICML:ICLA	/L=1
03626	742020		RTR	/AC=200K
03627	742020		RTR	/AC=40K
03630	744002		CLL:ICML	
03631	207340		LAC M40K	/AC=737777
03632	742010		RTL	/AC=577777
03633	742010		RTL	/L=0
03634	741401		SZL:ICMA	
03635	740040	H338	HALT	/ERROR; AC BIT 3 NOT 0, LAC M40K FAILED
03636	740200		SZA	
03637	740040	H339	HALT	/ERROR; AC DROPPED A BIT LAC M40K
				/TEST LAC M20K, AC BIT 4
03640	754002		CLL:ICML:ICLA	/L=1
03641	742020		RTR	/AC=200K

03642	742020	RTR	/AC=40K
03643	740020	RAR	/AC=20K
03644	744002	CLL!CML	
03645	207341	LAC M20K	/AC=757777
03646	742010	RTL	/AC=677777
03647	742010	RTL	/AC=377777
03650	740010	RAL	/L=0
03651	741401	SZL!CMA	
03652	740040	H340 HALT	/ERROR! AC BIT 4 NOT 0, LAC M20K FAILED
03653	740200	SZA	
03654	740040	H341 HALT	/ERROR! AC DROPPED A BIT, /LAC M20K FAILED /TEST LAC M10K, AC BIT 5
03655	754002	CLL!CML!CLA	/L=1
03656	742020	RTR	/AC=200K
03657	742020	RTR	/AC=40K
03660	742020	RTR	/AC=10K
03661	744002	CLL!CML	
03662	207342	LAC M10K	/AC=767777
03663	742010	RTL	/AC=737777
03664	742010	RTL	/AC=577777
03665	742010	RTL	/L=0
03666	741401	SZL!CMA	
03667	740040	H342 HALT	/ERROR! AC BIT 5 NOT 0, LAC M10K FAILED
03670	740200	SZA	
03671	740040	H343 HALT	/ERROR! AC DROPPED A BIT LAC M10K FAILED /TEST LAC M4K, AC BIT 6
03672	754002	CLL!CML!CLA	/L=1
03673	742020	RTR	/AC=200K
03674	742020	RTR	/AC=40K
03675	742020	RTR	/AC=10K
03676	740020	RAR	/AC=4K
03677	744002	CLL!CML	
03700	207343	LAC M4K	/AC=773777
03701	742010	RTL	/AC=757777
03702	742010	RTL	/AC=677777
03703	742010	RTL	/AC=377777
03704	742010	RAL	/L=0
03705	741401	SZL!CMA	
03706	740040	H344 HALT	/ERROR! AC BIT 6 NOT 0, LAC M4K FAILED
03707	740200	SZA	
03710	740040	H345 HALT	/ERROR! AC DROPPED A BIT, LAC M4K FAILED /TEST LAC M2K, AC BIT 7
03711	754002	CLL!CML!CLA	/L=1
03712	742020	RTR	/AC=200K
03713	742020	RTR	/AC=40K
03714	742020	RTR	/AC=10K
03715	742020	RTR	/AC=2K
03716	744002	CLL!CML	
03717	207344	LAC M2K	/AC=775777
03720	742010	RTL	/AC=767777
03721	742010	RTL	/AC=737777
03722	742010	RTL	/AC=577777
03723	742010	RTL	/L=0
03724	741401	SZL!CMA	

03725	740040	H346	HALT	/ERROR; AC BIT 7 NOT 0, LAC M2K FAILED
03726	740200		SZA	
03727	740040	H347	HALT	/ERROR; AC DROPPED A BIT, LAC M2K FAILED
				/TEST LAC M1K, AC BIT 8
03730	754002		CLL!CMLICLA	/L=1
03731	742020		RTR	/AC=200K
03732	742020		RTR	/AC=40K
03733	742020		RTR	/AC=10K
03734	742020		RTR	/AC=2K
03735	740020		RAR	/AC=1K
03736	744002		CLL!CML	
03737	207345		LAC M1K	/AC=776777
03740	742010		RTL	/AC=773777
03741	742010		RTL	/AC=757777
03742	742010		RTL	/AC=677777
03743	742010		RTL	/AC=377777
03744	740010		RAL	/L=0
03745	741401		SZL!CMA	
03746	740040	H348	HALT	/ERROR; AC BIT 8 NOT 0 LAC M1K FAILED
03747	740200		SZA	
03750	740040	H349	HALT	/ERROR; AC DROPPED A BIT, LAC M1K FAILED
				/TEST LAC M400, AC BIT 9
03751	754002		CLL!CMLICLA	/L=1
03752	742020		RTR	/AC=200K
03753	742020		RTR	/AC=40K
03754	742020		RTR	/AC=10K
03755	742020		RTR	/AC=2K
03756	742020		RTR	/AC=400
03757	744002		CLL!CML	
03760	207346		LAC M400	/AC=777377
03761	742010		RTL	/AC=775777
03762	742010		RTL	/AC=767777
03763	742010		RTL	/AC=737777
03764	742010		RTL	/AC=577777
03765	742010		RTL	/L=0
03766	741401		SZL!CMA	
03767	740040	H350	HALT	/ERROR; AC BIT 9 NOT 0, LAC M400 FAILED
03770	740200		SZA	
03771	740040	H351	HALT	/ERROR; AC DROPPED A BIT, LAC M400 FAILED
				/TEST LAC M200, AC BIT 10
03772	754002		CLL!CMLICLA	/L=1
03773	742020		RTR	/AC=200K
03774	742020		RTR	/AC=40K
03775	742020		RTR	/AC=10K
03776	742020		RTR	/AC=2K
03777	742020		RTR	/AC=400
04000	740020		RAR	/AC=200
04001	744002		CLL!CML	
04002	207347		LAC M200	/AC=777577
04003	742010		RTL	/AC=776777
04004	742010		RTL	/AC=773777
04005	742010		RTL	/AC=757777
04006	742010		RTL	/AC=677777
04007	742010		RTL	/AC=377777
04010	740010		RAL	/L=0

04011	741401		SZLICMA	
04012	740040	H352	HALT	/ERROR; AC BIT 10 NOT 0, LAC M200 FAILED
04013	740200		SZA	
04014	740040	H353	HALT	/ERROR; AC DROPPED A BIT, LAC M200 FAILED
				/TEST LAC M100, AC BIT 11
04015	754002		CLL!CML!CLA	/L=1
04016	742020		RTR	/AC=200K
04017	742020		RTR	/AC=40K
04020	742020		RTR	/AC=10K
04021	742020		RTR	/AC=2K
04022	742020		RTR	/AC=400
04023	742020		RTR	/AC=100
04024	744002		CLL!CML	
04025	207350		LAC M100	/AC=777677
04026	742010		RTL	/AC=777377
04027	742010		RTL	/AC=775777
04030	742010		RTL	/AC=767777
04031	742010		RTL	/AC=737777
04032	742010		RTL	/AC=577777
04033	742010		RTL	/L=0
04034	741401		SZLICMA	
04035	740040	H354	HALT	/ERROR; AC BIT 11 NOT 0, LAC M100 FAILED
04036	740200		SZA	
04037	740040	H355	HALT	/ERROR; AC DROPPED A BIT, LAC M100 FAILED
				/TEST LAC M40, AC BIT 12
04040	754002		CLL!CML!CLA	/L=1
04041	742020		RTR	/AC=200K
04042	742020		RTR	/AC=40K
04043	742020		RTR	/AC=10K
04044	742020		RTR	/AC=2K
04045	742020		RTR	/AC=400
04046	742020		RTR	/AC=100
04047	740020		BAR	/AC=40
04050	744002		CLL!CML	
04051	207351		LAC M40	/AC=777737
04052	742010		RTL	/AC=777577
04053	742010		RTL	/AC=776777
04054	742010		RTL	/AC=773777
04055	742010		RTL	/AC=757777
04056	742010		RTL	/AC=677777
04057	742010		RTL	/AC=377777
04060	740010		RAL	/L=0
04061	741401		SZLICMA	
04062	740040	H356	HALT	/ERROR; AC BIT 12 NOT 0, LAC M40 FAILED
04063	740200		SZA	
04064	740040	H357	HALT	/ERROR; AC DROPPED A BIT, LAC M40 FAILED
				/TEST LAC M20, AC BIT 13
04065	754002		CLA!CLL!CML	/L=1
04066	742020		RTR	/AC=200K
04067	742020		RTR	/AC=40K
04070	742020		RTR	/AC=10K
04071	742020		RTR	/AC=2K
04072	742020		RTR	/AC=400
04073	742020		RTR	/AC=100
04074	742020		RTR	/AC=20

04075	744002	CLL!CML	
04076	207352	LAC M20	/AC=777757
04077	742010	RTL	/AC=777677
04100	742010	RTL	/AC=777377
04101	742010	RTL	/AC=775777
04102	742010	RTL	/AC=767777
04103	742010	RTL	/AC=737777
04104	742010	RTL	/AC=577777
04105	742010	RTL	/L=0
04106	741401	SZL!CMA	
04107	740040	H358 HALT	/ERROR; AC BIT 13 NOT 0, LAC M20 FAILED
04110	740200	SZA	
04111	740040	H359 HALT	/ERROR; AC DROPPED A BIT, LAC M20 FAILED
			/TEST LAC M10, AC BIT 14
04112	754002	CLA!CLL!CML	/L=1
04113	742020	RTR	/AC=200K
04114	742020	RTR	/AC=40K
04115	742020	RTR	/AC=10K
04116	742020	RTR	/AC=2K
04117	742020	RTR	/AC=400
04120	742020	RTR	/AC=100
04121	742020	RTR	/AC=20
04122	740020	RAR	/AC=10
04123	744002	CLL!CML	
04124	207353	LAC M10	/AC=777767
04125	742010	RTL	/AC=777737
04126	742010	RTL	/AC=777577
04127	742010	RTL	/AC=776777
04130	742010	RTL	/AC=773777
04131	742010	RTL	/AC=757777
04132	742010	RTL	/AC=677777
04133	742010	RTL	/AC=377777
04134	740010	RAL	/L=0
04135	741401	SZL!CMA	
04136	740040	H360 HALT	/ERROR, AC BIT 14 NOT 0, LAC M100 FAILED
04137	740200	SZA	
04140	740040	H361 HALT	/ERROR; AC DROPPED A BIT, LAC M10 FAILED
			/TEST LAC M4, AC BIT 15
04141	754002	CLA!CLL!CML	/L=1
04142	742020	RTR	/AC=200K
04143	742020	RTR	/AC=40K
04144	742020	RTR	/AC=10K
04145	742020	RTR	/AC=2K
04146	742020	RTR	/AC=400
04147	742020	RTR	/AC=100
04150	742020	RTR	/AC=20
04151	742020	RTR	/AC=4
04152	744002	CLL!CML	
04153	207354	LAC M4	/AC=777773
04154	742010	RTL	/AC=777757
04155	742010	RTL	/AC=777677
04156	742010	RTL	/AC=777377
04157	742010	RTL	/AC=775777
04160	742010	RTL	/AC=767777
04161	742010	RTL	/AC=737777

04162	742010	RTL	/AC=577777
04163	742010	RTL	/L=0
04164	741401	SZL:OMA	
04165	740040	H362	HALT
04166	740200	SZA	/ERROR: AC BIT 15 NOT 0, LAC M4 FAILED
04167	740040	H363	HALT
			/ERROR: AC DROPPED A BIT, LAC M4 FAILED
			/TEST LAC M2, AC BIT 16
04170	754002	CLA:CLLICML	/L=1
04171	742020	RTR	/AC=200K
04172	742020	RTR	/AC=40K
04173	742020	RTR	/AC=10K
04174	742020	RTR	/AC=2K
04175	742020	RTR	/AC=400
04176	742020	RTR	/AC=100
04177	742020	RTR	/AC=20
04200	742020	RTR	/AC=4
04201	740020	PAR	/AC=2
04202	744002	CLL:CML	
04203	207355	LAC M2	/AC=777775
04204	742010	RTL	/AC=777767
04205	742010	RTL	/AC=777737
04206	742010	RTL	/AC=777577
04207	742010	RTL	/AC=776777
04210	742010	RTL	/AC=773777
04211	742010	RTL	/AC=757777
04212	742010	RTL	/AC=677777
04213	742010	RTL	/AC=377777
04214	742010	PAL	/L=2
04215	741401	SZL:OMA	
04216	740040	H364	HALT
04217	740200	SZA	/ERROR: AC BIT 16 NOT 0, LAC M2 FAILED
04220	740040	H365	HALT
			/ERROR: AC DROPPED A BIT, LAC M2 FAILED
			/TEST LAC M1, AC BIT 17
04221	754002	CLA:CLLICML	/L=1
04222	742020	RTR	/AC=200K
04223	742020	RTR	/AC=40K
04224	742020	RTR	/AC=10K
04225	742020	RTR	/AC=2K
04226	742020	RTR	/AC=400
04227	742020	RTR	/AC=100
04230	742020	RTR	/AC=20
04231	742020	RTR	/AC=4
04232	742020	RTR	/AC=1
04233	744002	CLL:CML	
04234	207355	LAC M1	/AC=777776
04235	742010	RTL	/AC=777773
04236	742010	RTL	/AC=777757
04237	742010	RTL	/AC=777677
04240	742010	RTL	/AC=777377
04241	742010	RTL	/AC=775777
04242	742010	RTL	/AC=767777
04243	742010	RTL	/AC=737777
04244	742010	RTL	/AC=577777
04245	742010	RTL	/L=0
04246	741401	SZL:OMA	

04247	740040	H366	HALT	/ERROR; AC BIT 17 NOT 0, LAC M1 FAILED
04250	740200		SZA	
04251	740040	H367	HALT	/ERROR, AC DROPPED A BIT, LAC M1 FAILED
			/INSTRUCTION TEST TAPE 5	
				/TEST AND K0, AC=0
04252	750000		CLA	/AC=0
04253	507303		AND K0	/0
04254	740200		SZA	
04255	740040	H368A	HALT	/ERROR, AC NOT 0 AND K0 FAILED
				/TEST AND K0, AC=400K
04256	207304		LAC K400K	/AC=400K
04257	507303		AND K0	/0
04260	740200		SZA	
04261	740040	H368B	HALT	/ERROR, AC BIT 0 NOT 0, AND K0 FAILED
				/TEST AND K0, AC=200K
04262	207305		LAC K200K	/AC=200K
04263	507303		AND K0	/0
04264	740200		SZA	
04265	740040	H368C	HALT	/ERROR, AC BIT 1 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=100K
04266	207306		LAC K100K	/AC=100K
04267	507303		AND K0	/0
04270	740200		SZA	
04271	740040	H368D	HALT	/ERROR, AC BIT 2 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=40K
04272	207307		LAC K40K	/AC=40K
04273	507303		AND K0	/0
04274	740200		SZA	
04275	740040	H368E	HALT	/ERROR, AC BIT 3 NOT A 0, AND K 0 FAILED
				/TEST AND K0, AC=20K
04276	207310		LAC K20K	/AC=20K
04277	507303		AND K0	/0
04300	740200		SZA	
04301	740040	H368F	HALT	/ERROR, AC BIT 4 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=10K
04302	207311		LAC K10K	/AC=10K
04303	507303		AND K0	/0
04304	740200		SZA	
04305	740040	H368G	HALT	/ERROR, AC BIT 5 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=4K
04306	207312		LAC K4K	/AC=4K
04307	507303		AND K0	/0
04310	740200		SZA	
04311	740040	H368H	HALT	/ERROR, AC BIT 6 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=2K
04312	207313		LAC K2K	/AC=2K
04313	507303		AND K0	/0
04314	740200		SZA	
04315	740040	H368I	HALT	/ERROR, AC BIT 7 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=1K
04316	207314		LAC K1K	/AC=1K
04317	507303		AND K0	/0
04320	740200		SZA	
04321	740040	H368J	HALT	/ERROR, AC BIT 8 NOT A 0, AND K0 FAILED
				/TEST AND K0, AC=400

04322	207315	LAC K400	/AC=400
04323	507303	AND K0	/0
04324	740200	SZA	
04325	740040	H369 HALT	/ERROR, AC BIT 9 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=200
04326	207316	LAC K200	/AC=200
04327	507303	AND K0	/0
04330	740200	SZA	
04331	740040	H370 HALT	/ERROR, AC BIT 10 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=100
04332	207317	LAC K100	/AC=100
04333	507303	AND K0	/0
04334	740200	SZA	
04335	740040	H371 HALT	/ERROR, AC BIT 11 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=40
04336	207320	LAC K40	/AC=40
04337	507303	AND K0	/0
04340	740200	SZA	
04341	740040	H372 HALT	/ERROR, AC BIT 12 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=20
04342	207321	LAC K20	/AC=20
04343	507303	AND K0	/0
04344	740200	SZA	
04345	740040	H373 HALT	/ERROR, AC BIT 13 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=10
04346	207322	LAC K10	/AC=10
04347	507303	AND K0	/0
04350	740200	SZA	
04351	740040	H374 HALT	/ERROR, AC BIT 14 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=4
04352	207323	LAC K4	/AC=4
04353	507303	AND K0	/0
04354	740200	SZA	
04355	740040	H375 HALT	/ERROR, AC BIT 15 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=2
04356	207324	LAC K2	/AC=2
04357	507303	AND K0	/0
04360	740200	SZA	
04361	740040	H376 HALT	/ERROR, AC BIT 16 NOT A 0, AND K0 FAILED
			/TEST AND K0, AC=1
04362	207325	LAC K1	/AC=1
04363	507303	AND K0	/0
04364	740200	SZA	
04365	740040	H377 HALT	/ERROR, AC BIT 17 NOT A 0, AND K0 FAILED
			/TEST AND K400K, AC=0
04366	207303	LAC K0	/AC=0
04367	507304	AND K400K	/400K
04370	740200	SZA	
04371	740040	H378 HALT	/ERROR, AC NOT 0 AND K400K FAILED
			/TEST AND K200K, AC=0
04372	207303	LAC K0	/AC=0
04373	507305	AND K200K	/200K
04374	740200	SZA	
04375	740040	H379 HALT	/ERROR, AC NOT 0, AND 200K FAILED
			/TEST AND K100K, AC=0

04376	207303	LAC K0	/AC=0
04377	507306	AND K100K	/100K
04400	740200	SZA	
04401	740040	H380 HALT	/ERROR, AC NOT 0, AND K100K FAILED
			/TEST AND K40K, AC=0
04402	207303	LAC K0	/AC=0
04403	507307	AND K40K	/40K
04404	740200	SZA	
04405	740040	H381 HALT	/ERROR, AC NOT 10, AND K40K FAILED
			/TEST AND K20K, AC=0
04406	207303	LAC K0	/AC=0
04407	507310	AND K20K	/20K
04410	740200	SZA	
04411	740040	H382 HALT	/ERROR, AC NOT 0, AND K20, FAILED
			/TEST AND K10K, AC=0
04412	207303	LAC K0	/AC=0
04413	507311	AND K10K	/10K
04414	740200	SZA	
04415	740040	H383 HALT	/ERROR, AC NOT 0, AND K10K FAILED
			/TEST AND K4K, AC=0
04416	207303	LAC K0	/AC=0
04417	507312	AND K4K	/4K
04420	740200	SZA	
04421	740040	H384 HALT	/ERROR, AC NOT 0 AND K4K FAILED
			/TEST AND K2K, AC=0
04422	207303	LAC K0	/AC=0
04423	507313	AND K2K	/2K
04424	740200	SZA	
04425	740040	H385 HALT	/ERROR, AC NOT 0 AND K2K FAILED
			/TEST AND K1K, AC=0
04426	207303	LAC K0	/AC=0
04427	507314	AND K1K	/1K
04430	740200	SZA	
04431	740040	H386 HALT	/ERROR, AC NOT 0 AND K1K FAILED
			/TEST AND K400, AC=0
04432	207303	LAC K0	/AC=0
04433	507315	AND K400	/400
04434	740200	SZA	
04435	740040	H387 HALT	/ERROR, AC NOT 0 AND 400 FAILED
			/TEST AND K200, AC=0
04436	207303	LAC K0	/AC=0
04437	507316	AND K200	/200
04440	740200	SZA	
04441	740040	H388 HALT	/ERROR, AC NOT 0, AND K200 FAILED
			/TEST AND K100, AC=0
04442	207303	LAC K0	/AC=0
04443	507317	AND K100	/100
04444	740200	SZA	
04445	740040	H389 HALT	/ERROR, AC NOT 0, AND K100 FAILED
			/TEST AND K40, AC=0
04446	207303	LAC K0	/AC=0
04447	507320	AND K40	/40
04450	740200	SZA	
04451	740040	H390 HALT	/ERROR, AC NOT 0, AND K40 FAILED
			/TEST AND K20, AC=0

04452	207303	LAC K0	/AC=0
04453	507321	AND K20	/20
04454	740200	SZA	
04455	740040	H391	HALT /ERROR, AC NOT 0, AND K20 FAILED
			/TEST AND K10, AC=0
04456	207303	LAC K0	/AC=0
04457	507322	AND K10	/10
04460	740200	SZA	
04461	740040	H392	HALT /ERROR, AC NOT 0, AND K10 FAILED
			/TEST AND K4, AC=0
04462	207303	LAC K0	/AC=0
04463	507323	AND K4	/4
04464	740200	SZA	
04465	740040	H393	HALT /ERROR, AC NOT 0, AND K4 FAILED
			/TEST AND K2, AC=0
04466	207303	LAC K0	/AC=0
04467	507324	AND K2	/2
04470	740200	SZA	
04471	740040	H394	HALT /ERROR, AC NOT 0, AND K2 FAILED
			/TEST AND K1, AC=0
04472	207303	LAC K0	/AC=0
04473	507325	AND K1	/1
04474	740200	SZA	
04475	740040	H395	HALT /ERROR, AC NOT 0, AND K1 FAILED
			/TEST AND K400K, AC=M400K
04476	207335	LAC M400K	/AC=377777
04477	507304	AND K400K	/400K
04500	740200	SZA	
04501	740040	H396	HALT /ERROR, AC NOT 0, AND K400K FAILED
			/TEST AND K200K, AC=M200K
04502	227336	LAC M200K	/AC=577777
04503	507305	AND K200K	/200K
04504	740200	SZA	
04505	740040	H397	HALT /ERROR, AC NOT 0, AND K200K FAILED
			/TEST AND K100K, AC=M100K
04506	207337	LAC M100K	/AC=677777
04507	507306	AND K100K	/100K
04510	740200	SZA	
04511	740040	H398	HALT /ERROR, AC NOT 0, AND K100K FAILED
			/TEST AND K40K, AC=M40K
04512	227340	LAC M40K	/AC=737777
04513	507307	AND K40K	/40K
04514	740200	SZA	
04515	740040	H399	HALT /ERROR, AC NOT 0, AND K40, FAILED
			/TEST AND K20K, AC=M20K
04516	207341	LAC M20K	/AC=757777
04517	507310	AND K20K	/20K
04520	740200	SZA	
04521	740040	H400	HALT /ERROR, AC NOT 0, AND K20K FAILED
			/TEST AND K10K, AC=M10K
04522	767777	LAW 7777	/AC=767777
04523	507311	AND K10K	/10K
04524	740200	SZA	
04525	740040	H401	HALT /ERROR, AC NOT 0, AND K10K FAILED
			/TEST AND K4K, AC=M4K

04526	773777	LAW 13777	/AC=773777
04527	507312	AND K4K	/4K
04530	740200	SZA	
04531	740040	H402 HALT	/ERROR; AC NOT 0, AND K4K FAILED
			/TEST AND K2K, AC=M2K
04532	775777	LAW 15777	/AC=775777
04533	507313	AND K2K	/2K
04534	740200	SZA	
04535	740040	H403 HALT	/ERROR; AC NOT 0, AND K2K FAILED
			/TEST AND K1K, AC=M1K
04536	776777	LAW 16777	/AC=776777
04537	507314	AND K1K	/1K
04540	740200	SZA	
04541	740040	H404 HALT	/ERROR; AC NOT 0, AND K1K FAILED
			/TEST AND K400, AC=M400
04542	777377	LAW 17377	/AC=777377
04543	507315	AND K400	/400
04544	740200	SZA	
04545	740040	H405 HALT	/ERROR; AC NOT 0, AND K400 FAILED
			/TEST AND K200, AC=M200
04546	777577	LAW 17577	/AC=777577
04547	507316	AND K200	/200
04550	740200	SZA	
04551	740040	H406 HALT	/ERROR; AC NOT 0, AND K200 FAILED
			/TEST AND K100, AC=M100
04552	777677	LAW 17677	/AC=777677
04553	507317	AND K100	/100
04554	740200	SZA	
04555	740040	H407 HALT	/ERROR; AC NOT 0, AND K100 FAILED
			/TEST AND K40, AC=M40
04556	777737	LAW 17737	/AC=777737
04557	507320	AND K40	/40
04560	740200	SZA	
04561	740040	H408 HALT	/ERROR; AC NOT 0, AC=K40 FAILED
			/TEST AND K20, AC=M20
04562	777757	LAW 17757	/AC=777757
04563	507321	AND K20	/20
04564	740200	SZA	
04565	740040	H409 HALT	/AC NOT 0, AND K20 FAILED
			/TEST AND K10, AC=M10
04566	777767	LAW 17767	/AC=777767
04567	507322	AND K10	/10
04570	740200	SZA	
04571	740040	H410 HALT	/ERROR; AC NOT 0, AND K10 FAILED
			/TEST AND K4, AC=M4
04572	777773	LAW 17773	/AC=777773
04573	507323	AND K4	/4
04574	740200	SZA	
04575	740040	H411 HALT	/ERROR; AC NOT 0, AND K4 FAILED
			/TEST AND K2, AC=K2
04576	777775	LAW 17775	/AC=777775
04577	507324	AND K2	/2
04600	740200	SZA	
04601	740040	H412 HALT	/ERROR; AC NOT 0, AND K2 FAILED
			/TEST AND K1, AC=K1

04602	777776	LAW 17776	/AC=777776
04603	507325	AND K1	/1
04604	740200	SZA	
04605	740040	H413 HALT	/ERROR, AC NOT 0, AND K1 FAILED
			/TEST AND M400K, AC=K400K
04606	750001	CLA!CMA	/AC=ONES
04607	207304	LAC K400K	/AC=400K
04610	507335	AND M400K	/377777
04611	740200	SZA	
04612	740040	H414 HALT	/ERROR, AC NOT 0, AND M400K FAILED
			/TEST AND M200K, AC=K200K
04613	750001	CLA!CMA	/AC=ONES
04614	207305	LAC K200K	/AC=200K
04615	507336	AND M200K	/577777
04616	740200	SZA	
04617	740040	H415 HALT	/ERROR, AC NOT 0, AND M200K FAILED
			/TEST AND M100K, AC=K100K
04620	750001	CLA!CMA	/AC=ONES
04621	207306	LAC K100K	/AC=100K
04622	507337	AND M100K	/677777
04623	740200	SZA	
04624	740040	H416 HALT	/ERROR, AC NOT 0, AND M40K FAILED
			/TEST AND M40K, AC=K40K
04625	750001	CLA!CMA	/AC=ONES
04626	207307	LAC K40K	/AC=40K
04627	507340	AND M40K	/737777
04630	740200	SZA	
04631	740040	H417 HALT	/ERROR, AC NOT 0, AND M40K FAILED
			/TEST AND M20K, AC=K20K
04632	750001	CLA!CMA	/AC=ONES
04633	207310	LAC K20K	/AC=20K
04634	507341	AND M20K	/757777
04635	740200	SZA	
04636	740040	H418 HALT	/ERROR, AC NOT 0, AND M20K FAILED
			/TEST AND M10K, AC=K10K
04637	750001	CLA!CMA	/AC=ONES
04640	207311	LAC K10K	/AC=10K
04641	507342	AND M10K	/767777
04642	740200	SZA	
04643	740040	H419 HALT	/ERROR, AC NOT 0, AND M10, FAILED
			/TEST AND M4K, AC=K4K
04644	750001	CLA!CMA	/AC=ONES
04645	207312	LAC K4K	/AC=4K
04646	507343	AND M4K	/773777
04647	740200	SZA	
04650	740040	H420 HALT	/ERROR, AC NOT 0, AND M4K FAILED
			/TEST AND M2K, AC=K2K
04651	750001	CLA!CMA	/AC=ONES
04652	207313	LAC K2K	/AC=2K
04653	507344	AND M2K	/775777
04654	740200	SZA	
04655	740040	H421 HALT	/ERROR, AC NOT 0, AND M2K FAILED
			/TEST AND M1K, AC=K1K
04656	750001	CLA!CMA	/AC=ONES
04657	207314	LAC K1K	/AC=1K

04660	507345		AND M1K	/776777
04661	740200		SZA	
04662	740040	H422	HALT	/ERROR, AC NOT 0, AND M1K FAILED
			/TEST AND M400K, AC=K400	
04663	750001		CLA!CMA	/AC=ONES
04664	207315		LAC K400	/AC=400
04665	507346		AND M400	/777377
04666	740200		SZA	
04667	740040	H423	HALT	/ERROR, AC NOT 0, AND M400 FAILED
			/TEST AND M200, AC=K200	
04670	750001		CLA!CMA	/AC=ONES
04671	207316		LAC K200	/AC=200
04672	507347		AND M200	/777577
04673	740200		SZA	
04674	740040	H424	HALT	/ERROR, AC NOT 0, AND M200 FAILED
			/TEST AND M100, AC=K100	
04675	750001		CLA!CMA	/AC=ONES
04676	207317		LAC K100	/AC=100
04677	507350		AND M100	/777677
04700	740200		SZA	
04701	740040	H425	HALT	/ERROR, AC NOT 0, AND M100 FAILED
			/TEST AND M40, AC=K40	
04702	750001		CLA!CMA	/AC=ONES
04703	207320		LAC K40	/AC=40
04704	507351		AND M40	/777737
04705	740200		SZA	
04706	740040	H426	HALT	/ERROR, AC NOT 0, AND M40 FAILED
			/TEST AND M20, AC=K20	
04707	750001		CLA!CMA	/AC=ONES
04710	207321		LAC K20	/AC=20
04711	507352		AND M20	/777757
04712	740200		SZA	
04713	740040	H427	HALT	/ERROR, AC NOT 0, AND M20 FAILED
			/TEST AND M10, AC=K10	
04714	750001		CLA!CMA	/AC=ONES
04715	207322		LAC K10	/AC=10
04716	507353		AND M10	/777767
04717	740200		SZA	
04720	740040	H428	HALT	/ERROR, AC NOT 0, AND M10 FAILED
			/TEST AND M4, AC=K4	
04721	750001		CLA!CMA	/AC=ONES
04722	207323		LAC K4	/AC=4
04723	507354		AND M4	/777773
04724	740200		SZA	
04725	740040	H429	HALT	/ERROR, AC NOT 0, AND M4 FAILED
			/TEST AND M2, AC=K2	
04726	750001		CLA!CMA	/AC=ONES
04727	207324		LAC K2	/AC=2
04730	507355		AND M2	/777775
04731	740200		SZA	
04732	740040	H430	HALT	/ERROR, AC NOT 0, AND M2 FAILED
			/TEST AND M1, AC=K1	
04733	750001		CLA!CMA	/AC=ONES
04734	207325		LAC K1	/AC=1
04735	507356		AND M1	/777776

04736	740200		SZA
04737	740040	H431	HALT /ERROR, AC NOT 0, AND M1 FAILED
			/TEST AND K400K, BIT 0
04740	207304		LAC K400K /AC=400K
04741	507304		AND K400K /400K
04742	744100		SMA:CLL
04743	740040	H432	HALT /ERROR, BIT 0 NOT A 1, AND K400K FAILED
			/TEST AND K200K, BIT 1
04744	207305		LAC K200K /AC=200K
04745	507305		AND K200K /200K
04746	742010		RTL /L=1
04747	744400		SNL:CLL
04750	740040	H433	HALT /ERROR, BIT 1 NOT A 1, AND K200, FAILED
			/TEST AND K100K, BIT 2
04751	207306		LAC K100K /AC=100K
04752	507306		AND K100K /100K
04753	742010		RTL /AC=400K
04754	744100		SMA:CLL
04755	740040	H434	HALT /ERROR, BIT 2 NOT A 1, AND K100, FAILED
			/TEST AND K40K, BIT 3
04756	207307		LAC K40K /AC=40K
04757	507307		AND K40K /40K
04760	742010		RTL /AC=200K
04761	742010		RTL /L=1
04762	744400		SNL:CLL
04763	740040	H435	HALT /ERROR, BIT 3 NOT A 1, AND K40K FAILED
			/TEST AND K20K, BIT 4
04764	207310		LAC K20K /AC=20K
04765	507310		AND K20K /20K
04766	742010		RTL /AC=100K
04767	742010		RTL /AC=400K
04770	744100		SMA:CLL
04771	740040	H436	HALT /ERROR, BIT 4 NOT A 1, AND K200K FAILED
			/TEST AND K10K, BIT 5
04772	207311		LAC K10K /AC=10K
04773	507311		AND K10K /10K
04774	742010		RTL /AC=40K
04775	742010		RTL /AC=200K
04776	742010		RTL /L=1
04777	744400		SNL:CLL
05000	740040	H437	HALT /ERROR, BIT 5 NOT A 1, AND K10, FAILED
			/TEST AND K4K, BIT 6
05001	207312		LAC K4K /AC=4K
05002	507312		AND K4K /4K
05003	742010		RTL /AC=20K
05004	742010		RTL /AC=100K
05005	742010		RTL /AC=400K
05006	744100		SMA:CLL
05007	740040	H438	HALT /ERROR, BIT 6 NOT A 1, AND K4K FAILED
			/TEST AND K2K, BIT 7
05010	207313		LAC K2K /AC=2K
05011	507313		AND K2K /2K
05012	742010		RTL /AC=10K
05013	742010		RTL /AC=40K
05014	742010		RTL /AC=200K

05015	742010	RTL	/L=1
05016	744400	SNL:CLL	
05017	740040	H439 HALT	/ERROR, BIT 7 NOT A 1, AND K2K FAILED
			/TEST AND K1K, BIT 8
05020	207314	LAC K1K	/AC=1K
05021	507314	AND K1K	/1K
05022	742010	RTL	/AC=4K
05023	742010	RTL	/AC=20K
05024	742010	RTL	/AC=100K
05025	742010	RTL	/AC=400K
05026	744100	SMA:CLL	
05027	740040	H440 HALT	/ERROR, BIT 8 NOT 1A, AND K1K FAILED
			/TEST AND K400, BIT 9
05030	207315	LAC K400	/AC=400
05031	507315	AND K400	/400
05032	742010	RTL	/AC=2K
05033	742010	RTL	/AC=10K
05034	742010	RTL	/AC=40K
05035	742010	RTL	/AC=200K
05036	742010	RTL	/L=1
05037	744400	SNL:CLL	
05040	740040	H441 HALT	/ERROR, BIT 9 NOT A 1, AND K400 FAILED
			/TEST AND K200, BIT 10
05041	207316	LAC K200	/AC=200
05042	507316	AND K200	/200
05043	742020	RTR	/AC=40
05044	742020	RTR	/AC=10
05045	742020	RTR	/AC=2
05046	742020	RTR	/L=1
05047	744400	SNL:CLL	
05050	740040	H442 HALT	/ERROR, BIT 10 NOT A 1, AND K200 FAILED
			/TEST AND K100 BIT 11
05051	207317	LAC K100	/AC=100
05052	507317	AND K100	/100
05053	742020	RTR	/AC=20
05054	742020	RTR	/AC=4
05055	742020	RTR	/AC=1
05056	742020	RTR	/AC=400K
05057	744100	SMA:CLL	
05060	740040	H443 HALT	/ERROR, BIT 11 NOT A 1, AND K100 FAILED
			/TEST AND K40, BIT 12
05061	207320	LAC K40	/AC=40
05062	507320	AND K40	/40
05063	742020	RTR	/AC=10
05064	742020	RTR	/AC=2
05065	742020	RTR	/L=1
05066	744400	SNL:CLL	
05067	740040	H444 HALT	/ERROR, BIT 12 NOT A 1, AND K40 FAILED
			/TEST AND K20, BIT 13
05070	207321	LAC K20	/AC=20
05071	507321	AND K20	/20
05072	742020	RTR	/AC=4
05073	742020	RTR	/AC=1
05074	742020	RTR	/AC=400K
05075	744100	SMA:CLL	

05076	740040	H445	HALT	/ERROR, BIT 13 NOT A 1, AND K20 FAILED
05077	207322		LAC K10	/TEST AND K10, BIT 14
05100	507322		AND K10	/AC=10
05101	742020		RTR	/10
05102	742020		RTR	/AC=2
05103	744400		SNL:CLL	/L=1
05104	740040	H446	HALT	/ERROR, BIT 14 NOT A 1, AND K10 FAILED
05105	207323		LAC K4	/TEST AND K4, BIT 15
05106	507323		AND K4	/AC=4
05107	742020		RTR	/4
05110	742020		RTR	/AC=1
05111	744100		RTR	/AC=400K
05112	740040	H447	HALT	/ERROR, BIT 15 NOT A 1, AND K4 FAILED
05113	207324		LAC K2	/TEST AND K2, BIT 16
05114	507324		AND K2	/AC=2
05115	742020		RTR	/2
05116	744400		SNL:CLL	/L=1
05117	740040	H448	HALT	/ERROR, BIT 16 NOT A 1, AND K2 FAILED
05120	207325		LAC K1	/TEST AND K1, BIT 17
05121	507325		AND K1	/AC=1
05122	742020		RTR	/1
05123	744100		SMA:CLL	/AC=400K
05124	740040	H449	HALT	/ERROR, BIT 17 NOT A 1, AND K1 FAILED
05125	754003		CLA:CMC:CLL:ICML	/TEST AND M400K, AC=ONES
05126	507335		AND M400K	/AC=ONES, L=1
05127	740010		RAL	/377777
05130	741401		SZL:CMS	/AC=ONES
05131	740040	H450	HALT	/L=0, AC=0
05132	740200		SZA	/ERROR BIT 0 NOT 0, AND M400K FAILED
05133	740040	H451	HALT	/ERROR, AC DROPPED A BIT, AND M400K FAILED
05134	754003		CLA:CMC:CLL:ICML	/TEST AND M200K, AC=ONES
05135	507336		AND M200K	/AC=ONES, L=1
05136	742010		RTL	/AC=577777
05137	741401		SZL:CMS	/AC=ONES
05140	740040	H452	HALT	/L=0, AC=0
05141	740200		SZA	/ERROR, BIT 1 NOT A 0, AND M200 FAILED
05142	740040	H453	HALT	/ERROR, AC DROPPED A BIT, AND M200K FAILED
05143	754003		CLA:CMC:CLL:ICML	/TEST AND M100K, AC=ONES
05144	507337		AND M100K	/AC=ONES, L=1
05145	742010		RTL	/AC=677777
05146	740010		RAL	/AC=377777
05147	741401		SZL:CMS	/AC=ONES
05150	740040	H454	HALT	/L=0, AC=0
05151	740200		SZA	/ERROR, BIT 2 NOT A 0, AND M100K FAILED
05152	740040	H455	HALT	/ERROR, AC DROPPED A BIT AND M100K FAILED
05153	754003		CLA:CMC:CLL:ICML	/TEST AND M40, AC=ONES
05154	507340		AND M40K	/TEST AND M40, AD=ONES
				/AC=737777

05155	742010		RTL	/AC=577777
05156	742010		RTL	/AC=ONES
05157	741401		SZL:DMA	/L=0, AC=0
05160	740040	H450	HALT	/ERROR, BIT3 NOT A 0, AND M40K FAILED
05161	740200		SZA	
05162	740040	H457	HALT	/ERROR, AC DROPPED A BIT, AND M40K FAILED
				/TEST AND M20K, AC=ONES
05163	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05164	507341		AND M20K	/AC=757777
05165	742010		RTL	/AC=677777
05166	740010		RAL	/AC=577777
05167	742010		RTL	/AC=ONES
05170	741401		SZL:DMA	/L=0, AC=0
05171	740040	H450	HALT	/ERROR, BIT4 NOT A 0, AND M20K FAILED
05172	740200		SZA	
05173	740040	H457	HALT	/ERROR, AC DROPPED A BIT, AND M20K FAILED
				/TEST AND M10K, AC=ONES
05174	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05175	507342		AND M10K	/AC=767777
05176	742010		RTL	/AC=737777
05177	742010		RTL	/AC=577777
05200	742010		RTL	/AC=ONES
05201	741401		SZL:DMA	/AC=0, L=0
05202	740040	H460	HALT	/ERROR, BIT 5 NOT A 0, AND M10K FAILED
05203	740200		SZA	
05204	740040	H461	HALT	/ERROR, AC DROPPED A BIT, AND M20F FAILED
				/TEST AND M4K, AC=ONS
05205	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05206	507343		AND M4K	/AC=773777
05207	742010		RTL	/AC=757777
05210	742010		RTL	/AC=677777
05211	740010		RAL	/AC=577777
05212	742010		RTL	/AC=ONES
05213	741401		SZL:DMA	/AC=0, L=0
05214	740040	H462	HALT	/ERROR, BIT 6 NOT A 0, AND M4K FAILED
05215	740200		SZA	
05216	740040	H463	HALT	/ERROR, AC DROPPED A BIT, AND M4K FAILED
				/TEST AND M2K, AC=ONES
05217	754003		CLA:DMA:CLL:ICML	/TEST AND M2K, AC=ONES
05220	507344		AND M2K	/AC=775777
05221	742010		RTL	/AC=767777
05222	742010		RTL	/AC=737777
05223	742010		RTL	/AC=577777
05224	742010		RTL	/AC=ONES
05225	741401		SZL:DMA	/AC=0, L=0
05226	740040	H464	HALT	/ERROR, BIT7 NOT A 0, AND M2K FAILED
05227	740200		SZA	
05230	740040	H465	HALT	/ERROR, AC DROPPED A BIT, AND M2K FAILED
				/TEST AND M1K, AC=ONES
05231	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05232	507345		AND M1K	/AC=776777
05233	742010		RTL	/AC=773777
05234	742010		RTL	/AC=757777
05235	742010		RTL	/AC=677777
05236	740010		RAL	/AC=577777

05237	742010		RTL	/AC=ONES
05240	741401		SZL:DMA	/AC=0, L=0
05241	740040	H460	HALT	/ERROR, BIT 8 NOT A 0, AND M1K FAILED
05242	740200		SZA	
05243	740040	H467	HALT	/ERROR, AD DROPPED A BIT, AND M1K FAILED
				/TEST AND M400, AC=ONES
05244	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05245	507346		AND M400	/AC=777377
05246	742020		RTR	/AC=777677
05247	740020		RAR	/AC=777737
05250	742020		RTR	/AC=777767
05251	742020		RTR	/AC=777775
05252	742020		RTR	/AC=ONES
05253	741401		SZL:DMA	/AC=0, L=0
05254	740040	H460	HALT	/ERROR, BIT 9 NOT A0, AND M400 FAILED
05255	740200		SZA	
05256	740040	H469	HALT	/ERROR, AC DROPPED A BIT, AND M400 FAILED
				/TEST AND M200, AC=ONES
05257	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05260	507347		AND M200	/AC=777577
05261	742020		RTR	/AC=777737
05262	742020		RTR	/AC=777767
05263	742020		RTR	/AC=777775
05264	742020		RTR	/AC=ONES
05265	741401		SZL:DMA	/AC=0, L=0
05266	740040	H470	HALT	/ERROR, BIT 10 NOT A 0, AND M200 FAILED
05267	740200		SZA	
05270	740040	H471	HALT	/ERROR, AC DROPPED A BIT, AND M200 FAILED
				/TEST AND M100, AC=ONES
05271	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05272	507350		AND M100	/AC=777677
05273	742020		RTR	/AC=777757
05274	742020		RTR	/AC=777773
05275	740020		RAR	/AC=777775
05276	742020		RTR	/AC=ONES
05277	741401		SZL:DMA	/AC=0, L=0
05300	740040	H472	HALT	/ERROR, BIT 11 NOT A 0, AND M100 FAILED
05301	740200		SZA	
05302	740040	H473	HALT	/ERROR, AD DROPPED A BIT, AND M100 FAILED
				/TEST AND M40, AC=ONES
05303	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05304	507351		AND M40	/AC=777737
05305	742020		RTR	/AC=777767
05306	742020		RTR	/AC=777775
05307	742020		RTR	/AC=ONES
05310	741401		SZL:DMA	/AC=0, L=0
05311	740040	H474	HALT	/ERROR, BIT 12 NOT A 0, AND M40 FAILED
05312	740200		SZA	
05313	740040	H475	HALT	/ERROR, AC DROPPED A BIT, AND M40 FAILED
				/TEST AND M20, AC=ONES
05314	754003		CLA:DMA:CLL:ICML	/AC=ONES, L=1
05315	507352		AND M20	/AC=777757
05316	742020		RTR	/AC=777773
05317	740020		RAR	/AC=777775
05320	742020		RTR	/AC=ONES

05321	741401		SZLICMA	/AC=0, L=0
05322	740040	H476	HALT	/ERROR; BIT 13 NOT A 0, AND M20 FAILED
05323	740200		SZA	
05324	740040	H477	HALT	/ERROR; BIT 13 NOT A 0, AND M20 FAILED
				/TEST AND M10, AC=ONES
05325	754003		CLAICMAICLLICML	/AC=ONES, L=1
05326	507353		AND M10	/AC=777767
05327	742020		RTR	/AC=777775
05330	742020		RTR	/AC=ONES
05331	741401		SZLICMA	/AC=0, L=0
05332	740040	H478	HALT	/ERROR; BIT 14 NOT A 0, AND M10 FAILED
05333	740200		SZA	
05334	740040	H479	HALT	/ERROR; AC DROPPED A BIT, AND M10 FAILED
				/TEST AND M4, AC=ONES
05335	754003		CLAICMAICLLICML	/AC=ONES, L=1
05336	507354		AND M4	/AC=777773
05337	740020		RAR	/AC=777775
05340	742020		RTR	/AC=ONES
05341	741401		SZLICMA	/AC=0, L=0
05342	740040	H480	HALT	/ERROR; BIT 15 IS NOT A 0, AND M4 FAILED
05343	740200		SZA	
05344	740040	H481	HALT	/ERROR; AC DROPPED A BIT, AND M4 FAILED
				/TEST AND M2, AC=ONES
05345	754003		CLAICMAICLLICML	/AC=ONES, L=1
05346	507355		AND M2	/AC=777775
05347	742020		RTR	/AC=ONES
05350	741401		SZLICMA	/AC=0, L=0
05351	740040	H482	HALT	/ERROR; BIT 16 NOT A 0, AND M2 FAILED
05352	740200		SZA	
05353	740040	H483	HALT	/ERROR; AC DROPPED A BIT, AND M2 FAILED
				/INST TEST TAPE 6
				/TEST AND M1, AC=ONES
05354	754003		CLAICMAICLLICML	/AC=ONES, L=1
05355	507356		AND M1	/AC=777776
05356	740020		RAR	/AC=ONES
05357	741401		SZLICMA	/AC=0, L=0
05360	740040	H484	HALT	/ERROR; BIT 17 NOT A 0, AND M1 FAILED
05361	740200		SZA	
05362	740040	H485	HALT	/ERROR; AC DROPPED A BIT, AND M1 FAILED
				/TEST AND K7S, AC=M400K
05363	744002		CLLICML	/L=1
05364	207335		LAC M400K	/AC=377777
05365	507334		AND K7S	/AC=ONES
05366	740010		RAL	/AC=ONES
05367	741401		SZLICMA	/AC=0, L=0
05370	740040	H486	HALT	/ERROR; BIT 0 NOT A 0, AND K7S FAILED
05371	740200		SZA	
05372	740040	H487	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, AC=M200K
05373	744002		CLLICML	/L=1
05374	207336		LAC M200K	/AC=577777
05375	507334		AND K7S	/AC=ONES
05376	742010		RTL	/AC=ONES
05377	741401		SZLICMA	/AC=0, L=0
05400	740040	H488	HALT	/ERROR; BIT 1 NOT A 0, AND K7S FAILED

05401	740200		SZA	
05402	740040	H489	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, AC=M100K
05403	744002		CLL:CML	/L=1
05404	207337		LAC M100K	/AC=677777
05405	507334		AND K7S	/AC=ONES
05406	740010		RAL	/AC=577777
05407	742010		RTL	/AC=ONES
05410	741401		SZL:CMA	/AC=0, L=0
05411	740040	H490	HALT	/ERROR; BIT 2 NOT A 0, AND K7S FAILED
05412	740200		SZA	
05413	740040	H491	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, AC=M40K
05414	744002		CLL:CML	/L=1
05415	207340		LAC M40K	/AC=737777
05416	507334		AND K7S	/AC=ONES
05417	742010		RTL	/AC=577777
05420	742010		RTL	/AC=ONES
05421	741401		SZL:CMA	/AC=0, L=0
05422	740040	H492	HALT	/ERROR; BIT 3 NOT A 0, AND K7S FAILED
05423	740200		SZA	
05424	740040	H493	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, M20K
05425	744002		CLL:CML	/L=1
05426	207341		LAC M20K	/AC=757777
05427	507334		AND K7S	/AC=ONES
05430	740010		RAL	/AC=737777
05431	742010		RTL	/AC=577777
05432	742010		RTL	/AC=ONES
05433	741401		SZL:CMA	/AC=0, L=0
05434	740040	H494	HALT	/ERROR; BIT 4 NOT A 0, AND K7S FAILED
05435	740200		SZA	
05436	740040	H495	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, M10K
05437	744002		CLL:CML	/L=1
05440	207342		LAC M10K	/AC=767777
05441	507334		AND K7S	/AC=ONES
05442	742010		RTL	/AC=737777
05443	742010		RTL	/AC=577777
05444	742010		RTL	/AC=ONES
05445	741401		SZL:CMA	/AC=0, L=0
05446	740040	H496	HALT	/ERROR; BIT 5 NOT A 0, AND K7S FAILED
05447	740200		SZA	
05450	740040	H497	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, M4K
05451	744002		CLL:CML	/L=1
05452	207343		LAC M4K	/AC=773777
05453	507334		AND K7S	/AC=ONES
05454	740010		RAL	/AC=767777
05455	742010		RTL	/AC=737777
05456	742010		RTL	/AC=577777
05457	742010		RTL	/AC=ONES
05460	741401		SZL:CMA	/AC=0, L=0
05461	740040	H498	HALT	/ERROR; BIT 6 NOT A 0, AND K7S FAILED
05462	740200		SZA	

05463	740040	H499	HALT	/ERROR; AC DROPPED A BIT AND K7S FAILED
			/TST AND K7S, M2K	
05464	744002		CLL!CML	/L=1
05465	207344		LAC M2K	/AC=775777
05466	507334		AND K7S	/AC=ONES
05467	742010		RTL	/AC=763777
05470	742010		RTL	/AC=737777
05471	742010		RTL	/AC=577777
05472	742010		RTL	/AC=ONES
05473	741401		SZL!CMA	/AC=0, L=0
05474	740040	H500	HALT	/ERROR; BIT 7 NOT A 0, AND K7S FAILED
05475	740200		SZA	
05476	740040	H501	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M1K	
05477	744002		CLL!CML	/L=1
05500	207345		LAC M1K	/AC=776777
05501	507334		AND K7S	/AC=ONES
05502	740010		RAL	/AC=775777
05503	742010		RTL	/AC=767777
05504	742010		RTL	/AC=737777
05505	742010		RTL	/AC=577777
05506	742010		RTL	/AC=ONES
05507	741401		SZL!CMA	/AC=0, L=0
05510	740040	H502	HALT	/ERROR; BIT 8 NOT A 0, AND K7S FAILED
05511	740200		SZA	
05512	740040	H503	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M400	
05513	744002		CLL!CML	/L=1
05514	207346		LAC M400	/AC=777377
05515	507334		AND K7S	/AC=ONES
05516	740020		RAR	/AC=777577
05517	742020		RTR	/AC=777737
05520	742020		RTR	/AC=777767
05521	742020		RTR	/AC=777775
05522	742020		RTR	/AC=ONES
05523	741401		SZL!CMA	/AC=0, L=0
05524	740040	H504	HALT	/ERROR; BIT 9 NOT A 0, AND K7S FAILED
05525	740200		SZA	
05526	740040	H505	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M200	
05527	744002		CLL!CML	/L=1
05530	207347		LAC M200	/AC=777577
05531	507334		AND K7S	/AC=ONES
05532	742020		RTR	/AC=777737
05533	742020		RTR	/AC=777767
05534	742020		RTR	/AC=777775
05535	742020		RTR	/AC=ONES
05536	741401		SZL!CMA	/AC=0, L=0
05537	740040	H506	HALT	/ERROR; BIT 10 NOT A 0, AND K7S FAILED
05540	740200		SZA	
05541	740040	H507	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M100	
05542	744002		CLL!CML	/L=1
05543	207350		LAC M100	/AC=777677
05544	507334		AND K7S	/AC=ONES

05545	740020	RAR	/AC=777737
05546	742020	RTR	/AC=777767
05547	742020	RTR	/AC=777775
05550	742020	RTR	/AC=ONES
05551	741401	SZL:OMA	/AC=0, L=0
05552	740040	H500 HALT	/ERROR: BIT 11 NOT A 0, AND K7S FAILED
05553	740200	SZA	
05554	740040	H509 HALT	/ERROR: AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M40
05555	744002	CLL:OML	/L=1
05556	207351	LAC M40	/AC=777737
05557	507334	AND K7S	/AC=ONES
05560	742020	RTR	/AC=777767
05561	742020	RTR	/AC=777775
05562	742020	RTR	/AC=ONES
05563	741401	SZL:OMA	/AC=0, L=0
05564	740040	H510 HALT	/ERROR: BIT 12 NOT A 0, AND K7S FAILED
05565	740200	SZA	
05566	740040	H511 HALT	/ERROR: AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M20
05567	744002	CLL:OML	/L=1
05570	207352	LAC M20	/AC=777757
05571	507334	AND K7S	/AC=ONES
05572	740020	RAR	/AC=777767
05573	742020	RTR	/AC=777775
05574	742020	RTR	/AC=ONES
05575	741401	SZL:OMA	/AC=0, L=0
05576	740040	H512 HALT	/ERROR: BIT 13 NOT A 0, AND K7S FAILED
05577	740200	SZA	
05600	740040	H513 HALT	/ERROR: AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M10
05601	744002	CLL:OML	/L=1
05602	207353	LAC M10	/AC=777767
05603	507334	AND K7S	/AC=ONES
05604	742020	RTR	/AC=777775
05605	742020	RTR	/AC=ONES
05606	741401	SZL:OMA	/AC=0, L=0
05607	740040	H514 HALT	/ERROR: BIT 14 NOT A 0, AND K7S FAILED
05610	740200	SZA	
05611	740040	H515 HALT	/ERROR: AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M4
05612	744002	CLL:OML	/L=1
05613	207354	LAC M4	/AC=777773
05614	507334	AND K7S	/AC=ONES
05615	740020	RAR	/AC=777775
05616	742020	RTR	/AC=ONES
05617	741401	SZL:OMA	/AC=0, L=0
05620	740040	H516 HALT	/ERROR: BIT 15 NOT A 0, AND K7S FAILED
05621	740200	SZA	
05622	740040	H517 HALT	/ERROR: AC DROPPED A BIT, AND K7S FAILED
			/TEST AND K7S, M2
05623	744002	CLL:OML	/L=1
05624	207355	LAC M2	/AC=777775
05625	507334	AND K7S	/AC=ONES
05626	742020	RTR	/AC=ONES

05627	741401		SZL!CMA	/AC=0, L=0
05630	740040	H518	HALT	/ERROR; BIT 16 NOT A 0, AND K7S FAILED
05631	740200		SZA	
05632	740040	H519	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/TEST AND K7S, M1
05633	744002		CLL!CML	/L=1
05634	207356		LAC M1	/AC=777776
05635	507334		AND K7S	/AC=ONES
05636	740020		RAR	/AC=ONES
05637	741401		SZL!CMA	/AC=0, L=0
05640	740040	H520	HALT	/ERROR; BIT 17 NOT A 0, AND K7S FAILED
05641	740200		SZA	
05642	740040	H521	HALT	/ERROR; AC DROPPED A BIT, AND K7S FAILED
				/PART 1 - TAPE 7
				/XOR, TAD, ADD
				/TEST XOR 0, AC=0
05643	750000		CLA	/AC=0
05644	247303		XOR K0	/0
05645	740200		SZA	
05646	740040	H522	HALT	/ERROR; AC NOT 0, XOR 0 FAILED
				/TEST XOR K0S, AC=400K
05647	754001		CLA!CMAICLL	/AC=ONES, L=0
05650	207304		LAC K400K	/AC=400K
05651	247303		XOR K0	/0
05652	740010		RAL	/AC=0
05653	740400		SNL	/L=1, AC=0
05654	740040	H523	HALT	/ERROR; BIT 0 NOT A 1 XOR K0S
				/FAILED
05655	740200		SZA	
05656	740040	H524	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=200K
05657	754001		CLA!CMAICLL	/AC=ONES, L=0
05660	207305		LAC K200K	/AC=200K
05661	247303		XOR K0	/AC=10
05662	742010		RTL	/AC=0
05663	740400		SNL	/L=1, AC=0
05664	740040	H525	HALT	/ERROR; BIT 1 NOT A 1, XOR K0S
				/FAILED
05665	740200		SZA	
05666	740040	H526	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=100K
05667	754001		CLA!CMAICLL	/AC=ONES, L=0
05670	207306		LAC K100K	/AC=100K
05671	247303		XOR K0	/0
05672	742010		RTL	/AC=400K
05673	740010		RAL	/AC=0
05674	740400		SNL	/L=1, AC=0
05675	740040	H527	HALT	/ERROR; BIT 2 NOT A 1, XOR K0S
				/FAILED
05676	740200		SZA	
05677	740040	H528	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=40K

05700	754001		CLA!CMAICLL	/AC=ONES, L=0
05701	207307		LAC K40K	/AC=40K
05702	247303		XOR K0	/0
05703	742010		RTL	/AC=200K
05704	742010		RTL	/AC=0
05705	740400		SNL	/L=1, AC=0
05706	740040	H529	HALT	/ERROR; BIT 3 NOT A 1, XOR K0S
				/FAILED
05707	740200		SZA	
05710	740040	H530	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=20K
05711	754001		CLA!CMAICLL	/AC=ONES, L=0
05712	207310		LAC K20K	/AC=20K
05713	247303		XOR K0	/0
05714	742010		RTL	/AC=100K
05715	742010		RTL	/AC=400K
05716	740010		RAL	/AC=0
05717	740400		SNL	/L=1, AC=0
05720	740040	H531	HALT	/ERROR; BIT 4 NOT A 1, XOR K0S
				/FAILED
05721	740200		SZA	
05722	740040	H532	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=10K
05723	754001		CLA!CMAICLL	/AC=ONES, L=0
05724	207311		LAC K10K	/AC=10K
05725	247303		XOR K0	/0
05726	742010		RTL	/AC=40K
05727	742010		RTL	/AC=200K
05730	742010		RTL	/AC=0
05731	740400		SNL	/L=1, AC=0
05732	740040	H533	HALT	/ERROR; BIT 5 NOT A 1, XOR K0S
				/FAILED
05733	740200		SZA	
05734	740040	H534	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=4K
05735	754001		CLA!CMAICLL	/AC=ONES, L=0
05736	207312		LAC K4K	/AC=4K
05737	247303		XOR K0	/0
05740	742010		RTL	/AC=20K
05741	742010		RTL	/AC=100K
05742	742010		RTL	/AC=400K
05743	740010		RAL	/AC=0
05744	740400		SNL	/L=1, AC=0
05745	740040	H535	HALT	/ERROR; BIT 6 NOT A 1, XOR K0S
				/FAILED
05746	740200		SZA	
05747	740040	H536	HALT	/ERROR; AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=2K
05750	754001		CLA!CMAICLL	/AC=ONES, L=0
05751	207313		LAC K2K	/AC=2K
05752	247303		XOR K0	/0

05753	742010		RTL	/AC=10K
05754	742010		RTL	/AC=40K
05755	742010		RTL	/AC=200K
05756	742010		RTL	/AC=0
05757	740400		SNL	/L=1, AC=0
05760	740040	H537	HALT	/ERROR: BIT 7 NOT A 1, XOR K0S
				/FAILED
05761	740200		SZA	
05762	740040	H538	HALT	/ERROR: AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=1K
05763	754001		CLA:CMACLL	/AC=ONES, L=0
05764	207314		LAC K1K	/AC=1K
05765	247303		XOR K0	/0
05766	742010		RTL	/AC=4K
05767	742010		RTL	/AC=20K
05770	742010		RTL	/AC=100K
05771	742010		RTL	/AC=400K
05772	740010		RAL	/AC=0
05773	740400		SNL	/L=1, AC=0
05774	740040	H539	HALT	/ERROR: BIT 8 NOT A 1, XOR K0S
				/FAILED
05775	740200		SZA	
05776	740040	H540	HALT	/ERROR: AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=400
05777	754001		CLA:CMACLL	/AC=ONES, L=0
06000	207315		LAC K400	/AC=400
06001	247303		XOR K0	/0
06002	742020		RTR	/AC=100
06003	742020		RTR	/AC=20
06004	742020		RTR	/AC=4
06005	742020		RTR	/AC=1
06006	740020		RAR	/AC=0
06007	740400		SNL	/L=1, AC=0
06010	740040	H541	HALT	/ERROR: BIT 9 NOT A 1, XOR K0S
				/FAILED
06011	740200		SZA	
06012	740040	H542	HALT	/ERROR: AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=200
06013	754001		CLA:CMACLL	/AC=ONES, L=0
06014	207316		LAC K200	/AC=200
06015	247303		XOR K0	/0
06016	742020		RTR	/AC=40
06017	742020		RTR	/AC=10
06020	742020		RTR	/AC=2
06021	742020		RTR	/AC=0
06022	740400		SNL	/L=1, AC=0
06023	740040	H543	HALT	/ERROR: BIT 10 NOT A 1, XOR K0S
				/FAILED
06024	740200		SZA	
06025	740040	H544	HALT	/ERROR: AC PICKED A BIT, XOR K0S
				/FAILED
				/TEST XOR K0S, AC=100

06026	754001	CLA!CMAICLL	/AC=ONES, L=0
06027	207317	LAC K100	/AC=100
06030	247303	XOR K0	/0
06031	742020	RTR	/AC=100
06032	742020	RTR	/AC=20
06033	742020	RTR	/AC=4
06034	740020	RAR	/AC=1
06035	740400	SNL	/L=1, AC=0
06036	740040	H545 HALT	/ERROR! BIT 11 NOT A 1, XOR K0S
			/FAILED
06037	740200	SZA	
06040	740040	H546 HALT	/ERROR! AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=40
06041	754001	CLA!CMAICLL	/AC=ONES, L=0
06042	207320	LAC K40	/AC=40
06043	247303	XOR K0	/0
06044	742020	RTR	/AC=10
06045	742020	RTR	/AC=2
06046	742020	RTR	/AC=0
06047	740400	SNL	/L=1
06050	740040	H547 HALT	/ERROR! BIT 12 NOT A 1, XOR K0S
			/FAILED
06051	740200	SZA	
06052	740040	H548 HALT	/ERROR! AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=20
06053	754001	CLA!CMAICLL	/AC=ONES, L=0
06054	207321	LAC K20	/AC=20
06055	247303	XOR K0	/0
06056	742020	RTR	/AC=4
06057	742020	RTR	/AC=1
06060	740020	RAR	/AC=0
06061	740400	SNL	/L=1, AC=0
06062	740040	H549 HALT	/ERROR! BIT 13 NOT A 1, XOR K0S
			/FAILED
06063	740200	SZA	
06064	740040	H550 HALT	/ERROR! AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=10
06065	754001	CLA!CMAICLL	/AC=ONES, L=0
06066	207322	LAC K10	/AC=10
06067	247303	XOR K0	/0
06070	742020	RTR	/AC=2
06071	742020	RTR	/AC=0
06072	740400	SNL	/L=1, AC=0
06073	740040	H551 HALT	/ERROR! BIT 14 NOT A 1, XOR K0S
			/FAILED
06074	740200	SZA	
06075	740040	H552 HALT	/ERROR! AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=4
06076	754001	CLA!CMAICLL	/AC=ONES, L=0
06077	207323	LAC K4	/AC=4
06100	247303	XOR K0	/0

06101	742020	RTR	/AC=1
06102	740020	RAR	/AC=0
06103	740400	SNL	/L=1, AC=0
06104	740040	H553 HALT	/ERROR; BIT 15 NOT A 1, XOR K0S
			/FAILED
06105	740200	SZA	
06106	740040	H554 HALT	/ERROR; AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=2
06107	754001	CLA!CMAICLL	/AC=ONES, L=0
06110	207324	LAC K2	/AC=2
06111	247303	XOR K0	/0
06112	742020	RTR	/AC=0
06113	740400	SNL	/L=1, AC=0
06114	740040	H555 HALT	/ERROR; BIT 16 NOT A 1, XOR K0S
			/FAILED
06115	740200	SZA	
06116	740040	H556 HALT	/ERROR; AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K0S, AC=1
06117	754001	CLA!CMAICLL	/AC=ONES, L=0
06120	207325	LAC K1	/AC=1
06121	247303	XOR K0	/0
06122	740020	RAR	/AC=0
06123	740400	SNL	/L=1, AC=0
06124	740040	H557 HALT	/ERROR; BIT 17 NOT A 1, XOR K0S
			/FAILED
06125	740200	SZA	
06126	740040	H558 HALT	/ERROR; AC PICKED A BIT, XOR K0S
			/FAILED
			/TEST XOR K400K, AC=0
06127	754000	CLA!CLL	/AC=0, L=0
06130	247304	XOR K400K	/400K
06131	740010	RAL	/AC=0
06132	740400	SNL	/L=1, AC=0
06133	740040	H559 HALT	/ERROR; BIT 0 NOT A 1, XOR K400K FAILED
06134	740200	SZA	
06135	740040	H560 HALT	/ERROR; AC PICKED A BIT, XOR K400K FAILED
			/TEST XOR K200K, AC=0
06136	754000	CLA!CLL	/AC=0, L=0
06137	247305	XOR K200K	/200K
06140	742010	RTL	/AC=0
06141	740400	SNL	/L=1, AC=0
06142	740040	H561 HALT	/ERROR; BIT 1 NOT A 1, XOR K200K FAILED
06143	740200	SZA	
06144	740040	H562 HALT	/ERROR; AC PICKED A BIT, XOR K200K FAILED
			/TEST XOR K100K, AC=0
06145	754000	CLA!CLL	/AC=0, L=0
06146	247306	XOR K100K	/100K
06147	742010	RTL	/AC=400K
06150	740010	RAL	/AC=0
06151	740400	SNL	/L=1, AC=0
06152	740040	H563 HALT	/ERROR; BIT 2 NOT A 1, XOR K100K FAILED
06153	740200	SZA	
06154	740040	H564 HALT	/ERROR; AC PICKED A BIT, XOR K100K FAILED

			/TEST XOR K40K, AC=0
06155	754000	CLA:CLL	/AC=0, L=0
06156	247307	XOR K40K	/40K
06157	742010	RTL	/200K
06160	742010	RTL	/AC=0
06161	740400	SNL	/L=1, AC=0
06162	740040	H565 HALT	/ERROR; BIT 3 NOT A 1, XOR K40K FAILED
06163	740200	SZA	
06164	740040	H566 HALT	/ERROR; AC PICKED A BIT, XOR K40K FAILED
			/TEST XOR K20K, AC=0
06165	754000	CLA:CLL	/AC=0, L=0
06166	247310	XOR K20K	/20K
06167	742010	RTL	/AC=100K
06170	742010	RTL	/AC=400K
06171	740010	RAL	/AC=0
06172	740400	SNL	/L=1, AC=0
06173	740040	H567 HALT	/ERROR; BIT 4 NOT A 1, XOR K20K FAILED
06174	740200	SZA	
06175	740040	H568 HALT	/ERROR; AC PICKED A BIT, XOR K20K FAILED
			/TEST XOR K10K, AC=0
06176	754000	CLA:CLL	/AC=0, L=0
06177	247311	XOR K10K	/10K
06200	742010	RTL	/AC=40K
06201	742010	RTL	/AC=200K
06202	742010	RTL	/AC=0
06203	740400	SNL	/L=1, AC=0
06204	740040	H569 HALT	/ERROR; BIT 5 NOT A 1, XOR K10K FAILED
06205	740200	SZA	
06206	740040	H570 HALT	/ERROR; AC PICKED A BIT, XOR K10K FAILED
			/TEST XOR K4K, AC=0
06207	754000	CLA:CLL	/AC=0, L=0
06210	247312	XOR K4K	/4K
06211	742010	RTL	/AC=20K
06212	742010	RTL	/AC=100K
06213	742010	RTL	/AC=400K
06214	740010	RAL	/AC=0
06215	740400	SNL	/L=1, AC=0
06216	740040	H571 HALT	/ERROR; BIT 6 NOT A 1, XOR K4K FAILED
06217	740200	SZA	
06220	740040	H572 HALT	/ERROR; AC PICKED A BIT, XOR K4K FAILED
			/TEST XOR K2K, AC=0
06221	754000	CLA:CLL	/AC=0, L=0
06222	247313	XOR K2K	/2K
06223	742010	RTL	/AC=10K
06224	742010	RTL	/AC=40K
06225	742010	RTL	/AC=200K
06226	742010	RTL	/AC=0
06227	740400	SNL	/L=1, AC=0
06230	740040	H573 HALT	/ERROR; BIT 7 NOT A 1, XOR K2K FAILED
06231	740200	SZA	
06232	740040	H574 HALT	/ERROR; AC PICKED A BIT, XOR K2K FAILED
			/TEST XOR K1K, AC=0
06233	754000	CLA:CLL	/AC=0, L=0
06234	247314	XOR K1K	/1K
06235	742010	RTL	/AC=4K

06236	742010	RTL	/AC=20K
06237	742010	RTL	/AC=100K
06240	742010	RTL	/AC=400K
06241	740010	RAL	/AC=0
06242	740400	SNL	/L=1, AC=0
06243	740040	H575 HALT	/ERROR; BIT 8 NOT A 1, XOR K1K FAILED
06244	740200	SZA	
06245	740040	H576 HALT	/ERROR; AC PICKED A BIT, XOR K1K FAILED
			/TEST XOR K400, AC=0
06246	754000	CLA:CLL	/AC=0, L=0
06247	247315	XOR K400	/400
06250	742020	RTR	/AC=100
06251	742020	RTR	/AC=20
06252	742020	RTR	/AC=4
06253	742020	RTR	/AC=1
06254	740020	RAR	/AC=0
06255	740400	SNL	/L=1, AC=0
06256	740040	H577 HALT	/ERROR; BIT 9 NOT A 1, XOR K400 FAILED
06257	740200	SZA	
06260	740040	H578 HALT	/ERROR; AC PICKED A BIT, XOR K400 FAILED
			/TEST XOR K200, AC=0
06261	754000	CLA:CLL	/AC=0, L=0
06262	247316	XOR K200	/200
06263	742020	RTR	/AC=40
06264	742020	RTR	/AC=10
06265	742020	RTR	/AC=2
06266	742020	RTR	/AC=0
06267	740400	SNL	/L=1, AC=0
06270	740040	H579 HALT	/ERROR; BIT 10 NOT A 1, XOR K200 FAILED
06271	740200	SZA	
06272	740040	H580 HALT	/ERROR; AC PICKED A BIT, XOR K200 FAILED
			/TEST XOR K100, AC=0
06273	754000	CLA:CLL	/AC=0, L=0
06274	247317	XOR K100	/100
06275	742020	RTR	/AC=20
06276	742020	RTR	/AC=4
06277	742020	RTR	/AC=1
06300	740020	RAR	/AC=0
06301	740400	SNL	/L=1, AC=0
06302	740040	H581 HALT	/ERROR; BIT 11 NOT A 1, XOR K100 FAILED
06303	740200	SZA	
06304	740040	H582 HALT	/ERROR; AC PICKED A BIT, XOR K100 FAILED
			/TEST XOR K40, AC=0
06305	754000	CLA:CLL	/AC=0, L=0
06306	247320	XOR K40	/40
06307	742020	RTR	/AC=10
06310	742020	RTR	/AC=2
06311	742020	RTR	/AC=0
06312	740400	SNL	/L=1, AC=0
06313	740040	H583 HALT	/ERROR; BIT 12 NOT A 1, XOR K40 FAILED
06314	740200	SZA	
06315	740040	H584 HALT	/ERROR; AC PICKED A BIT, XOR K40 FAILED
			/TEST XOR K20, AC=0
06316	754000	CLA:CLL	/AC=0, L=0
06317	247321	XOR K20	/20

06320	742020	RTR	/AC=4
06321	742020	RTR	/AC=1
06322	740020	RAR	/AC=0
06323	740400	SNL	/L=1, AC=0
06324	740040	H585 HALT	/ERROR! BIT 13 NOT A 1, XOR K20 FAILED
06325	740200	SZA	
06326	740040	H586 HALT	/ERROR! AC PICKED A BIT, XOR K20 FAILED
			/TEST XOR K10, AC=0
06327	754000	CLA!CLL	/AC=0, L=0
06330	247322	XOR K10	/10
06331	742020	RTR	/AC=2
06332	742020	RTR	/AC=0
06333	740400	SNL	/L=1, AC=0
06334	740040	H587 HALT	/ERROR! BIT 14 NOT A 1, XOR K10 FAILED
06335	740200	SZA	
06336	740040	H588 HALT	/ERROR! AC PICKED A BIT, XOR K10 FAILED
			/TEST XOR K4, AC=0
06337	754000	CLA!CLL	/AC=0, L=0
06340	247323	XOR K4	/4
06341	742020	RTR	/AC=1
06342	740020	RAR	/AC=0
06343	740400	SNL	/L=1, AC=0
06344	740040	H589 HALT	/ERROR! BIT 15 NOT A 1, XOR K4 FAILED
06345	740200	SZA	
06346	740040	H590 HALT	/ERROR! AC PICKED A BIT, XOR K4 FAILED
			/TEST XOR K2, AC=0
06347	754000	CLA!CLL	/AC=0, L=0
06350	247324	XOR K2	/2
06351	742020	RTR	/AC=0
06352	740400	SNL	/L=1, AC=0
06353	740040	H591 HALT	/ERROR! BIT 16 NOT A 1, XOR K2 FAILED
06354	740200	SZA	
06355	740040	H592 HALT	/ERROR! AC PICKED A BIT, XOR K2 FAILED
			/TEST XOR K1, AC=0
06356	754000	CLA!CLL	/AC=0, L=0
06357	247325	XOR K1	/1
06360	740020	RAR	/AC=0
06361	740400	SNL	/L=1, AC=0
06362	740040	H593 HALT	/ERROR! BIT 17 NOT A 1, XOR K1 FAILED
06363	740200	SZA	
06364	740040	H594 HALT	/ERROR! AC PICKED A BIT, XOR K1 FAILED
			/INST, TEST TAPE 8
			/TEST XOR K400K, AC=K400K
06365	207304	LAC K400K	/AC=400K
06366	247304	XOR K400K	/400K
06367	740200	SZA	
06370	740040	H595 HALT	/ERROR! BIT 0 NOT A 0, XOR K400K FAILED
			/TEST XOR K200K, AC=K200K
06371	207305	LAC K200K	/AC=200K
06372	247305	XOR K200K	/200K
06373	740200	SZA	
06374	740040	H596 HALT	/ERROR! BIT 1 NOT A 0, XOR K200K FAILED
			/TEST XOR K100K, AC=K100K
06375	207306	LAC K100K	/AC=100K
06376	247306	XOR K100K	/100K

06377	740200		SZA	
06400	740040	H597	HALT	/ERROR; BIT 2 NOT A 0, XOR K100K FAILED
				/TEST XOR K400K, AC=K40K
26401	207307		LAC K40K	/AC=40K
26402	247307		XOR K40K	/40K
06423	740200		SZA	
06424	740040	H598	HALT	/ERROR; BIT 3 NOT A 0, XOR K40K FAILED
				/TEST XOR K20K, AC=K20K
06425	207310		LAC K20K	/AC=20K
06426	247310		XOR K20K	/20K
06427	740200		SZA	
06410	740040	H599	HALT	/ERROR; BIT 4 NOT A 0, XOR K20K FAILED
				/TEST XOR K10K, AC=K10K
06411	207311		LAC K10K	/AC=10K
06412	247311		XOR K10K	/10K
06413	740200		SZA	
06414	740040	H600	HALT	/ERROR; BIT 5 NOT A 0, XOR K10K FAILED
				/TEST XOR K4K, AC=K4K
06415	207312		LAC K4K	/AC=4K
06416	247312		XOR K4K	/4K
06417	740200		SZA	
06420	740040	H601	HALT	/ERROR; BIT 6 NOT A 0, XOR K4K FAILED
				/TEST XOR K2K, AC=K2K
06421	207313		LAC K2K	/AC=2K
06422	247313		XOR K2K	/2K
06423	740200		SZA	
06424	740040	H602	HALT	/ERROR; BIT 7 NOT A 0, XOR K2K FAILED
				/TEST XOR K1K, AC=K1K
06425	207314		LAC K1K	/AC=1K
06426	247314		XOR K1K	/1K
06427	740200		SZA	
06430	740040	H603	HALT	/ERROR; BIT 8 NOT A 0, XOR K1K FAILED
				/TEST XOR K400, AC=K400
06431	207315		LAC K400	/AC=400
06432	247315		XOR K400	/400
06433	740200		SZA	
06434	740040	H604	HALT	/ERROR; BIT 9 NOT A 0, XOR K400 FAILED
				/TEST XOR K200, AC=K200
06435	207316		LAC K200	/AC=200
06436	247316		XOR K200	/200
06437	740200		SZA	
06440	740040	H605	HALT	/ERROR; BIT 10 NOT A 0, XOR K200 FAILED
				/TEST XOR K100, AC=K100
06441	207317		LAC K100	/AC=100
06442	247317		XOR K100	/100
06443	740200		SZA	
06444	740040	H606	HALT	/ERROR; BIT 11 NOT A 0, XOR K100 FAILED
				/TEST XOR K40, AC=K40
06445	207320		LAC K40	/AC=40
06446	247320		XOR K40	/40
06447	740200		SZA	
06450	740040	H607	HALT	/ERROR; BIT 12 NOT A 0, XOR K40 FAILED
				/TEST XOR K20, AC=K20
06451	207321		LAC K20	/AC=20
06452	247321		XOR K20	/20

26453	740200		SZA	
26454	740040	H608	HALT	/ERROR; BIT 13 NOT A 0, XOR K20 FAILED
				/TEST XOR K10, AC=K10
26455	207322		LAC K10	/AC=10
26456	247322		XOR K10	/10
26457	740200		SZA	
26460	740040	H609	HALT	/ERROR; BIT 14 NOT A 0, XOR K10 FAILED
				/TEST XOR K4, AC=K4
26461	207323		LAC K4	/AC=4
26462	247323		XOR K4	/4
26463	740200		SZA	
26464	740040	H610	HALT	/ERROR; BIT 15 NOT A 0, XOR K4 FAILED
				/TEST XOR K2, AC=K2
26465	207324		LAC K2	/AC=2
26466	247324		XOR K2	/2
26467	740200		SZA	
26470	740040	H611	HALT	/ERROR; BIT 16 NOT A 0, XOR K2 FAILED
				/TEST XOR K1, AC=K1
26471	207325		LAC K1	/AC=1
26472	247325		XOR K1	/1
26473	740200		SZA	
26474	740040	H612	HALT	/ERROR; BIT 17 NOT A 0, XOR K1 FAILED
				/TEST XOR K7S, AC=M400K
26475	744000		CLL	
26476	207335		LAC M400K	/AC=377777
26477	247334		XOR K7S	/777777
26500	740010		RAL	/AC=0
26501	740400		SNL	/AC=0, L=1
26502	740040	H613	HALT	/ERROR; BIT 0 A 1, XOR K7S FAILED
26503	744200		SZA!CLL	
26504	740040	H614	HALT	/ERROR; AC PICKED A BIT, XOR K7 S FAILED
				/TEST XOR K7S, AC=M200K
26505	207336		LAC M200K	/AC=577777
26506	247334		XOR K7S	/777777
26507	742010		RTL	/AC=0
26510	740400		SNL	/AC=0, L=1
26511	740040	H615	HALT	/ERROR; BIT 1 NOT A 1, XOR K7S FAILED
26512	744200		SZA!CLL	
26513	740040	H616	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M100K
26514	207337		LAC M100K	/AC=677777
26515	247334		XOR K7S	/777777
26516	740010		RAL	/AC=200K
26517	742010		RTL	/AC=0
26520	740400		SNL	/AC=0, L=1
26521	740040	H617	HALT	/ERROR; BIT 2 NOT A 1, XOR K7S FAILED
26522	744200		SZA!CLL	
26523	740040	H618	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M40K
26524	207340		LAC M40K	/AC=737777
26525	247334		XOR K7S	/777777
26526	742010		RTL	/AC=200K
26527	742010		RTL	/AC=0
26530	740400		SNL	/AC=0, L=1
26531	740040	H619	HALT	/ERROR; BIT 3 NOT A 1, XOR K7S FAILED

06532	744200		SZA:CLL	
06533	740040	H620	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M20K
06534	207341		LAC M20K	/AC=757777
06535	247334		XOR K7S	/777777
06536	740010		RAL	/AC=40K
06537	742010		RTL	/AC=200K
06540	742010		RTL	/AC=0
06541	740400		SNL	/AC=0, L=1
06542	740040	H621	HALT	/ERROR; BIT 4 NOT A 1, XOR K7S FAILED
06543	744200		SZA:CLL	
06544	740040	H622	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M10K
06545	207342		LAC M10K	/AC=767777
06546	247334		XOR K7S	/777777
06547	742010		RTL	/AC=40K
06550	742010		RTL	/AC=200K
06551	742010		RTL	/AC=0
06552	740400		SNL	/AC=0, L=1
06553	740040	H623	HALT	/ERROR; BIT 5 NOT A 1, XOR K7S FAILED
06554	744200		SZA:CLL	
06555	740040	H624	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M4K
06556	207343		LAC M4K	/AC=773777
06557	247334		XOR K7S	/777777
06560	740010		RAL	/AC=10K
06561	742010		RTL	/AC=40K
06562	742010		RTL	/AC=200K
06563	742010		RTL	/AC=0
06564	740400		SNL	/AC=0, L=1
06565	740040	H625	HALT	/ERROR; BIT 6 NOT A 1, XOR K7S FAILED
06566	744200		SZA:CLL	
06567	740040	H626	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M2K
06570	207344		LAC M2K	/AC=775777
06571	247334		XOR K7S	/777777
06572	742010		RTL	/AC=10K
06573	742010		RTL	/AC=40K
06574	742010		RTL	/AC=200K
06575	742010		RTL	/AC=0
06576	740400		SNL	/AC=0, L=1
06577	740040	H627	HALT	/ERROR; BIT 7 NOT A 1, XOR K7S FAILED
06600	744200		SZA:CLL	
06601	740040	H628	HALT	/ERROR; AC PICKED A BIT, XOR K7S FAILED
				/TEST XOR K7S, AC=M1K
06602	207345		LAC M1K	/AC=776777
06603	247334		XOR K7S	/777777
06604	740010		RAL	/AC=2K
06605	742010		RTL	/AC=10K
06606	742010		RTL	/AC=40K
06607	742010		RTL	/AC=200K
06610	742010		RTL	/AC=0
06611	740400		SNL	/AC=0, L=1
06612	740040	H629	HALT	/ERROR; BIT 8 NOT A 1, XOR K7S FAILED
06613	744200		SZA:CLL	

06614	740040	H630	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED
06615	207346		LAC M400	/TEST XOR K7S, AC=M400
06616	247334		XOR K7S	/AC=777377
06617	740020		RAR	/AC=200
06620	742020		RTR	/AC=40
06621	742020		RTR	/AC=10
06622	742020		RTR	/AC=2
06623	742020		RTR	/AC=0
06624	740400		SNL	/AC=0, L=1
06625	740040	H631	HALT	/ERROR) BIT 9 NOT A 1, XOR K7S FAILED
06626	744200		SZAICLL	
06627	740040	H632	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED
06630	207347		LAC M200	/TEST XOR K7S, AC=M200
06631	247334		XOR K7S	/AC=777577
06632	742020		RTR	/AC=40
06633	742020		RTR	/AC=10
06634	742020		RTR	/AC=2
06635	742020		RTR	/AC=0
06636	740400		SNL	/AC=0, L=1
06637	740040	H633	HALT	/ERROR) BIT 10 NOT A 1, XOR K7S FAILED
06640	744200		SZAICLL	
06641	740040	H634	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED
06642	207350		LAC M100	/TEST XOR K7S, AC=M100
06643	247334		XOR K7S	/AC=777677
06644	740020		RAR	/AC=40
06645	742020		RTR	/AC=10
06646	742020		RTR	/AC=2
06647	742020		RTR	/AC=0
06650	740400		SNL	/AC=0, L=1
06651	740040	H635	HALT	/ERROR) BIT 11 NOT A 1, XOR K7S FAILED
06652	744200		SZAICLL	
06653	740040	H636	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED
06654	207351		LAC M40	/TEST XOR K7S, AC=M40
06655	247334		XOR K7S	/AC=777737
06656	742020		RTR	/AC=10
06657	742020		RTR	/AC=2
06660	742020		RTR	/AC=0
06661	740400		SNL	/AC=0, L=1
06662	740040	H637	HALT	/ERROR) BIT 12 NOT A 1, XOR K7S FAILED
06663	744200		SZAICLL	
06664	740040	H638	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED
06665	207352		LAC M20	/TEST XOR K7S, AC=M20
06666	247334		XOR K7S	/AC=777757
06667	740020		RAR	/777777
06670	742020		RTR	/AC=10
06671	742020		RTR	/AC=2
06672	740400		SNL	/AC=0, L=1
06673	740040	H639	HALT	/ERROR) BIT 13 NOT A 1, XOR K7S FAILED
06674	744200		SZAICLL	
06675	740040	H640	HALT	/ERROR) AC PICKED A BIT, XOR K7S FAILED

			/TEST XOR K7S, AC=M10
06676	207353	LAC M10	/AC=777767
06677	247334	XOR K7S	/777777
06700	742020	RTR	/AC=2
06701	742020	RTR	/AC=0
06702	740400	SNL	/AC=0, L=1
06703	740040	H641	HALT /ERROR; BIT 14 NOT A 1, XOR K7S FAILED
06704	744200	SZAICLL	
06705	740040	H642	HALT /ERROR; AC PICKED A BIT, XOR K7S FAILED
			/TEST XOR K7S, AC=M4
06706	207354	LAC M4	/AC=777773
06707	247334	XOR K7S	/777777
06710	740020	RAR	/AC=2
06711	742020	RTR	/AC=0
06712	740400	SNL	/AC=0, L=1
06713	740040	H643	HALT /ERROR; BIT 15 NOT A 1, XOR K7S FAILED
06714	744200	SZAICLL	
06715	740040	H644	HALT /ERROR; AC PICKED A BIT, XOR K7S FAILED
			/TEST XOR K7S, AC=M2
06716	207355	LAC M2	/AC=777775
06717	247334	XOR K7S	/777777
06720	742020	RTR	/AC=0
06721	740400	SNL	/AC=0, L=1
06722	740040	H645	HALT /ERROR; BIT 16 NOT A 1, XOR K7S FAILED
06723	744200	SZAICLL	
06724	740040	H646	HALT /ERROR; AC PICKED A BIT, XOR K7S FAILED
			/TEST XOR K7S, AC=M1
06725	207356	LAC M1	/AC=777776
06726	247334	XOR K7S	/777777
06727	740020	RAR	/AC=0
06730	740400	SNL	/AC=0, L=1
06731	740040	H647	HALT /ERROR; BIT 17 NOT A 1, XOR K7S FAILED
06732	744200	SZAICLL	
06733	740040	H648	HALT /ERROR; AC PICKED A BIT, XOR K7S FAILED
			/TEST XOR M400K, AC=ONES
06734	754001	CLAICMAICLL	/AC=ONES, L=0
06735	247335	XOR M400K	/377777
06736	740010	RAL	/AC=0
06737	740400	SNL	/AC=0, L=1
06740	740040	H649	HALT /ERROR; BIT 0 NOT A 1, XOR M400K FAILED
06741	740200	SZA	/ERROR; AC PICKED A BIT, XOR M400K FAILED
06742	740040	H650	HALT
			/TEST XOR M200K, AC=ONES
06743	754001	CLAICMAICLL	/AC=ONES, L=0
06744	247336	XOR M200K	/577777
06745	742010	RTL	/AC=0
06746	740400	SNL	/AC=0, L=1
06747	740040	H651	HALT /ERROR; BIT 1 NOT A 1, XOR M200K FAILED
06750	740200	SZA	/ERROR; AC PICKED A BIT, XOR M200K FAILED
06751	740040	H652	HALT
			/TEST XOR M100K, AC=ONES
06752	754001	CLAICMAICLL	/AC=ONES, L=0
06753	247337	XOR M100K	/677777
06754	740010	RAL	/AC=200K
06755	742010	RTL	/AC=0

06756	740400		SNL	/AC=0, L=1
06757	740040	H65J	HALT	/ERROR; BIT 2 NOT A 1, XOR M100K FAILED
06760	740200		SZA	/ERROR; AC PICKED A BIT, XOR M100K FAILED
06761	740040	H654	HALT	
				/TEST XOR M40K, AC=ONES
06762	754001		CLA!CMA!CLL	/AC=ONES, L=0
06763	247340		XOR M40K	/737777
06764	742010		RTL	/AC=200K
06765	742010		RTL	/AC=0
06766	740400		SNL	/AC=0, L=1
06767	740040	H655	HALT	/ERROR; BIT 3 NOT A 1, XOR M40K FAILED
06770	740200		SZA	/ERROR; AC PICKED A BIT, XOR M40K FAILED
06771	740040	H656	HALT	
				/TEST XOR M20K, AC=ONES
06772	754001		CLA!CMA!CLL	/AC=ONES, L=0
06773	247341		XOR M20K	/757777
06774	740010		RAL	/AC=40K
06775	742010		RTL	/AC=200K
06776	742010		RTL	/AC=0
06777	740400		SNL	/AC=0, L=1
07000	740040	H657	HALT	/ERROR; BIT 4 NOT A 1, XOR M20K FAILED
07001	740200		SZA	/ERROR; AC PICKED A BIT, XOR M20K FAILED
07002	740040	H658	HALT	
				/TEST XOR M10K, AC=ONES
07003	754001		CLA!CMA!CLL	/AC=ONES, L=0
07204	247342		XOR M10K	/767777
07005	742010		RTL	/AC=40K
07006	742010		RTL	/AC=200K
07007	742010		RTL	/AC=0
07010	740400		SNL	/AC=0, L=1
07011	740040	H659	HALT	/ERROR; BIT 5 NOT A 1 XOR M10K FAILED
07012	740200		SZA	/ERROR; AC PICKED A BIT, XOR M10K FAILED
07013	740040	H660	HALT	
				/TEST XOR M4K, AC=ONES
07014	754001		CLA!CMA!CLL	/AC=ONES, L=0
07015	247343		XOR M4K	/773777
07016	740010		RAL	/AC=10K
07017	742010		RTL	/AC=40K
07020	742010		RTL	/AC=200K
07021	742010		RTL	/AC=0
07022	740400		SNL	/AC=0, L=1
07023	740040	H661	HALT	/ERROR; BIT 6 NOT A 1, XOR M4K FAILED
07024	740200		SZA	/ERROR; AC PICKED A BIT, XOR M4K FAILED
07025	740040	H662	HALT	
				/TEST XOR M2K, AC=ONES
07026	754001		CLA!CMA!CLL	/AC=ONES, L=0
07027	247344		XOR M2K	/775777
07030	742010		RTL	/AC=10K
07031	742010		RTL	/AC=40K
07032	742010		RTL	/AC=200K
07033	742010		RTL	/AC=0
07034	740400		SNL	/AC=0, L=1
07035	740040	H663	HALT	/ERROR; BIT 7 NOT A 1, XOR M2K FAILED
07036	740200		SZA	/ERROR; AC PICKED A BIT, XOR M2K FAILED
07037	740040	H664	HALT	

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                                /TEST XOR M1K, AC=ONES
07040 754001 CLA!CMAICLL /AC=ONES, L=0
07041 247345 XOR M1K /776777
07042 740010 RAL /AC=2K
07043 742010 RTL /AC=10K
07044 742010 RTL /AC=40K
07045 742010 RTL /AC=200K
07046 742010 RTL /AC=0
07047 740400 SNL /AC=0, L=1
07050 740040 H665 HALT /ERROR! BIT 8 NOT A 1, XOR M1K FAILED
07051 740200 SZA /ERROR! AC PICKED A BIT, XOR M1K FAILED
07052 740040 H666 HALT

                                /TEST XOR M400, AC=ONES
07053 754001 CLA!CMAICLL /AC=ONES, L=0
07054 247346 XOR M400 /777377
07055 740020 RAR /AC=200
07056 742020 RTR /AC=40
07057 742020 RTR /AC=10
07060 742020 RTR /AC=2
07061 742020 RTR /AC=0
07062 740400 SNL /AC=0, L=1
07063 740040 H667 HALT /ERROR! BIT 9 NOT A 1, XOR M400 FAILED
07064 740200 SZA /ERROR! AC PICKED A BIT, XOR M400 FAILED
07065 740040 H668 HALT

                                /TEST XOR M200, AC=ONES
07066 754001 CLA!CMAICLL /AC=ONES, L=0
07067 247347 XOR M200 /777577
07070 742020 RTR /AC=40
07071 742020 RTR /AC=10
07072 742020 RTR /AC=2
07073 742020 RTR /AC=0
07074 740400 SNL /AC=0, L=1
07075 740040 H669 HALT /ERROR! BIT 10 NOT A 1, XOR M200 FAILED
07076 740200 SZA /ERROR! AC PICKED A BIT, XOR M200 FAILED
07077 740040 H670 HALT

                                /TEST XOR M100, AC=ONES
07100 754001 CLA!CMAICLL /AC=ONES, L=0
07101 247350 XOR M100 /777677
07102 740020 RAR /AC=40
07103 742020 RTR /AC=10
07104 742020 RTR /AC=2
07105 742020 RTR /AC=0
07106 740400 SNL /AC=0, L=1
07107 740040 H671 HALT /ERROR! BIT 11 NOT A 1, XOR M100 FAILED
07110 740200 SZA /ERROR! AC PICKED A BIT, XOR M100 FAILED
07111 740040 H672 HALT

                                /TEST XOR M40, AC=ONES
07112 754001 CLA!CMAICLL /AC=ONES, L=0
07113 247351 XOR M40 /777737
07114 742020 RTR /AC=10
07115 742020 RTR /AC=2
07116 742020 RTR /AC=0
07117 740400 SNL /AC=0, L=1
07120 740040 H673 HALT /ERROR! BIT 12 NOT A 1, XOR M40 FAILED
07121 740200 SZA /ERROR! AC PICKED A BIT, XOR M40 FAILED

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07122 740040 H674 HALT
                                /TEST XOR M20, AC=ONES
07123 754001 CLA!CMA!CLL /AC=ONES, L=0
07124 247352 XOR M20 /777757
07125 740020 RAR /AC=10
07126 742020 RTR /AC=2
07127 742020 RTR /AC=0
07130 740400 SNL /AC=0, L=1
07131 740040 H675 HALT /ERROR; BIT 14 NOT A 1, XOR M20 FAILED
07132 740200 SZA /ERROR; AC PICKED A BIT, XOR M20 FAILED
07133 740040 H676 HALT

                                /TEST XOR M10, AC=ONES
07134 754001 CLA!CMA!CLL /AC=ONES, L=0
07135 247353 XOR M10 /777767
07136 742020 RTR /AC=2
07137 742020 RTR /AC=0
07140 740400 SNL /AC=0, L=1
07141 740040 H677 HALT /ERROR; BIT 14 NOT A 1, XOR M10 FAILED
07142 740200 SZA /ERROR; AC PICKED A BIT, XOR M10 FAILED
07143 740040 H678 HALT

                                /TEST XOR M4, AC=ONES
07144 754001 CLA!CMA!CLL /AC=ONES, L=0
07145 247354 XOR M4 /77773
07146 740020 RAR /AC=2
07147 742020 RTR /AC=0
07150 740400 SNL /AC=0, L=1
07151 740040 H679 HALT /ERROR; BIT 15 NOT A 1,, XOR M4 FAILED
07152 740200 SZA /ERROR; AC PICKED A BIT, XOR M4 FAILED
07153 740040 H680 HALT

                                /TEST XOR M2, AC=ONES
07154 754001 CLA!CMA!CLL /AC=ONES, L=0
07155 247355 XOR M2 /777775
07156 742020 RTR /AC=
07157 740400 SNL /AC=0, L=1
07160 740040 H681 HALT /ERROR; BIT 16 NOT A 1, XOR M2 FAILED
07161 740200 SZA /ERROR; AC PICKED A BIT, XOR M2 FAILED
07162 740040 H682 HALT

                                /TEST XOR M1, AC=ONES
07163 754001 CLA!CMA!CLL /AC=ONES, L=0
07164 247356 XOR M1 /777776
07165 740020 RAR /AC=0
07166 740400 SNL /AC=0, L=1
07167 740040 H683 HALT /ERROR; BIT 17 NOT A 1, XOR M1 FAILED
07170 740200 SZA /ERROR; AC PICKED A BIT, XOR M1 FAILED
07171 740040 H684 HALT

                                /TEST XOR K7S, AC=ONES, L=1
07172 754003 CLA!CMA!CLL!CML /AC=ONES, L=1
07173 247334 XOR K7S /777777
07174 740200 SZA
07175 740040 H685 HALT /ERROR; AC NOT 0, XOR K7S FAILED
07176 740400 SNL
07177 740040 H686 HALT /ERROR; LINK=0, XOR K7S FAILED

                                /TEST XOR K7S, AC=ONES, L=0
07200 754001 CLA!CMA!CLL /AC=ONES, L=0
07201 247334 XOR K7S /777777

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07202	741400		SZL	
07203	740040	H687	HALT	/ERROR; LINK=1, XOR K7S FAILED
				/TEST XOR K2525, AC=252525, L=1
07204	744002		CLL!CML	/L=1
07205	207367		LAC K2525	/AC=252525
07206	247367		XOR K2525	/252525
07207	740200		SZA	
07210	740040	H688	HALT	/ERROR; AC NOT 0, XOR K2525 FAILED
07211	740400		SNL	
07212	740040	H689	HALT	/ERROR; LINK NOT A 1, XOR K2525 FAILED
				/TEST XOR K5252, AC=525252, L=0
07213	744000		CLL	/L=0
07214	207366		LAC K5252	/AC=525252
07215	247366		XOR K5252	/525252
07216	740200		SZA	
07217	740040	H690	HALT	/ERROR; AC NOT 0, XOR K5252 FAILED
07220	741400		SZL	
07221	740040	H691	HALT	/ERROR; LINK NOT A 0, XOR K5252 FAILED
				/TEST XOR K5252, AC=252525
07222	207367		LAC K2525	/AC=252525
07223	247366		XOR K5252	/525252
07224	740001		CMA	/AC=0
07225	740200		SZA	
07226	740040	H692	HALT	/ERROR; AC NOT ONES, XOR K5252 FAILED
				/TEST LAC, AND, XOR SERIES, L=1
07227	744002		CLL!CML	/L=1
07230	207334		LAC K7S	/AC=ONES
07231	507334		AND K7S	/AC=ONES
07232	247334		XOR K7S	/AC=0
07233	740200		SZA	
07234	740040	H693	HALT	/ERROR; AC NOT 0, SERIES FAILED
07235	740400		SNL	
07236	740040	H694	HALT	/ERROR; LINK NOT A 1, SERIES FAILED
				/TEST LAC, XOR, AND SERIES, L=0
07237	744000		CLL	/L=0
07240	207303		LAC K0	/AC=0
07241	247334		XOR K7S	/AC=ONES
07242	507334		AND K7S	/AC=ONES
07243	740001		CMA	/AC=0
07244	740200		SZA	
07245	740040	H695	HALT	/ERROR; AC NOT ONES, SERIES FAILED
07246	741400		SZL	
07247	740040	H696	HALT	/ERROR; LINK NOT A 0, SERIES FAILED
				/TEST XOR K7S SERIES, L=0
07250	754001		CLL!CLA!CMA	/AC=ONES
07251	247334		XOR K7S	/AC=0
07252	247334		XOR K7S	/AC=ONES
07253	247334		XOR K7S	/AC=0
07254	247334		XOR K7S	/AC=ONES
07255	247334		XOR K7S	/AC=0
07256	247334		XOR K7S	/AC=ONES
07257	740001		CMA	/AC=0
07260	740200		SZA	
07261	740040	H697	HALT	/ERROR; AC NOT 0, XOR SERIES FAILED
07262	741400		SZL	


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07263 740040 H698 HALT /ERROR! LINK NOT 0, XOR SERIES FAILED
07264 207470 LAC COUNT
07265 347325 TAD K1
07266 047470 DAC COUNT
07267 740200 SZA
07270 600201 JMP PART1
07271 750004 LAS /DON'T RING BELL IF AC SW0=1
07272 741100 SPA
07273 600201 JMP PART1
07274 760207 LAW 207
07275 700406 TLS
07276 700401 TSF
07277 607276 JMP .-1
07300 207426 BEGIN LAC M77
07301 047470 DAC COUNT
07302 600201 JMP PART1
/PDP-9 INSTRUCTION TEST PART 1 AND 1A - LAST TAPE
/CONSTANTS
740030 IAC=740030
742030 SWHA=742030
740000 NOP1=NOP
740000 NOP2=NOP
740000 NOPJ=NOP
740040 HALT=HLT
07303 000000 K0 0
07304 400000 K400K 400000
07305 200000 K200K 200000
07306 100000 K100K 100000
07307 040000 K40K 40000
07310 020000 K20K 20000
07311 010000 K10K 10000
07312 004000 K4K 4000
07313 002000 K2K 2000
07314 001000 K1K 1000
07315 000400 K400 400
07316 000200 K200 200
07317 000100 K100 100
07320 000040 K40 40
07321 000020 K20 20
07322 000010 K10 10
07323 000004 K4 4
07324 000002 K2 2
07325 000001 K1 1
07326 111111 K1S 111111
07327 222222 K2S 222222
07330 333333 K3S 333333
07331 444444 K4S 444444
07332 555555 K5S 555555
07333 666666 K6S 666666
07334 777777 K7S 777777
07335 377777 M400K 377777
07336 577777 M200K 577777
07337 677777 M100K 677777
07340 737777 M40K 737777
07341 757777 M20K 757777

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07342	767777	M10K	767777
07343	773777	M4K	773777
07344	775777	M2K	775777
07345	776777	M1K	776777
07346	777377	M400	777377
07347	777577	M200	777577
07350	777677	M100	777677
07351	777737	M40	777737
07352	777757	M20	777757
07353	777767	M10	777767
07354	777773	M4	777773
07355	777775	M2	777775
07356	777776	M1	777776
07357	377776	M400KM	377776
07360	577776	M200KM	577776
07361	677776	M100KM	677776
07362	737776	M40KM	737776
07363	757776	M20KM	757776
07364	767776	M10KM	767776
07365	773776	M4KM	773776
07366	525252	K5252	525252
07367	252525	K2525	252525
07370	775776	M2KM	775776
07371	776776	M1KM	776776
07372	777376	M400M	777376
07373	777576	M200M	777576
07374	777676	M100M	777676
07375	777736	M40M	777736
07376	777756	M20M	777756
07377	777766	M10M	777766
07400	777772	M4M	777772
07401	777774	M2M	777774
07402	777775	M1M	777775
07403	000003	K3	3
07404	000007	K7	7
07405	000017	K17	17
07406	000037	K37	37
07407	000077	K77	77
07410	000177	K177	177
07411	000377	K377	377
07412	000777	K777	777
07413	001777	K1777	1777
07414	003777	K3777	3777
07415	007777	K7777	7777
07416	017777	K17777	17777
07417	037777	K37777	37777
07420	077777	K77777	77777
07421	177777	M600K	177777
07422	777774	M3	777774
07423	777770	M7	777770
07424	777760	M17	777760
07425	777740	M37	777740
07426	777700	M77	777700
07427	777600	M177	777600
07430	777400	M377	777400

07431	777000	M777	777000
07432	776000	M1777	776000
07433	774000	M3777	774000
07434	770000	M7777	770000
07435	760000	M17777	760000
07436	740000	M37777	740000
07437	700000	M77777	700000
07440	600000	K600K	600000
07441	777000	K777K	777000
07442	000005	K5	5
07443	000011	K11	11
07444	000021	K21	21
07445	000041	K41	41
07446	000101	K101	101
07447	000201	K201	201
07450	000401	K401	401
07451	001001	K1001	1001
07452	002001	K2001	2001
07453	004001	K4001	4001
07454	010001	K10001	10001
07455	740040	KHALT	740040
07456	017760	KCAL0	17760
07457	002021	K2021	2021
07460	002120	K2120	2120
07461	617760	KCALE	617760
07462	741000	KSKP	SKP
07463	750000	KCLA	CLA
07464	525253	K5253	525253
07465	060000	K60K	60000
07466	625253	K6625	625253
07467	615253	K615	615253
07470	777777	COUNT	777777
	000000		.END
			NO ERROR LINES

BEGIN	07300
CLOF	700004
CLON	700044
CLSF	700001
COUNT	07470
HALT	740040
H1	00202
H10	00242
H100	00257
H101	00263
H102	00265
H103	00271
H104	00273
H105	00277
H106	01003
H107	01007
H108	01013
H109	01017
H11	00247
H110	01023
H111	01027
H112	01033
H113	01040
H114	01045
H115	01052
H116	01057
H117	01104
H118	01131
H119	01145
H12	00252
H120	01161
H121	01166
H122	01170
H123	01175
H124	01177
H125	01204
H126	01206
H127	01213
H128	01215
H129	01222
H13	00255
H130	01224
H131	01231
H132	01233
H133	01240
H134	01242
H135	01247
H136	01251
H137	01257
H138	01261
H139	01267
H14	00260
H140	01271
H141	01277
H142	01301

H143	01307
H144	01335
H145	21363
H146	01400
H147	01415
H148	01421
H149	01425
H15	00264
H150	01431
H151	01435
H152	01441
H153	01443
H154	01447
H155	01451
H156	01454
H157	01460
H158	01463
H159	01470
H16	00270
H160	01474
H161	01501
H162	01506
H163	01513
H164	01520
H165	01525
H166	01531
H167	01535
H168	01541
H169	01545
H17	00273
H170	01551
H171	01555
H172	01561
H173	01565
H174	01570
H175	01574
H176	01600
H177	01604
H178	01610
H179	01613
H18	00276
H180	01616
H181	01621
H182	01625
H183	01632
H184	01636
H185	01644
H186	01651
H187	01655
H188	01660
H189	01664
H19	00303
H190	01670
H192	01674
H193	01701

H194	01706
H195	01713
H196	01720
H197	01723
H198	01726
H199	01731
H2	00205
H20	00307
H200	01734
H201	01741
H202	01744
H203	01747
H204	01752
H205	01755
H206	01761
H207	01765
H208	01771
H21	00312
H210	01775
H211	02000
H212	02015
H213	02032
H214	02034
H215	02041
H216	02046
H217	02050
H218	02065
H219	02100
H22	00317
H220	02103
H221	02110
H222	02113
H223	02116
H224	02121
H225	02124
H226	02126
H227	02132
H228	02135
H229	02140
H23	00323
H230	02143
H231	02146
H232	02151
H233	02153
H234	02162
H235	02171
H236	02201
H237	02211
H238	02222
H239	02233
H24	00330
H240	02245
H241	02257
H242	02272
H243	02305

H244	02321
H245	02335
H246	02352
H247	02365
H248	02367
H249	02404
H25	00335
H250	02406
H251	02423
H252	02425
H253	02444
H254	02446
H255	02465
H256	02467
H257	02510
H258	02512
H259	02533
H26	00341
H260	02535
H261	02560
H262	02562
H263	02605
H264	02627
H265	02634
H266	02636
H267	02663
H268	02665
H269	02714
H27	00344
H270	02716
H271	02745
H272	02747
H273	02760
H274	02762
H275	02767
H276	02775
H277A	03001
H277B	03003
H277C	03010
H277D	03015
H277E	03023
H278	03031
H279	03040
H28	00350
H280	03047
H281	03057
H282	03067
H283	03120
H284	03111
H285	03123
H286	03135
H287	03150
H288	03163
H289	03177
H29	00354

H290	03213
H291	03230
H292	03245
H293	03251
H294	03255
H295	03261
H296	03266
H297	03270
H298	03275
H299	03277
H3	00211
H30	02360
H300	03305
H301	03307
H302	03315
H303	03317
H304	03326
H305	03330
H306	03337
H307	03341
H308	03351
H309	03353
H31	00364
H310	03363
H311	03365
H312	03376
H313	03400
H314	03411
H315	03413
H316	03425
H317	03427
H318	03441
H319	03443
H32	00370
H320	03456
H321	03460
H322	03473
H323	03475
H324	03511
H325	03513
H326	03527
H327	03531
H328	03546
H329	03553
H33	00374
H330	03565
H331	03567
H332	03576
H333	03600
H334	03607
H335	03611
H336	03622
H337	03624
H338	03635
H339	03637

H34	00377
H340	03652
H341	23654
H342	03667
H343	03671
H344	23706
H345	03710
H346	03725
H347	03727
H348	03746
H349	23750
H35	04404
H350	03767
H351	03771
H352	04012
H353	24014
H354	24035
H355	04037
H356	04062
H357	04064
H358	04107
H359	04111
H36	00410
H360	04136
H361	04140
H362	04165
H363	04167
H364	04216
H365	04220
H366	04247
H367	04251
H368A	04255
H368B	04261
H368C	04265
H368D	04271
H368E	04275
H368F	04301
H368G	04305
H368H	04311
H368I	04315
H368J	04321
H369	04325
H37	00413
H370	04331
H371	04335
H372	04341
H373	04345
H374	04351
H375	04355
H376	04361
H377	04365
H378	04371
H379	04375
H38	00417
H380	04401

H381	04405
H382	04411
H383	04415
H384	04421
H385	04425
H386	04431
H387	04435
H388	04441
H389	04445
H39	00423
H390	04451
H391	04455
H392	04461
H393	04465
H394	04471
H395	04475
H396	04501
H397	04505
H398	04511
H399	04515
H4	00214
H40	00432
H400	04521
H401	04525
H402	04531
H403	04535
H404	04541
H405	04545
H406	04551
H407	04555
H408	04561
H409	04565
H41	00437
H410	04571
H411	04575
H412	04601
H413	04605
H414	04612
H415	04617
H416	04624
H417	04631
H418	04636
H419	04643
H42	00445
H420	04650
H421	04655
H422	04662
H423	04667
H424	04674
H425	04701
H426	04706
H427	04713
H428	04720
H429	04725
H43	00451

H430	04732
H431	04737
H432	04743
H433	04750
H434	04755
H435	04763
H436	04771
H437	05000
H438	05007
H439	05017
H44	00455
H440	05027
H441	05040
H442	05050
H443	05060
H444	05067
H445	05076
H446	05104
H447	05112
H448	05117
H449	05124
H45	00461
H450	05131
H451	05133
H452	05140
H453	05142
H454	05150
H455	05152
H456	05160
H457	05162
H458	05171
H459	05173
H46	00464
H460	05202
H461	05204
H462	05214
H463	05216
H464	05226
H465	05230
H466	05241
H467	05243
H468	05254
H469	05256
H47	00470
H470	05266
H471	05270
H472	05300
H473	05302
H474	05311
H475	05313
H476	05322
H477	05324
H478	05332
H479	05334
H48	00473

H480	05342
H481	05344
H482	05351
H483	05353
H484	05360
H485	05362
H486	05370
H487	05372
H488	05400
H489	05402
H49	00477
H490	05411
H491	05413
H492	05422
H493	05424
H494	05434
H495	05436
H496	05446
H497	05450
H498	05461
H499	05463
H5	00220
H50	00502
H500	05474
H501	05476
H502	05510
H503	05512
H504	05524
H505	05526
H506	05537
H507	05541
H508	05552
H509	05554
H51	00505
H510	05564
H511	05566
H512	05576
H513	05600
H514	05607
H515	05611
H516	05620
H517	05622
H518	05630
H519	05632
H52	00511
H520	05640
H521	05642
H522	05646
H523	05654
H524	05656
H525	05664
H526	05666
H527	05675
H528	05677
H529	05706

H53	00515
H530	05710
H531	05720
H532	05722
H533	05732
H534	05734
H535	05745
H536	05747
H537	05760
H538	05762
H539	05774
H54	00522
H540	05776
H541	06010
H542	06012
H543	06023
H544	06025
H545	06036
H546	06040
H547	06050
H548	06052
H549	06062
H55	00526
H550	06064
H551	06073
H552	06075
H553	06104
H554	06106
H555	06114
H556	06116
H557	06124
H558	06126
H559	06133
H56	00532
H560	06135
H561	06142
H562	06144
H563	06152
H564	06154
H565	06162
H566	06164
H567	06173
H568	06175
H569	06204
H57	00536
H570	06206
H571	06216
H572	06220
H573	06230
H574	06232
H575	06243
H576	06245
H577	06256
H578	06260
H579	06270

H58	00542
H580	06272
H581	06302
H582	06304
H583	06313
H584	06315
H585	06324
H586	06326
H587	06334
H588	06336
H589	06344
H59	02545
H590	06346
H591	06353
H592	06355
H593	06362
H594	06364
H595	06370
H596	06374
H597	06400
H598	06404
H599	06410
H6	00223
H60	00551
H600	06414
H601	06420
H602	06424
H603	06430
H604	06434
H605	06440
H606	06444
H607	06450
H608	06454
H609	06460
H61	00554
H610	06464
H611	06470
H612	06474
H613	06502
H614	06504
H615	06511
H616	06513
H617	06521
H618	06523
H619	06531
H62	00560
H620	06533
H621	06542
H622	06544
H623	06553
H624	06555
H625	06565
H626	06567
H627	06577
H628	06601

H629	06612
H63	00563
H630	06614
H631	06625
H632	06627
H633	06637
H634	06641
H635	06651
H636	06653
H637	06662
H638	06664
H639	06673
H64	00567
H640	06675
H641	06703
H642	06705
H643	06713
H644	06715
H645	06722
H646	06724
H647	06731
H648	06733
H649	06740
H65	00572
H650	06742
H651	06747
H652	06751
H653	06757
H654	06761
H655	06767
H656	06771
H657	07000
H658	07002
H659	07011
H66	00575
H660	07013
H661	07023
H662	07025
H663	07035
H664	07037
H665	07050
H666	07052
H667	07063
H668	07065
H669	07075
H67	00601
H670	07077
H671	07107
H672	07111
H673	07120
H674	07122
H675	07131
H676	07133
H677	07141
H678	07143

H679	07151
H68	00604
H680	07153
H681	07160
H682	07162
H683	07167
H684	07171
H685	07175
H686	07177
H687	07203
H688	07210
H689	07212
H69	00607
H690	07217
H691	07221
H692	07226
H693	07234
H694	07236
H695	07245
H696	07247
H697	07261
H698	07263
H7	00227
H70	07612
H71	00615
H72	00620
H73	00624
H74	00627
H75	00632
H76	00636
H77	00642
H78	00646
H79	00652
H8	00232
H80	00656
H81	00662
H82	00666
H83	00671
H84	00674
H85	00677
H86	00703
H87	00706
H88	00711
H89	00714
H9	00236
H90	00717
H91	00723
H92	00727
H93	00732
H94	00735
H95	00740
H96	00744
H97	00747
H98	00751
H99	00755

IAC	740030
KCALE	07461
KCAL0	07456
KCLA	07463
KHALT	07455
KRB	700312
KSF	700301
KSKP	07462
K0	07303
K1	07325
K1K	07314
K1S	07326
K10	07322
K10K	07311
K100	07317
K100K	07306
K10001	07454
K1001	07451
K101	07446
K11	07443
K17	07405
K177	07410
K1777	07413
K17777	07416
K2	07324
K2K	07313
K2S	07327
K20	07321
K20K	07310
K200	07316
K200K	07305
K2001	07452
K201	07447
K2021	07457
K21	07444
K2120	07460
K2525	07367
K3	07403
K3S	07330
K37	07406
K377	07411
K3777	07414
K37777	07417
K4	07323
K4K	07312
K4S	07331
K40	07320
K40K	07307
K400	07315
K400K	07304
K4001	07453
K401	07450
K41	07445
K5	07442
K5S	07332

K5252	07366
K5253	07464
K6S	07333
K60K	07465
K600K	07440
K615	07467
K6625	07466
K7	07404
K7S	07334
K77	07407
K777	07412
K777K	07441
K7777	07415
K77777	07420
M1	07356
M1K	07345
M1KM	07371
M1M	07402
M10	07353
M10K	07342
M10KM	07364
M10M	07377
M100	07350
M100K	07337
M100KM	07361
M100M	07374
M17	07424
M177	07427
M1777	07432
M17777	07435
M2	07355
M2K	07344
M2KM	07370
M2M	07401
M20	07352
M20K	07341
M20KM	07363
M20M	07376
M200	07347
M200K	07336
M200KM	07360
M200M	07373
M3	07422
M37	07425
M377	07430
M3777	07433
M37777	07436
M4	07354
M4K	07343
M4KM	07365
M4M	07400
M40	07351
M40K	07340
M40KM	07362
M40M	07375

M400	07346
M400K	07335
M400KM	07357
M400M	07372
M600K	07421
M7	07423
M77	07426
M777	07431
M7777	07434
M77777	07437
NOP1	740000
NOP2	740000
NOP3	740000
PART1	00201
PCF	700202
PSA	700204
PSB	700244
PSF	700201
RCF	700102
RRB	700112
RSA	700104
RSB	700144
RSF	700101
SWHA	742030
TCF	700402
TLS	700406
TSF	700401

PART1	00201
H1	00202
H2	00205
H3	00211
H4	00214
H5	00220
H6	00223
H7	00227
H8	00232
H9	00236
H10	00242
H11	00247
H12	00252
H13	00255
H14	00260
H15	00264
H16	00270
H17	00273
H18	00276
H19	00303
H20	00307
H21	00312
H22	00317
H23	00323
H24	00330
H25	00335
H26	00341
H27	00344
H28	00350
H29	00354
H30	00360
H31	00364
H32	00370
H33	00374
H34	00377
H35	00404
H36	00410
H37	00413
H38	00417
H39	00423
H40	00432
H41	00437
H42	00445
H43	00451
H44	00455
H45	00461
H46	00464
H47	00470
H48	00473
H49	00477
H50	00502
H51	00505
H52	00511
H53	00515
H54	00522

H55	00526
H56	00532
H57	00536
H58	00542
H59	00545
H60	00551
H61	00554
H62	00560
H63	00563
H64	00567
H65	00572
H66	00575
H67	00601
H68	00604
H69	00607
H70	00612
H71	00615
H72	00620
H73	00624
H74	00627
H75	00632
H76	00636
H77	00642
H78	00646
H79	00652
H80	00656
H81	00662
H82	00666
H83	00671
H84	00674
H85	00677
H86	00703
H87	00706
H88	00711
H89	00714
H90	00717
H91	00723
H92	00727
H93	00732
H94	00735
H95	00740
H96	00744
H97	00747
H98	00751
H99	00755
H100	00757
H101	00763
H102	00765
H103	00771
H104	00773
H105	00777
H106	01003
H107	01007
H108	01013
H109	01017

H110	01023
H111	01027
H112	01033
H113	01040
H114	01045
H115	01052
H116	01057
H117	01104
H118	01131
H119	01145
H120	01161
H121	01166
H122	01170
H123	01175
H124	01177
H125	01204
H126	01206
H127	01213
H128	01215
H129	01222
H130	01224
H131	01231
H132	01233
H133	01240
H134	01242
H135	01247
H136	01251
H137	01257
H138	01261
H139	01267
H140	01271
H141	01277
H142	01301
H143	01307
H144	01335
H145	01363
H146	01400
H147	01415
H148	01421
H149	01425
H150	01431
H151	01435
H152	01441
H153	01443
H154	01447
H155	01451
H156	01454
H157	01460
H158	01463
H159	01470
H160	01474
H161	01501
H162	01506
H163	01513
H164	01520

H165	01525
H166	01531
H167	01535
H168	01541
H169	01545
H170	01551
H171	01555
H172	01561
H173	01565
H174	01570
H175	01574
H176	01600
H177	01604
H178	01610
H179	01613
H180	01616
H181	01621
H182	01625
H183	01632
H184	01636
H185	01644
H186	01651
H187	01655
H188	01660
H189	01664
H190	01670
H192	01674
H193	01701
H194	01706
H195	01713
H196	01720
H197	01723
H198	01726
H199	01731
H200	01734
H201	01741
H202	01744
H203	01747
H204	01752
H205	01755
H206	01761
H207	01765
H208	01771
H210	01775
H211	02000
H212	02015
H213	02032
H214	02034
H215	02041
H216	02046
H217	02050
H218	02065
H219	02100
H220	02103
H221	02110

H222	02113
H223	02116
H224	02121
H225	02124
H226	02126
H227	02132
H228	02135
H229	02140
H230	02143
H231	02146
H232	02151
H233	02153
H234	02162
H235	02171
H236	02201
H237	02211
H238	02222
H239	02233
H240	02245
H241	02257
H242	02272
H243	02305
H244	02321
H245	02335
H246	02352
H247	02365
H248	02367
H249	02404
H250	02406
H251	02423
H252	02425
H253	02444
H254	02446
H255	02465
H256	02467
H257	02510
H258	02512
H259	02533
H260	02535
H261	02560
H262	02562
H263	02605
H264	02607
H265	02634
H266	02636
H267	02663
H268	02665
H269	02714
H270	02716
H271	02745
H272	02747
H273	02760
H274	02762
H275	02767
H276	02775

H277A	03001
H277B	03003
H277C	03010
H277D	03015
H277E	03023
H278	03031
H279	03040
H280	03047
H281	03057
H282	03067
H283	03100
H284	03111
H285	03123
H286	03135
H287	03150
H288	03163
H289	03177
H290	03213
H291	03230
H292	03245
H293	03251
H294	03255
H295	03261
H296	03266
H297	03270
H298	03275
H299	03277
H300	03305
H301	03307
H302	03315
H303	03317
H304	03326
H305	03330
H306	03337
H307	03341
H308	03351
H309	03353
H310	03363
H311	03365
H312	03376
H313	03400
H314	03411
H315	03413
H316	03425
H317	03427
H318	03441
H319	03443
H320	03456
H321	03460
H322	03473
H323	03475
H324	03511
H325	03513
H326	03527
H327	03531

H328	03546
H329	03550
H330	03565
H331	03567
H332	03576
H333	03600
H334	03607
H335	03611
H336	03622
H337	03624
H338	03635
H339	03637
H340	03652
H341	03654
H342	03667
H343	03671
H344	03706
H345	03710
H346	03725
H347	03727
H348	03746
H349	03750
H350	03767
H351	03771
H352	04012
H353	04014
H354	04035
H355	04037
H356	04062
H357	04064
H358	04107
H359	04111
H360	04136
H361	04140
H362	04165
H363	04167
H364	04216
H365	04220
H366	04247
H367	04251
H368A	04255
H368B	04261
H368C	04265
H368D	04271
H368E	04275
H368F	04301
H368G	04305
H368H	04311
H368I	04315
H368J	04321
H369	04325
H370	04331
H371	04335
H372	04341
H373	04345

H374	04351
H375	04355
H376	04361
H377	04365
H378	04371
H379	04375
H380	04401
H381	04405
H382	04411
H383	04415
H384	04421
H385	04425
H386	04431
H387	04435
H388	04441
H389	04445
H390	04451
H391	04455
H392	04461
H393	04465
H394	04471
H395	04475
H396	04501
H397	04505
H398	04511
H399	04515
H400	04521
H401	04525
H402	04531
H403	04535
H404	04541
H405	04545
H406	04551
H407	04555
H408	04561
H409	04565
H410	04571
H411	04575
H412	04581
H413	04605
H414	04612
H415	04617
H416	04624
H417	04631
H418	04636
H419	04643
H420	04650
H421	04655
H422	04662
H423	04667
H424	04674
H425	04701
H426	04706
H427	04713
H428	04723

H429	04725
H430	04732
H431	04737
H432	04743
H433	04750
H434	04755
H435	04763
H436	04771
H437	05000
H438	05007
H439	05017
H440	05027
H441	05040
H442	05050
H443	05060
H444	05067
H445	05076
H446	05104
H447	05112
H448	05117
H449	05124
H450	05131
H451	05133
H452	05140
H453	05142
H454	05150
H455	05152
H456	05160
H457	05162
H458	05171
H459	05173
H460	05202
H461	05204
H462	05214
H463	05216
H464	05226
H465	05230
H466	05241
H467	05243
H468	05254
H469	05256
H470	05266
H471	05270
H472	05300
H473	05302
H474	05311
H475	05313
H476	05322
H477	05324
H478	05332
H479	05334
H480	05342
H481	05344
H482	05351
H483	05353

H484	05360
H485	05362
H486	05370
H487	05372
H488	05400
H489	05402
H490	05411
H491	05413
H492	05422
H493	05424
H494	05434
H495	05436
H496	05446
H497	05450
H498	05461
H499	05463
H500	05474
H501	05476
H502	05510
H503	05512
H504	05524
H505	05526
H506	05537
H507	05541
H508	05552
H509	05554
H510	05564
H511	05566
H512	05576
H513	05600
H514	05607
H515	05611
H516	05620
H517	05622
H518	05630
H519	05632
H520	05640
H521	05642
H522	05646
H523	05654
H524	05656
H525	05664
H526	05666
H527	05675
H528	05677
H529	05706
H530	05710
H531	05720
H532	05722
H533	05732
H534	05734
H535	05745
H536	05747
H537	05760
H538	05762

H539	05774
H540	05776
H541	06010
H542	06012
H543	06023
H544	06025
H545	06036
H546	06040
H547	06050
H548	06052
H549	06062
H550	06064
H551	06073
H552	06075
H553	06104
H554	06106
H555	06114
H556	06116
H557	06124
H558	06126
H559	06133
H560	06135
H561	06142
H562	06144
H563	06152
H564	06154
H565	06162
H566	06164
H567	06173
H568	06175
H569	06204
H570	06206
H571	06216
H572	06220
H573	06230
H574	06232
H575	06243
H576	06245
H577	06256
H578	06260
H579	06270
H580	06272
H581	06302
H582	06304
H583	06313
H584	06315
H585	06324
H586	06326
H587	06334
H588	06336
H589	06344
H590	06346
H591	06353
H592	06355
H593	06362

H594	06364
H595	06370
H596	06374
H597	06400
H598	06404
H599	06410
H600	06414
H601	06420
H602	06424
H603	06430
H604	06434
H605	06440
H606	06444
H607	06450
H608	06454
H609	06460
H610	06464
H611	06470
H612	06474
H613	06502
H614	06504
H615	06511
H616	06513
H617	06521
H618	06523
H619	06531
H620	06533
H621	06542
H622	06544
H623	06553
H624	06555
H625	06565
H626	06567
H627	06577
H628	06601
H629	06612
H630	06614
H631	06625
H632	06627
H633	06637
H634	06641
H635	06651
H636	06653
H637	06662
H638	06664
H639	06673
H640	06675
H641	06703
H642	06705
H643	06713
H644	06715
H645	06722
H646	06724
H647	06731
H648	06733

H649	06740
H650	06742
H651	06747
H652	06751
H653	06757
H654	06761
H655	06767
H656	06771
H657	07000
H658	07002
H659	07011
H660	07013
H661	07023
H662	07025
H663	07035
H664	07037
H665	07050
H666	07052
H667	07063
H668	07065
H669	07075
H670	07077
H671	07107
H672	07111
H673	07120
H674	07122
H675	07131
H676	07133
H677	07141
H678	07143
H679	07151
H680	07153
H681	07160
H682	07162
H683	07167
H684	07171
H685	07175
H686	07177
H687	07203
H688	07210
H689	07212
H690	07217
H691	07221
H692	07226
H693	07234
H694	07236
H695	07245
H696	07247
H697	07261
H698	07263
BEGIN	07300
K0	07303
K400K	07304
K200K	07305
K100K	07306

K40K	07307
K20K	07310
K10K	07311
K4K	07312
K2K	07313
K1K	07314
K400	07315
K200	07316
K100	07317
K40	07320
K20	07321
K10	07322
K4	07323
K2	07324
K1	07325
K1S	07326
K2S	07327
K3S	07330
K4S	07331
K5S	07332
K6S	07333
K7S	07334
M400K	07335
M200K	07336
M100K	07337
M40K	07340
M20K	07341
M10K	07342
M4K	07343
M2K	07344
M1K	07345
M400	07346
M200	07347
M100	07350
M40	07351
M20	07352
M10	07353
M4	07354
M2	07355
M1	07356
M400KM	07357
M200KM	07360
M100KM	07361
M40KM	07362
M20KM	07363
M10KM	07364
M4KM	07365
K5252	07366
K2525	07367
M2KM	07370
M1KM	07371
M400M	07372
M200M	07373
M100M	07374
M40M	07375

M20M	07376
M10M	07377
M4M	07400
M2M	07401
M1M	07402
K3	07403
K7	07404
K17	07405
K37	07406
K77	07407
K177	07410
K377	07411
K777	07412
K1777	07413
K3777	07414
K7777	07415
K17777	07416
K37777	07417
K77777	07420
M600K	07421
M3	07422
M7	07423
M17	07424
M37	07425
M77	07426
M177	07427
M377	07430
M777	07431
M1777	07432
M3777	07433
M7777	07434
M17777	07435
M37777	07436
M77777	07437
K600K	07440
K777K	07441
K5	07442
K11	07443
K21	07444
K41	07445
K101	07446
K201	07447
K401	07450
K1001	07451
K2001	07452
K4001	07453
K10001	07454
KHALT	07455
KCAL0	07456
K2021	07457
K2120	07460
KCALE	07461
KSKP	07462
KCLA	07463
K5253	07464

K60K	07465
K6625	07466
K615	07467
COUNT	07470
CLSF	700001
CLOF	700004
CLON	700044
RSF	700101
RCF	700102
RSA	700104
RRB	700112
RSB	700144
PSF	700201
PCF	700202
PSA	700204
PSB	700244
KSF	700301
KRB	700312
TSF	700401
TCF	700402
TLS	700406
NOP1	740000
NOP2	740000
NOP3	740000
IAC	740030
HALT	740040
SWHA	742030

.TITLE 15LIT1A
 /PDP-15 INST. TEST
 /COPYRIGHT 1969, 1970, DIGITAL EQUIPMENT CORP., MAYNARD, MASS.
 /(PART 1A)- TAPE 1

00200		.FULL	
00200	604446	.LOC 200	
00201	754000	JMP BEGIN	/TEST TAD K0, AC=0, L=0
00202	344451	PART1A CLA:CLL	/AC=0, L=0
00203	740200	TAD K0	/AC=0
00204	740040	SZA	
00205	741400	H699 HALT	/ERROR; AC NOT 0, TAD K0 FAILED
00206	740040	SZL	
		H700 HALT	/ERROR; LINK NOT 0, TAD K0 FAILED
			/TEST TAD K0, AC=0, L=1
00207	754002	CLA:CLL:CML	/AC=0, L=1
00210	344451	TAD K0	/AC=0
00211	740400	SNL	
00212	740040	H701 HALT	/ERROR; LINK NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=400K
00213	750001	CLA:CMA	/AC=ONES
00214	204452	LAC K400K	/AC=400K
00215	344451	TAD K0	/0
00216	244452	XOR K400K	/0
00217	740200	SZA	
00220	740040	H702 HALT	/ERROR; BIT 0 NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=200K
00221	750001	CLA:CMA	/AC=ONES
00222	204453	LAC K200K	/AC=200K
00223	344451	TAD K0	/0
00224	244453	XOR K200K	/0
00225	740200	SZA	
00226	740040	H703 HALT	/ERROR; BIT 1 NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=100K
00227	750001	CLA:CMA	/AC = ONES
00230	204454	LAC K100K	/AC=100K
00231	344451	TAD K0	/0
00232	244454	XOR K100K	/0
00233	740200	SZA	
00234	740040	H704 HALT	/ERROR; BIT 2 NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=40K
00235	750001	CLA:CMA	/AC=ONES
00236	204455	LAC K40K	/AC=40K
00237	344451	TAD K0	/0
00240	244455	XOR K40K	/0
00241	740200	SZA	
00242	740040	H705 HALT	/ERROR; BIT 3 NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=20K
00243	750001	CLA:CMA	/AC=ONES
00244	204456	LAC K20K	/AC=20K
00245	344451	TAD K0	/0
00246	244456	XOR K20K	/0
00247	740200	SZA	
00250	740040	H706 HALT	/ERROR; BIT 4 NOT A 1, TAD K0 FAILED
			/TEST TAD K0, AC=10K
00251	750001	CLA:CMA	/AC=ONES

00252	204457		LAC K10K	/AC=10K
00253	344451		TAD K0	/0
00254	244457		XOR K10K	/0
00255	740200		SZA	
00256	740040	H707	HALT	/ERROR: BIT 5 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=4K
00257	750001		CLA:CMA	/AC=ONES
00260	204460		LAC K4K	/AC=4K
00261	344451		TAD K0	/0
00262	244460		XOR K4K	/0
00263	740200		SZA	
00264	740040	H708	HALT	/ERROR: BIT 6 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=2K
00265	750001		CLA:CMA	/AC=ONES
00266	204461		LAC K2K	/AC=2K
00267	344451		TAD K0	/0
00270	244461		XOR K2K	/0
00271	740200		SZA	
00272	740040	H709	HALT	/ERROR: BIT 7 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=1K
00273	750001		CLA:CMA	/AC=ONES
00274	204462		LAC K1K	/AC=1K
00275	344451		TAD K0	/0
00276	244462		XOR K1K	/0
00277	740200		SZA	
00300	740040	H710	HALT	/ERROR: BIT 8 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=400
00301	750001		CLA:CMA	/AC=ONES
00302	204463		LAC K400	/AC=400
00303	344451		TAD K0	/0
00304	244463		XOR K400	/0
00305	740200		SZA	
00306	740040	H711	HALT	/ERROR: BIT 9 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=200
00307	750001		CLA:CMA	/AC=ONES
00310	204464		LAC K200	/AC=200
00311	344451		TAD K0	/0
00312	244464		XOR K200	/0
00313	740200		SZA	
00314	740040	H712	HALT	/ERROR: BIT 10 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=100
00315	750001		CLA:CMA	/AC=ONES
00316	204465		LAC K100	/AC=100
00317	344451		TAD K0	/0
00320	244465		XOR K100	/0
00321	740200		SZA	
00322	740040	H713	HALT	/ERROR: BIT 11 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=40
00323	750001		CLA:CMA	/AC=ONES
00324	204466		LAC K40	/AC=40
00325	344451		TAD K0	/0
00326	244466		XOR K40	/0
00327	740200		SZA	
00330	740040	H714	HALT	/ERROR: BIT 12 NOT A 1, TAD K0 FAILED
				/TEST TAD K0, AC=20

00331	750001		CLA!CMA	/AC=ONES
00332	204467		LAC K20	/AC=20
00333	344451		TAD K0	/0
00334	244467		XOR K20	/0
00335	740200		SZA	
00336	740040	H715	HALT	/ERROR; BIT 13 NOT A 1, TAD K0 FAILED /TEST TAD K0, AC=10
00337	750001		CLA!CMA	/AC=ONES
00340	204470		LAC K10	/AC=10
00341	344451		TAD K0	/0
00342	244470		XOR K10	/0
00343	740200		SZA	
00344	740040	H716	HALT	/ERROR; BIT 14 NOT A 1, TAD K0 FAILED /TEST TAD K0, AC=4
00345	750001		CLA!CMA	/AC=ONES
00346	204471		LAC K4	/AC=4
00347	344451		TAD K0	/AC=0
00350	244471		XOR K4	/0
00351	740200		SZA	
00352	740040	H717	HALT	/ERROR; BIT 15 NOT A 1, TAD K0 FAILED /TEST TAD K0, AC=2
00353	750001		CLA!CMA	/AC=ONES
00354	204472		LAC K2	/AC=2
00355	344451		TAD K0	/0
00356	244472		XOR K2	/0
00357	740200		SZA	
00360	740040	H718	HALT	/ERROR; BIT 16 NOT A 1, TAD K0 FAILED /TEST TAD K0, AC=1
00361	750001		CLA!CMA	/AC=ONES
00362	204473		LAC K1	/AC=1
00363	344451		TAD K0	/0
00364	244473		XOR K1	/0
00365	740200		SZA	
00366	740040	H719	HALT	/ERROR; BIT 17 NOT A 1, TAD K0 FAILED /INSTRUCTION TEST TAPE 2
00367	750000		CLA	/TEST TAD K400K, AC=0
00370	344452		TAD K400K	/AC=0
00371	244452		XOR K400K	/AC=400K
00372	740200		SZA	/0
00373	740040	H720	HALT	/ERROR; BIT 0 NOT A 1, TAD K400K FAILED /TEST TAD K200K, AC=0
00374	750000		CLA	/AC=0
00375	344453		TAD K200K	/AC=200K
00376	244453		XOR K200K	/0
00377	740200		SZA	
00400	740040	H721	HALT	/ERROR; BIT 1 NOT A 1, TAD K200 K FAILED /TEST TAD K100K, AC=0
00401	750000		CLA	/AC=0
00402	344454		TAD K100K	/AC=100K
00403	244454		XOR K100K	/0
00404	740200		SZA	
00405	740040	H722	HALT	/ERROR; BIT 2 NOT A 1, TAD K100K FAILED /TEST TAD K40K, AC=0
00406	750000		CLA	/AC=0

PAGE	4	15LIT _A	15LIT _{1A}	.
00407	344455		TAD K40K	/AC=40K
00410	244455		XOR K40K	/0
00411	740200		SZA	
00412	740040	H723	HALT	/ERROR; BIT 3 NOT A 1, TAD K40K FAILED /TEST TAD K20K, AC=0
00413	750000		CLA	/AC=0
00414	344456		TAD K20K	/AC=20K
00415	244456		XOR K20K	/0
00416	740200		SZA	
00417	740040	H724	HALT	/ERROR; BIT 4 NOT A 1, TAD K20K FAILED /TEST TAD K10K, AC=0
00420	750000		CLA	/AC=0
00421	344457		TAD K10K	/AC=10K
00422	244457		XOR K10K	/0
00423	740200		SZA	
00424	740040	H725	HALT	/ERROR; BIT 5 NOT A 1, TAD K10K FAILED /TEST TAD K4K, AC=0
00425	750000		CLA	/AC=0
00426	344460		TAD K4K	/AC=4K
00427	244460		XOR K4K	/0
00430	740200		SZA	
00431	740040	H726	HALT	/ERROR; BIT 6 NOT A 1, TAD K4K FAILED /TEST TAD K2K, AC=0
00432	750000		CLA	/AC=0
00433	344461		TAD K2K	/AC=2K
00434	244461		XOR K2K	/0
00435	740200		SZA	
00436	740040	H727	HALT	/ERROR; BIT 7 NOT A 1, TAD K2K FAILED /TEST TAD K1K, AC=0
00437	750000		CLA	/AC=0
00440	344462		TAD K1K	/AC=1K
00441	244462		XOR K1K	/0
00442	740200		SZA	
00443	740040	H728	HALT	/ERROR; BIT 8 NOT A 1, TAD K1K FAILED /TEST TAD K400, AC=0
00444	750000		CLA	/AC=0
00445	344463		TAD K400	/AC=400
00446	244463		XOR K400	/0
00447	740200		SZA	
00450	740040	H729	HALT	/ERROR; BIT 9 NOT A 1, TAD K400 FAILED /TEST TAD K200, AC=0
00451	750000		CLA	/AC=0
00452	344464		TAD K200	/AC=200
00453	244464		XOR K200	/0
00454	740200		SZA	
00455	740040	H730	HALT	/ERROR; BIT 10 NOT A 1, TAD K200 FAILED /TEST TAD K100, AC=0
00456	750000		CLA	/AC=0
00457	344465		TAD K100	/AC=100
00460	244465		XOR K100	/0
00461	740200		SZA	
00462	740040	H731	HALT	/ERROR; BIT 11 NOT A 1, TAD K100 FAILED /TEST TAD K40, AC=0
00463	750000		CLA	/AC=0
00464	344466		TAD K40	/AC=40

00465	244466		XOR K40	/0
00466	740200		SZA	
00467	740040	H732	HALT	/ERROR; BIT 12 NOT A 1, TAD K40 FAILED
				/TEST TAD K20, AC=0
00470	750000		CLA	/AC=0
00471	344467		TAD K20	/AC=20
00472	244467		XOR K20	/0
00473	740200		SZA	
00474	740040	H733	HALT	/ERROR; BIT 13 NOT A 1, TAD K20 FAILED
				/TEST TAD K10, AC=0
00475	750000		CLA	/AC=0
00476	344470		TAD K10	/AC=10
00477	244470		XOR K10	/0
00500	740200		SZA	
00501	740040	H734	HALT	/ERROR; BIT 14 NOT A 1, TAD K10 FAILED
				/TEST TAD K4, AC=0
00502	750000		CLA	/AC=0
00503	344471		TAD K4	/AC=4
00504	244471		XOR K4	/0
00505	740200		SZA	
00506	740040	H735	HALT	/ERROR; BIT 15 NOT A 1, TAD K4 FAILED
				/TEST TAD K2, AC=0
00507	750000		CLA	/AC=0
00510	344472		TAD K2	/AC=2
00511	244472		XOR K2	/0
00512	740200		SZA	
00513	740040	H736	HALT	/ERROR; BIT 16 NOT A 1, TAD K2 FAILED
				/TEST TAD K1, AC=0
00514	750000		CLA	/AC=0
00515	344473		TAD K1	/AC=1
00516	244473		XOR K1	/0
00517	740200		SZA	
00520	740040	H737	HALT	/ERROR; BIT 17 NOT A 1, TAD K1 FAILED
				/TEST TAD M400K, AC=0
00521	750000		CLA	/AC=0
00522	344503		TAD M400K	/AC=377777
00523	244503		XOR M400K	/0
00524	740200		SZA	
00525	740040	H738	HALT	/ERROR; TAD M400K FAILED
				/TEST TAD M200K, AC=0
00526	750000		CLA	/AC=0
00527	344504		TAD M200K	/AC=577777
00530	244504		XOR M200K	/0
00531	740200		SZA	
00532	740040	H739	HALT	/ERROR; TAD M200K FAILED
				/TEST TAD M100K, AC=0
00533	750000		CLA	/AC=0
00534	344505		TAD M100K	/AC=677777
00535	244505		XOR M100K	/0
00536	740200		SZA	
00537	740040	H740	HALT	/ERROR; TAD M100K FAILED
				/TEST TAD M40K, AC=0
00540	750000		CLA	/AC=0
00541	344506		TAD M40K	/AC=737777
00542	244506		XOR M40K	/0

00543	740200		SZA	
00544	740040	H741	HALT	/ERROR; TAD M40K FAILED /TEST TAD M20K, AC=0 /AC=0
00545	750000		CLA	
00546	344507		TAD M20K	/AC=759777
00547	244507		XOR M20K	/0
00550	740200		SZA	
00551	740040	H742	HALT	/ERROR; TAD M20K FAILED /TEST TAD M10K, AC=0 /AC=0
00552	750000		CLA	
00553	344510		TAD M10K	/AC=767777
00554	244510		XOR M10K	/0
00555	740200		SZA	
00556	740040	H743	HALT	/ERROR; TAD M10K FAILED /TEST TAD M4K, AC=0 /AC=0
00557	750000		CLA	
00560	344511		TAD M4K	/AC=773777
00561	244511		XOR M4K	/0
00562	740200		SZA	
00563	740040	H744	HALT	/ERROR; TAD M4K FAILED /TEST TAD M2K, AC=0 /AC=0
00564	750000		CLA	
00565	344512		TAD M2K	/AC=775777
00566	244512		XOR M2K	/0
00567	740200		SZA	
00570	740040	H745	HALT	/ERROR; TAD M2K FAILED /TEST TAD M1K, AC=0 /AC=0
00571	750000		CLA	
00572	344513		TAD M1K	/AC=776777
00573	244513		XOR M1K	/0
00574	740200		SZA	
00575	740040	H746	HALT	/ERROR; TAD M1K FAILED /TEST TAD M400, AC=0 /AC=0
00576	750000		CLA	
00577	344514		TAD M400	/AC=777377
00600	244514		XOR M400	/0
00601	740200		SZA	
00602	740040	H747	HALT	/ERROR; TAD M400 FAILED /TEST TAD M200, AC=0 /AC=0
00603	750000		CLA	
00604	344515		TAD M200	/AC=777577
00605	244515		XOR M200	/0
00606	740200		SZA	
00607	740040	H748	HALT	/ERROR; TAD M200 FAILED /TEST TAD M100, AC=0 /AC=0
00610	750000		CLA	
00611	344516		TAD M100	/AC=777677
00612	244516		XOR M100	/0
00613	740200		SZA	
00614	740040	H749	HALT	/ERROR; TAD M100 FAILED /TEST TAD M40, AC=0 /AC=0
00615	750000		CLA	
00616	344517		TAD M40	/AC=777737
00617	244517		XOR M40	/0
00620	740200		SZA	

PAGE	7	15LITA	15LIT1A	
00621	740040	H750	HALT	/ERROR; TAD M40 FAILED /TEST M20, AC=0
00622	750000		CLA	/AC=0
00623	344520		TAD M20	/AC=777757
00624	244520		XOR M20	/0
00625	740200		SZA	
00626	740040	H751	HALT	/ERROR; TAD M20 FAILED /TEST TAD M10, AC=0
00627	750000		CLA	/AC=0
00630	344521		TAD M10	/AC=777767
00631	244521		XOR M10	/0
00632	740200		SZA	
00633	740040	H752	HALT	/ERROR; TAD M10 FAILED /TEST TAD M4, AC=0
00634	750000		CLA	/AC=0
00635	344522		TAD M4	/AC=777773
00636	244522		XOR M4	/0
00637	740200		SZA	
00640	740040	H753	HALT	/ERROR; TAD M4 FAILED /TEST TAD M2, AC=0
00641	750000		CLA	/AC=0
00642	344523		TAD M2	/AC=777775
00643	244523		XOR M2	/0
00644	740200		SZA	
00645	740040	H754	HALT	/ERROR; TAD M2 FAILED /TEST TAD M1, AC=0
00646	750000		CLA	/AC=0
00647	344524		TAD M1	/AC=777776
00650	244524		XOR M1	/0
00651	740200		SZA	
00652	740040	H755	HALT	/ERROR; TAD M1 FAILED /TEST TAD M400K, TAD K400K, L=0
00653	754000		CLA!CLL	/AC=0, L=0
00654	344503		TAD M400K	/AC=377777
00655	344452		TAD K400K	/400000
00656	740001		CMA	/AC=0
00657	740200		SZA	
00660	740040	H756	HALT	/ERROR; INCLUSIVE TAD FAILURE
00661	741400		SZL	
00662	740040	H757	HALT	/ERROR; LINK NOT A 0 /TEST TAD M200K, TAD K200K, L=0
00663	754000		CLA!CLL	/AC=0, L=0
00664	344504		TAD M200K	/AC=577777
00665	344453		TAD K200K	/200000
00666	740001		CMA	/AC=0
00667	740200		SZA	
00670	740040	H758	HALT	/ERROR; INCLUSIVE TAD FAILURE
00671	741400		SZL	
00672	740040	H759	HALT	/ERROR; LINK NOT A 0 /TEST TAD M100K, TAD K100K, L=0
00673	754000		CLA!CLL	/AC=0, L=0
00674	344505		TAD M100K	/AC=677777
00675	344454		TAD K100K	/100000
00676	740001		CMA	/AC=0
00677	740200		SZA	

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00700	740040	H760	HALT	/ERROR: INCLUSIVE TAD FAILURE
00701	741400		SZL	
00702	740040	H761	HALT	/ERROR: LINK NOT A 0
00703	754000		CLA:CLL	/TEST TAD M40K, TAD K40K, L=0
00704	344506		TAD M40K	/AC=0, L=0
00705	344455		TAD K40K	/AC=737777
00706	740001		CMA	/040000
00707	740200		SZA	/AC=0
00710	740040	H762	HALT	/ERROR: INCLUSIVE TAD FAILURE
00711	741400		SZL	
00712	740040	H763	HALT	/ERROR: LINK NOT A 0
00713	754000		CLA:CLL	/TEST TAD M20K, TAD K20K, L=0
00714	344507		TAD M20K	/AC=0, L=0
00715	344456		TAD K20K	/AC=757777
00716	740001		CMA	/020000
00717	740200		SZA	/AC=0
00720	740040	H764	HALT	/ERROR: INCLUSIVE TAD FAILURE
00721	741400		SZL	
00722	740040	H765	HALT	/ERROR: LINK NOT A 0
00723	754000		CLA:CLL	/TEST TAD M10K, TAD K10K, L=0
00724	344510		TAD M10K	/AC=0, L=0
00725	344457		TAD K10K	/AC=767777
00726	740001		CMA	/010000
00727	740200		SZA	/AC=0
00730	740040	H766	HALT	/ERROR: INCLUSIVE TAD FAILURE
00731	741400		SZL	
00732	740040	H767	HALT	/ERROR: LINK NOT A 0
00733	754000		CLA:CLL	/TEST TAD M4K, TAD K4K, L=0
00734	344511		TAD M4K	/AC=0, L=0
00735	344460		TAD K4K	/AC=773777
00736	740001		CMA	/004000
00737	740200		SZA	/AC=0
00740	740040	H768	HALT	/ERROR: INCLUSIVE TAD FAILURE
00741	741400		SZL	
00742	740040	H769	HALT	/ERROR: LINK NOT A 0
00743	754000		CLA:CLL	/TEST TAD M2K, TAD K2K, L=0
00744	344512		TAD M2K	/AC=0, L=0
00745	344461		TAD K2K	/AC=775777
00746	740001		CMA	/002000
00747	740200		SZA	/AC=0
00750	740040	H770	HALT	/ERROR: INCLUSIVE TAD FAILURE
00751	741400		SZL	
00752	740040	H771	HALT	/ERROR: LINK NOT A 0
00753	754000		CLA:CLL	/TEST TAD M1K, TAD K1K, L=0
00754	344513		TAD M1K	/AC=0, L=0
00755	344462		TAD K1K	/AC=776777
00756	740001		CMA	/001000
00757	740200		SZA	/AC=0
00760	740040	H772	HALT	/ERROR: INCLUSIVE TAD FAILURE

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00761	741400			SZL	
00762	740040	H773		HALT	/ERROR; LINK NOT A 0 /TEST TAD M400, TAD K400, L=0 /AC=0, L=0
00763	754000			CLA:CLL	
00764	344514			TAD M400	/AC=777377
00765	344463			TAD K400	/000400
00766	740001			CMA	/AC=0
00767	740200			SZA	
00770	740040	H774		HALT	/ERROR; INCLUSIVE TAD FAILURE
00771	741400			SZL	
00772	740040	H775		HALT	/ERROR; LINK NOT A 0 /TEST TAD M200, TAD K200, L=0 /AC=0, L=0
00773	754000			CLA:CLL	
00774	344515			TAD M200	/AC=777577
00775	344464			TAD K200	/000200
00776	740001			CMA	/AC=0
00777	740200			SZA	
01000	740040	H776		HALT	/ERROR; INCLUSIVE TAD FAILURE
01001	741400			SZL	
01002	740040	H777		HALT	/ERROR; LINK NOT A 0 /TEST TAD M100, TAD K100, L=0 /AC=0, L=0
01003	754000			CLA:CLL	
01004	344516			TAD M100	/AC=777677
01005	344465			TAD K100	/000100
01006	740001			CMA	/AC=0
01007	740200			SZA	
01010	740040	H778		HALT	/ERROR; INCLUSIVE TAD FAILURE
01011	741400			SZL	
01012	740040	H779		HALT	/ERROR; LINK NOT A 0 /TEST TAD M40, TAD K40, L=0 /AC=0, L=0
01013	754000			CLA:CLL	
01014	344517			TAD M40	/AC=777737
01015	344466			TAD K40	/000040
01016	740001			CMA	/AC=0
01017	740200			SZA	
01020	740040	H780		HALT	/ERROR; INCLUSIVE TAD FAILURE
01021	741400			SZL	
01022	740040	H781		HALT	/ERROR; LINK NOT A 0 /TEST TAD M20, TAD K20, L=0 /AC=0, L=0
01023	754000			CLA:CLL	
01024	344520			TAD M20	/AC=777757
01025	344467			TAD K20	/000020
01026	740001			CMA	/AC=0
01027	740200			SZA	
01030	740040	H782		HALT	/ERROR; INCLUSIVE TAD FAILURE
01031	741400			SZL	
01032	740040	H783		HALT	/ERROR; LINK NOT A 0 /TEST TAD M10, TAD K10, L=0 /AC=0, L=0
01033	754000			CLA:CLL	
01034	344521			TAD M10	/AC=777767
01035	344470			TAD K10	/000010
01036	740001			CMA	/AC=0
01037	740200			SZA	
01040	740040	H784		HALT	/ERROR; INCLUSIVE TAD FAILURE
01041	741400			SZL	

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01042	740040	H785	HALT	/ERROR; LINK NOT A 0	
01043	754000		CLA:CLL	/TEST TAD M4, TAD K4, L=0	
01044	344522		TAD M4	/AC=0, L=0	
01045	344471		TAD K4	/AC=777773	
01046	740001		CMA	/000004	
01047	740200		SZA	/AC=0	
01050	740040	H786	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01051	741400		SZL		
01052	740040	H787	HALT	/ERROR; LINK NOT A 0	
01053	754000		CLA:CLL	/TEST TAD M2, TAD K2, L=0	
01054	344523		TAD M2	/AC=0, L=0	
01055	344472		TAD K2	/AC=777775	
01056	740001		CMA	/000002	
01057	740200		SZA	/AC=0	
01060	740040	H788	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01061	741400		SZL		
01062	740040	H789	HALT	/ERROR; LINK NOT A 0	
01063	754000		CLA:CLL	/TEST TAD M1, TAD K1, L=0	
01064	344524		TAD M1	/AC=0, L=0	
01065	344473		TAD K1	/AC=777776	
01066	740001		CMA	/000001	
01067	740200		SZA	/AC=0	
01070	740040	H790	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01071	741400		SZL		
01072	740040	H791	HALT	/ERROR; LINK NOT A 0	
01073	754002		CLA:CLL:CML	/TEST TAD K400K, TAD M400K, L=1	
01074	344452		TAD K400K	/AC=0, L=1	
01075	344503		TAD M400K	/AC=400000	
01076	740001		CMA	/377777	
01077	740200		SZA	/AC=0	
01100	740040	H792	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01101	740400		SNL		
01102	740040	H793	HALT	/ERROR; LINK NOT A 1	
01103	754002		CLA:CLL:CML	/TEST TAD K200K, L=1	
01104	344453		TAD K200K	/AC=0, L=1	
01105	344504		TAD M200K	/AC=200000	
01106	740001		CMA	/577777	
01107	740200		SZA	/AC=0	
01110	740040	H794	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01111	740400		SNL		
01112	740040	H795	HALT	/ERROR; LINK NOT A 1	
01113	754002		CLA:CLL:CML	/TEST TAD K100K, TAD M100K, L=1	
01114	344454		TAD K100K	/AC=0, L=1	
01115	344505		TAD M100K	/AC=100000	
01116	740001		CMA	/677777	
01117	740200		SZA	/AC=0	
01120	740040	H796	HALT	/ERROR; INCLUSIVE TAD FAILURE	
01121	740400		SNL		
01122	740040	H797	HALT	/ERROR; LINK NOT A 1	

01123	754002		CLA:CLL:CML	/TEST TAD K40K, TAD M40K, L=1
01124	344455		TAD K40K	/AC=0, L=1
01125	344506		TAD M40K	/AC=040000
01126	740001		CMA	/737777
01127	740200		SZA	/AC=0
01130	740040	H798	HALT	/ERROR: INCLUSIVE TAD FAILURE
01131	740400		SNL	
01132	740040	H799	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K20K, TAD M20K, L=1
01133	754002		CLA:CLL:CML	/AC=0, L=1
01134	344456		TAD K20K	/AC=020000
01135	344507		TAD M20K	/750000
01136	740001		CMA	/AC=0
01137	740200		SZA	
01140	740040	H800	HALT	/ERROR: INCLUSIVE TAD FAILURE
01141	740400		SNL	
01142	740040	H801	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K10K, TAD M10K, L=1
01143	754002		CLA:CLL:CML	/AC=0, L=1
01144	344457		TAD K10K	/AC=010000
01145	344510		TAD M10K	/767777
01146	740001		CMA	/AC=0
01147	740200		SZA	
01150	740040	H802	HALT	/ERROR: INCLUSIVE TAD FAILURE
01151	740400		SNL	
01152	740040	H803	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K4K, TAD M4K, L=1
01153	754002		CLA:CLL:CML	/AC=0, L=1
01154	344460		TAD K4K	/AC=004000
01155	344511		TAD M4K	/773777
01156	740001		CMA	/AC=0
01157	740200		SZA	
01160	740040	H804	HALT	/ERROR: INCLUSIVE TAD FAILURE
01161	740400		SNL	
01162	740040	H805	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K2K, TAD M2K, L=1
01163	754002		CLA:CLL:CML	/AC=0, L=1
01164	344461		TAD K2K	/AC=002000
01165	344512		TAD M2K	/775777
01166	740001		CMA	/AC=0
01167	740200		SZA	
01170	740040	H806	HALT	/ERROR: INCLUSIVE TAD FAILURE
01171	740400		SNL	
01172	740040	H807	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K1K, TAD M1K, L=1
01173	754002		CLA:CLL:CML	/AC=0, L=1
01174	344462		TAD K1K	/AC=001000
01175	344513		TAD M1K	/776777
01176	740001		CMA	/AC=0
01177	740200		SZA	
01200	740040	H808	HALT	/ERROR: INCLUSIVE TAD FAILURE
01201	740400		SNL	
01202	740040	H809	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K400, TAD M400, L=1

01203	754002		CLA:CLL:CML	/AC=0, L=1
01204	344463		TAD K400	/AC=000400
01205	344514		TAD M400	/777377
01206	740001		CMA	/AC=0
01207	740200		SZA	
01210	740040	H810	HALT	/ERROR: INCLUSIVE TAD FAILURE
01211	740400		SNL	
01212	740040	H811	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K200, TAD M200, L=1
01213	754002		CLA:CLL:CML	/AC=0, L=1
01214	344464		TAD K200	/AC=000200
01215	344515		TAD M200	/777577
01216	740001		CMA	/AC=0
01217	740200		SZA	
01220	740040	H812	HALT	/ERROR: INCLUSIVE TAD FAILURE
01221	740400		SNL	
01222	740040	H813	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K100, TAD M100, L=1
01223	754002		CLA:CLL:CML	/AC=0, L=1
01224	344465		TAD K100	/AC=000100
01225	344516		TAD M100	/777677
01226	740001		CMA	/AC=0
01227	740200		SZA	
01230	740040	H814	HALT	/ERROR: INCLUSIVE TAD FAILURE
01231	740400		SNL	
01232	740040	H815	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K40, TAD M40, L=1
01233	754002		CLA:CLL:CML	/AC=0, L=1
01234	344466		TAD K40	/AC=000040
01235	344517		TAD M40	/777737
01236	740001		CMA	/AC=0
01237	740200		SZA	
01240	740040	H816	HALT	/ERROR: INCLUSIVE TAD FAILURE
01241	740400		SNL	
01242	740040	H817	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K20, TAD M20, L=1
01243	754002		CLA:CLL:CML	/AC=0, L=1
01244	344467		TAD K20	/AC=000020
01245	344520		TAD M20	/777757
01246	740001		CMA	/AC=0
01247	740200		SZA	
01250	740040	H818	HALT	/ERROR: INCLUSIVE TAD FAILURE
01251	740400		SNL	
01252	740040	H819	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K10, TAD M10, L=1
01253	754002		CLA:CLL:CML	/AC=0, L=1
01254	344470		TAD K10	/AC=000010
01255	344521		TAD M10	/777767
01256	740001		CMA	/AC=0
01257	740200		SZA	
01260	740040	H820	HALT	/ERROR: INCLUSIVE TAD FAILURE
01261	740400		SNL	
01262	740040	H821	HALT	/ERROR: LINK NOT A 1
				/TEST TAD K4, TAD M4, L=1
01263	754002		CLA:CLL:CML	/AC=0, L=1

01264	344471		TAD K4	/AC=000004
01265	344522		TAD M4	/777773
01266	740001		CMA	/AC=0
01267	740200		SZA	
01270	740040	H822	HALT	/ERROR; INCLUSIVE TAD FAILURE
01271	740400		SNL	
01272	740040	H823	HALT	/ERROR; LINK NOT A 1
		/INST. TEST TAPE 3		
				/TEST TAD K2, TAD M2, L=1
01273	754002		CLA:CLL:CML	/AC=0, L=1
01274	344472		TAD K2	/AC=000002
01275	344523		TAD M2	/777775
01276	740001		CMA	/AC=0
01277	740200		SZA	
01300	740040	H824	HALT	/ERROR; INCLUSIVE TAD FAILURE
01301	740400		SNL	
01302	740040	H825	HALT	/ERROR; LINK NOT A 1
				/TEST TAD K1, TAD M1, L=1
				/AC=0, L=1
01303	754002		CLA:CLL:CML	/AC=000001
01304	344473		TAD K1	/777776
01305	344524		TAD M1	/AC=0
01306	740001		CMA	
01307	740200		SZA	
01310	740040	H826	HALT	/ERROR; INVLSIVE TAD FAILURE
01311	740400		SNL	
01312	740040	H827	HALT	/ERROR; LINK NOT A 1
				/TEST TAD K1S, TAD K6S
				/AC=0
01313	750000		CLA	/AC=111111
01314	344474		TAD K1S	/666666
01315	344501		TAD K6S	/AC=0
01316	740001		CMA	
01317	740200		SZA	
01320	740040	H828	HALT	/ERROR; INVLSIVE TAD FAILURE
				/TEST TAD K2S, TAD K5S
				/AC=0
01321	750000		CLA	/AC=222222
01322	344475		TAD K2S	/555555
01323	344500		TAD K5S	/AC=0
01324	740001		CMA	
01325	740200		SZA	
01326	740040	H829	HALT	/ERROR; INCLUSIVE TAD FAILURE
				/TEST TAD K3S, TAD K4S
				/AC=0
01327	750000		CLA	/AC=333333
01330	344476		TAD K3S	/444444
01331	344477		TAD K4S	/AC=0
01332	740001		CMA	
01333	740200		SZA	
01334	740040	H830	HALT	/ERROR; INCLUSIVE TAD FAILURE
				/TEST TAD K4S, TAD K3S
				/AC=0
01335	750000		CLA	/AC=444444
01336	344477		TAD K4S	/AC=333333
01337	344476		TAD K3S	/AC=0
01340	740001		CMA	
01341	740200		SZA	
01342	740040	H831	HALT	/ERROR; INCLUSIVE TAD FAILURE
				/TEST TAD K5S, TAD K2S

01343	750000		CLA	/AC=0
01344	344500		TAD K5S	/AC=555555
01345	344475		TAD K2S	/AC=222222
01346	740001		CMA	/AC=0
01347	740200		SZA	
01350	740040	H832	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K6S, TAD K1S
01351	750000		CLA	/AC=0
01352	344501		TAD K6S	/AC=666666
01353	344474		TAD K1S	/111111
01354	740001		CMA	/AC=0
01355	740200		SZA	
01356	740040	H833	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K7S, TAD K0
01357	750000		CLA	/AC=0
01360	344502		TAD K7S	/AC=777777
01361	344451		TAD K0	/AC=000000
01362	740001		CMA	/AC=0
01363	740200		SZA	
01364	740040	H834	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K0, TAD K7S
01365	750000		CLA	/AC=0
01366	344451		TAD K0	/AC=000000
01367	344502		TAD K7S	/777777
01370	740001		CMA	/AC=0
01371	740200		SZA	
01372	740040	H835	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K2525, TAD K5252
01373	750000		CLA	/AC=0
01374	344535		TAD K2525	/AC=252525
01375	344534		TAD K5252	/525252
01376	740001		CMA	/AC=0
01377	740200		SZA	
01400	740040	H836	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K5252, TAD K2525
01401	750000		CLA	/AC=0
01402	344534		TAD K5252	/AC=525252
01403	344535		TAD K2525	/252525
01404	740001		CMA	/AC=0
01405	740200		SZA	
01406	740040	H837	HALT	/ERROR; INCLUSIVE TAD FAILURE /TEST TAD K400K, AC=400K, L=0
01407	744000		CLL	/L=0
01410	204452		LAC K400K	/AC=400K
01411	344452		TAD K400K	/400K
01412	740200		SZA	
01413	740040	H838	HALT	/ERROR; BIT 0 NOT A 0, SUM FAILED
01414	740400		SNL	
01415	740040	H839	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K400K, AC=400K, L=1
01416	744002		CLL:CML	/L=1
01417	204452		LAC K400K	/AC=400K
01420	344452		TAD K400K	/400K
01421	741400		SZL	
01422	740040	H840	HALT	/ERROR; BIT 0 CARRY FAILED TO COMPLEMENT LINK

01423	204453	LAC K200K	/TEST TAD K200K, AC=K200K
01424	344453	TAD K200K	/AC=200K
01425	244452	XOR K400K	/AC=400K
01426	740200	SZA	/0
01427	740040	H841 HALT	/ERROR; BIT 1 SUM OR CARRY FAILED
			/TEST TAD K100K, AC=K100K
01430	204454	LAC K100K	/AC=100K
01431	344454	TAD K100K	/AC=200K
01432	244453	XOR K200K	/0
01433	740200	SZA	
01434	740040	H842 HALT	/ERROR; BIT 2 SUM OR CARRY FAILED
			/TEST TAD K40K, AC=K40K
01435	204455	LAC K40K	/AC=40K
01436	344455	TAD K40K	/AC=100K
01437	244454	XOR K100K	/0
01440	740200	SZA	
01441	740040	H843 HALT	/ERROR; BIT 3 SUM OR CARRY FAILED
			/TEST TAD K20K, AC=K20K
01442	204456	LAC K20K	/AC=20K
01443	344456	TAD K20K	/AC=40K
01444	244455	XOR K40K	/0
01445	740200	SZA	
01446	740040	H844 HALT	/ERROR; BIT 4 SUM OR CARRY FAILED
			/TEST TAD K10K, AC=K 10K
01447	204457	LAC K10K	/AC=10K
01450	344457	TAD K10K	/AC=20K
01451	244456	XOR K20K	/0
01452	740200	SZA	
01453	740040	H845 HALT	/ERROR; BIT 5 SUM OR CARRY FAILED
			/TEST TAD K4K, AC=K4K
01454	204460	LAC K4K	/AC=4K
01455	344460	TAD K4K	/AC=10K
01456	244457	XOR K10K	/0
01457	740200	SZA	
01460	740040	H846 HALT	/ERROR; BIT 6 SUM OR CARRY FAILED
			/TEST TAD K2K, AC=K2K
01461	204461	LAC K2K	/AC=2K
01462	344461	TAD K2K	/AC=4K
01463	244460	XOR K4K	/0
01464	740200	SZA	
01465	740040	H847 HALT	/ERROR; BIT 7 SUM OR CARRY FAILED
			/TEST TAD K1K, AC=K1K
01466	204462	LAC K1K	/AC=1K
01467	344462	TAD K1K	/AC=2K
01470	244461	XOR K2K	/0
01471	740200	SZA	
01472	740040	H848 HALT	/ERROR; BIT 8 SUM OR CARRY FAILED
			/TEST TAD K400, AC=K400
01473	204463	LAC K400	/AC=400
01474	344463	TAD K400	/AC=1K
01475	244462	XOR K1K	/0
01476	740200	SZA	
01477	740040	H849 HALT	/ERROR; BIT 9 SUM OR CARRY FAILED
			/TEST TAD K200, AC=K200

01500	204464		LAC K200	/AC=200
01501	344464		TAD K200	/AC=400
01502	244463		XOR K400	/0
01503	740200		SZA	
01504	740040	H850	HALT	/ERROR; BIT 10 SUM OR CARRY FAILED /TEST TAD K100, AC=K100
01505	204465		LAC K100	/AC=100
01506	344465		TAD K100	/AC=200
01507	244464		XOR K200	/0
01510	740200		SZA	
01511	740040	H851	HALT	/ERROR; BIT 11 SUM OR CARRY FAILED /TEST TAD K40, AC=K40
01512	204466		LAC K40	/AC=40
01513	344466		TAD K40	/AC=100
01514	244465		XOR K100	/0
01515	740200		SZA	
01516	740040	H852	HALT	/ERROR; BIT 12 SUM OR CARRY FAILED /TEST TAD K20, AC=K20
01517	204467		LAC K20	/AC=20
01520	344467		TAD K20	/AC=40
01521	244466		XOR K40	/0
01522	740200		SZA	
01523	740040	H853	HALT	/ERROR; BIT 13 SUM OR CARRY FAILED /TEST TAD K10, AC=K10
01524	204470		LAC K10	/AC=10
01525	344470		TAD K10	/AC=20
01526	244467		XOR K20	/0
01527	740200		SZA	
01530	740040	H854	HALT	/ERROR; BIT 14 SUM OR CARRY FAILED /TEST TAD K4, AC=K4
01531	204471		LAC K4	/AC=4
01532	344471		TAD K4	/AC=10
01533	244470		XOR K10	/0
01534	740200		SZA	
01535	740040	H855	HALT	/ERROR; BIT 15 SUM OR CARRY FAILED /TEST TAD K2, AC=K2
01536	204472		LAC K2	/AC=2
01537	344472		TAD K2	/AC=4
01540	244471		XOR K4	/0
01541	740200		SZA	
01542	740040	H856	HALT	/ERROR; BIT 16 SUM OR CARRY FAILED /TEST TAD K1, AC=K1
01543	204473		LAC K1	/AC=1
01544	344473		TAD K1	/AC=2
01545	244472		XOR K2	/0
01546	740200		SZA	
01547	740040	H857	HALT	/ERROR; BIT 17 SUM OR CARRY FAILED /TEST TAD M400K, AC=ONES, L=1
01550	754003		CLA!CMA!CLL!CML	/AC=ONES, L-1
01551	344503		TAD M400K	/377777
01552	244525		XOR M400KM	/377776
01553	740200		SZA	/AC=0
01554	740040	H858	HALT	/ERROR; TAD M400K FAILED
01555	741400		SZL	
01556	740040	H859	HALT	/ERROR; LINK NOT A 0, CARRY FAILED

01557	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01560	344504		TAD M200K /577777
01561	244526		XOR M200KM /577776
01562	740200		SZA /AC=0
01563	740040	H860	HALT /ERROR; TAD M200K FAILED
01564	741400		SzL
01565	740040	H861	HALT /ERROR; LINK NOT A 0, CARRY FAILED
			/TEST TAD M100K, AC=ONES, L=1
01566	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01567	344505		TAD M100K /677777
01570	244527		XOR M100KM /677776
01571	740200		SZA /AC=0
01572	740040	H862	HALT /ERROR; TAD M100K FAILED
01573	741400		SzL
01574	740040	H863	HALT /ERROR; LINK NOT A 0, CARRY FAILED
			/TEST TAD M40K, AC=ONES, L=1
01575	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01576	344506		TAD M40K /737777
01577	244530		XOR M40KM /737776
01600	740200		SZA /AC=0
01601	740040	H864	HALT /ERROR; TAD M40K FAILED
01602	741400		SzL
01603	740040	H865	HALT /ERROR; LINK NOT A 0, CARRY FAILED
			/TEST TAD M20K, AC=ONES, L=1
01604	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01605	344507		TAD M20K /757777
01606	244531		XOR M20KM /757776
01607	740200		SZA /AC=0
01610	740040	H866	HALT /ERROR; TAD M20K FAILED
01611	741400		SzL
01612	740040	H867	HALT /ERROR; LINK NOT A 0, CARRY FAILED
			/TEST TAD M10K, AC=ONES, L=1
01613	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01614	344510		TAD M10K /767777
01615	244532		XOR M10KM /767776
01616	740200		SZA /AC=0
01617	740040	H868	HALT /ERROR; TAD M10K FAILED
01620	741400		SzL
01621	740040	H869	HALT /ERROR; LINK NOT A 0 CARRY FAILED
			/TEST TAD M4K, AC=ONES L=1
01622	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01623	344511		TAD M4K /773777
01624	244533		XOR M4KM /773776
01625	740200		SZA /AC=0
01626	740040	H870	HALT /ERROR; TAD M4K FAILED
01627	741400		SzL
01630	740040	H871	HALT /ERROR; LINK NOT A 0, CARRY FAILED
			/TEST TAD M2K, AC=ONES, L=1
01631	754003		CLA!CMA!CLL!CML /AC=ONES, L=1
01632	344512		TAD M2K /775777
01633	244536		XOR M2KM /775776
01634	740200		SZA /AC=0
01635	740040	H872	HALT /ERROR; TAD M2K FAILED
01636	741400		SzL

01637	740040	H873	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01640	754003		CLA:CMA:CLL:CML	/TEST TAD M1K, AC=ONES, L=1
01641	344513		TAD M1K	/AC=ONES, L=1
01642	244537		XOR M1KM	/776777
01643	740200		SZA	/AC=0
01644	740040	H874	HALT	/ERROR;TAD M1K FAILED
01645	741400		SZL	
01646	740040	H875	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01647	754003		CLA:CMA:CLL:CML	/TEST TAD M400, AC=ONES, L=1
01650	344514		TAD M400	/AC=ONES, L=1
01651	244540		XOR M400M	/777377
01652	740200		SZA	/777376
01653	740040	H876	HALT	/AC=0
01654	741400		SZL	/ERROR; TAD M400 FAILED
01655	740040	H877	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01656	754003		CLA:CMA:CLL:CML	/TEST TAD M200, AC=ONES, L=1
01657	344515		TAD M200	/AC=ONES, L=1
01660	244541		XOR M200M	/777577
01661	740200		SZA	/777576
01662	740040	H878	HALT	/AC=0
01663	741400		SZL	/ERROR; TAD M200 FAILED
01664	740040	H879	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01665	754003		CLA:CMA:CLL:CML	/TEST TAD M100, AC=ONES, L=1
01666	344516		TAD M100	/AC=ONES, L=1
01667	244542		XOR M100M	/777677
01670	740200		SZA	/777676
01671	740040	H880	HALT	/AC=0
01672	741400		SZL	/ERROR; TAD M100 FAILED
01673	740040	H881	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01674	754003		CLA:CMA:CLL:CML	/TEST TAD M40, AC=ONES, L=1
01675	344517		TAD M40	/AC=ONES, L=1
01676	244543		XOR M40M	/777737
01677	740200		SZA	/777736
01700	740040	H882	HALT	/AC=0
01701	741400		SZL	/ERROR; TAD M40 FAILED
01702	740040	H883	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01703	754003		CLA:CMA:CLL:CML	/TEST TAD M20, AC=ONES, L=1
01704	344520		TAD M20	/AC=ONES, L=1
01705	244544		XOR M20M	/777757
01706	740200		SZA	/777756
01707	740040	H884	HALT	/AC=0
01710	741400		SZL	/ERROR; TAD M20 FAILED
01711	740040	H885	HALT	/ERROR; LINK NOT A 0, CARRY FAILED
01712	754003		CLA:CMA:CLL:CML	/TEST TAD M10, AC=ONES, L=1
01713	344521		TAD M10	/AC=ONES, L=1
01714	244545		XOR M10M	/777767
01715	740200		SZA	/777766
01716	740040	H886	HALT	/AC=0
				/ERROR; TAD M10 FAILED

01717	741400		SZL	
01720	740040	H887	HALT	/ERROR; LINK NOT A 0, CARRY FAILED /TEST TAD M4, AC=ONES, L=1
01721	754003		CLA!CMA!CLL!CML	/AC=ONES, L=1
01722	344522		TAD M4	/777773
01723	244546		XOR M4M	/777772
01724	740200		SZA	/AC=0
01725	740040	H888	HALT	/ERROR; TAD M4 FAILED
01726	741400		SZL	
01727	740040	H889	HALT	/ERROR; LINK NOT A 0, CARRY FAILED /TEST TAD 2, AC=ONES, L=1
01730	754003		CLA!CMA!CLL!CML	/AC=ONES, L=1
01731	344523		TAD M2	/777775
01732	244547		XOR M2M	/777774
01733	740200		SZA	/AC=0
01734	740040	H890	HALT	/ERROR; TAD M2 FAILED
01735	741400		SZL	
01736	740040	H891	HALT	/ERROR; LINK NOT A 0, CARRY FAILED /TEST TAD M1, AC=ONES, L=1
01737	754003		CLA!CMA!CLL!CML	/AC=ONES, L=1
01740	344524		TAD M1	/777776
01741	244550		XOR M1M	/777775
01742	740200		SZA	/AC=0
01743	740040	H892	HALT	/ERROR; TAD M1 FAILED
01744	741400		SZL	
01745	740040	H893	HALT	/ERROR; LINK NOT A 0, CARRY FAILED /TEST TAD K7S, AC=M400K, L=0
01746	744000		CLL	/L=0
01747	204503		LAC M400K	/AC=377777
01750	344502		TAD K7S	/ONES
01751	244525		XOR M400KM	/377776
01752	740200		SZA	/AC=0
01753	740040	H894	HALT	/ERROR; TAD K7S FAILED
01754	740400		SNL	
01755	740040	H895	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M200K, L=0
01756	744000		CLL	/L=0
01757	204504		LAC M200K	/AC=577777
01760	344502		TAD K7S	/ONES
01761	244526		XOR M200KM	/577776
01762	740200		SZA	/AC=0
01763	740040	H896	HALT	/ERROR; TAD K7S FAILED
01764	740400		SNL	
01765	740040	H897	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M100K, L=0
01766	744000		CLL	/L=0
01767	204505		LAC M100K	/AC=677777
01770	344502		TAD K7S	/ONES
01771	244527		XOR M100KM	/677776
01772	740200		SZA	/AC=0
01773	740040	H898	HALT	/ERROR; TAD K7S FAILED
01774	740400		SNL	
01775	740040	H899	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M40KM, L=0
01776	744000		CLL	/L=0

01777	204506		LAC M40K	/AC=737777
02000	344502		TAD K7S	/ONES
02001	244530		XOR M40KM	/737776
02002	740200		SZA	/AC=0
02003	740040	H900	HALT	/ERROR; TAD K7S FAILED
02004	740400		SNL	
02005	740040	H901	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M20K, L=0
				/L=0
02006	744000		CLL	
02007	204507		LAC M20K	/AC=757777
02010	344502		TAD K7S	/ONES
02011	244531		XOR M20KM	/757776
02012	740200		SZA	/AC=0
02013	740040	H902	HALT	/ERROR; TAD K7S FAILED
02014	740400		SNL	
02015	740040	H903	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M10K, L=0
				/L=0
02016	744000		CLL	
02017	204510		LAC M10K	/AC=767777
02020	344502		TAD K7S	/ONES
02021	244532		XOR M10KM	/767776
02022	740200		SZA	/AC=0
02023	740040	H904	HALT	/ERROR; TAD K7S FAILED
02024	740400		SNL	
02025	740040	H905	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M4K, L=0
				/L=0
02026	744000		CLL	
02027	204511		LAC M4K	/AC=773777
02030	344502		TAD K7S	/ONES
02031	244533		XOR M4KM	/773776
02032	740200		SZA	
02033	740040	H906	HALT	/ERROR; TAD K7S FAILED
02034	740400		SNL	
02035	740040	H907	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M2K, L=0
				/L=0
02036	744000		CLL	
02037	204512		LAC M2K	/AC=775777
02040	344502		TAD K7S	/ONES
02041	244536		XOR M2KM	/775776
02042	740200		SZA	
02043	740040	H908	HALT	/ERROR; TAD K7S FAILED
02044	740400		SNL	
02045	740040	H909	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M1K, L=1
				/L=0
02046	744000		CLL	
02047	204513		LAC M1K	/AC=776777
02050	344502		TAD K7S	/ONES
02051	244537		XOR M1KM	/776776
02052	740200		SZA	/AC=0
02053	740040	H910	HALT	/ERROR; TAD K8S FAILED
02054	740400		SNL	
02055	740040	H911	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M400, L=0
				/L=0
02056	744000		CLL	
02057	204514		LAC M400	/AC=777377

02060	344502		TAD K7S	/ONES
02061	244540		XOR M400M	/777376
02062	740200		SZA	/AC=0
02063	740040	H912	HALT	/ERROR; TAD K7S FAILED
02064	740400		SNL	
02065	740040	H913	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S AC=M200, L=0 /L=0
02066	744000		CLL	
02067	204515		LAC M200	/AC=777577
02070	344502		TAD K7S	/ONES
02071	244541		XOR M200M	/777576
02072	740200		SZA	/AC=0
02073	740040	H914	HALT	/ERROR; TAD K7S FAILED
02074	740400		SNL	
02075	740040	H915	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M100, L=0 /L=0
02076	744000		CLL	
02077	204516		LAC M100	/AC=777677
02100	344502		TAD K7S	/ONES
02101	244542		XOR M100M	/777676
02102	740200		SZA	/AC=0
02103	740040	H916	HALT	/ERROR; TAD K7S FAILED
02104	740400		SNL	
02105	740040	H917	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M40, L=0 /L=0
02106	744000		CLL	
02107	204517		LAC M40	/AC=777737
02110	344502		TAD K7S	/ONES
02111	244543		XOR M40M	/777736
02112	740200		SZA	/AC=0
02113	740040	H918	HALT	/ERROR; TAD K7S FAILED
02114	740400		SNL	
02115	740040	H919	HALT	/ERROR LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M20, L=0 /L=0
02116	744000		CLL	
02117	204520		LAC M20	/AC=777757
02120	344502		TAD K7S	/ONES
02121	244544		XOR M20M	/777756
02122	740200		SZA	/AC=0
02123	740040	H920	HALT	/ERROR; TAD K7S FAILED
02124	740400		SNL	
02125	740040	H921	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M10, L=0 /L=0
02126	744000		CLL	
02127	204521		LAC M10	/AC=7777767
02130	344502		TAD K7S	/ONES
02131	244545		XOR M10M	/7777766
02132	740200		SZA	/AC=0
02133	740040	H922	HALT	/ERROR; TAD K7S FAILED
02134	740400		SNL	
02135	740040	H923	HALT	/ERROR; LINK NOT A 1, CARRY FAILED /TEST TAD K7S, AC=M4, L=0 /L=0
02136	744000		CLL	
02137	204522		LAC M4	/AC=777773
02140	344502		TAD K7S	/ONES

02141	244546		XOR M4M	/777772
02142	740200		SZA	/AC=0
02143	740040	H924	HALT	/ERROR; TAD K7S FAILED
02144	740400		SNL	
02145	740040	H925	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M2, L=0
02146	744000		CLL	/L=0
02147	204523		LAC M2	/AC=777775
02150	344502		TAD K7S	/ONES
02151	244547		XOR M2M	/777774
02152	740200		SZA	/AC=0
02153	740040	H926	HALT	/ERROR; TAD K7S FAILED
02154	740400		SNL	
02155	740040	H927	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=M1, L=0
02156	744000		CLL	/L=0
02157	204524		LAC M1	/AC=777776
02160	344502		TAD K7S	/ONES
02161	244550		XOR M1M	/777775
02162	740200		SZA	/AC=0
02163	740040	H930	HALT	/ERROR; TAD K7S FAILED
02164	740400		SNL	
02165	740040	H931	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
				/TEST TAD K7S, AC=ONES, L=0
02166	754001		CLA!CMA!CLL	/AC=ONES, L=0
02167	344502		TAD K7S	/ONES
02170	244524		XOR M1	/777776
02171	740200		SZA	/AC=0
02172	740040	H932	HALT	/ERROR; TAD K7S FAILED
02173	740400		SNL	
02174	740040	H933	HALT	/ERROR; LINK NOT A 1, CARRY FAILED
			/INST TEST TAPE 4	
				/TEST ADD K1S TO K6S, L=0
02175	744000		CLL	/L=0
02176	304474		ADD K1S	/111111
02177	304501		ADD K6S	/666666
02200	740001		CMA	/AC=0
02201	740200		SZA	
02202	740040	H934	HALT	/ERROR; ADD K1S TO K6S FAILED
02203	741400		SZL	
02204	740040	H935	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K2S TO K5; L=0
02205	744000		CLL	/L=0
02206	304475		ADD K2S	/222222
02207	304500		ADD K5S	/555555
02210	740001		CMA	/AC=0
02211	740200		SZA	
02212	740040	H936	HALT	/ERROR; ADD K2S TO K5S FAILED
02213	741400		SZL	
02214	740040	H937	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K3S TO K4S, L=0
02215	744000		CLL	/L=0
02216	304476		ADD K3S	/333333
02217	304477		ADD K4S	/444444
02220	740001		CMA	/AC=0

02221	740200		SZA	
02222	740040	H938	HALT	/ERROR: ADD K3S TO K4S FAILED
02223	741400		SZL	
02224	740040	H939	HALT	/ERROR: LINK NOT A 0 /TEST ADD K4S TO K3S, L=0 /L=0
02225	744000		CLL	/L=0
02226	304477		ADD K4S	/444444
02227	304476		ADD K3S	/333333
02230	740001		CMA	/AC=0
02231	740200		SZA	
02232	740040	H940	HALT	/ERROR: ADD K4S TO K3S FAILED
02233	741400		SZL	
02234	740040	H941	HALT	/ERROR: LINK NOT A 0 /TEST ADD K5S TO K2S, L=0 /L=0
02235	744000		CLL	/L=0
02236	304500		ADD K5S	/555555
02237	304475		ADD K2S	/222222
02240	740001		CMA	/AC=0
02241	740200		SZA	
02242	740040	H942	HALT	/ERROR: ADD K5S TO K2S FAILED
02243	741400		SZL	
02244	740040	H943	HALT	/ERROR: LINK NOT A 0 /TEST AND K6S TO K1S, L=0 /L=0
02245	744000		CLL	/L=0
02246	304501		ADD K6S	/666666
02247	304474		ADD K1S	/111111
02250	740001		CMA	/AC=0
02251	740200		SZA	
02252	740040	H944	HALT	/ERROR: ADD K6S TO K1S FAILED
02253	741400		SZL	
02254	740040	H945	HALT	/ERROR: LINK NOT A 0 /TEST ADD K7S TO K0S, L=0 /L=0
02255	744000		CLL	/L=0
02256	304502		ADD K7S	/777777
02257	304451		ADD K0	/0
02260	740001		CMA	/AC=0
02261	740200		SZA	
02262	740040	H946	HALT	/ERROR: ADD K7S TO K0S FAILED
02263	741400		SZL	
02264	740040	H947	HALT	/ERROR: LINK NOT A 0 /TEST ADD 252525, AC=525252, L=0 /AC=ONES, L=0
02265	754001		CLL:CLA:CMA	
02266	204534		LAC K5252	/AC=525252
02267	304535		ADD K2525	/252525
02270	740001		CMA	/AC=0
02271	740200		SZA	
02272	740040	H948	HALT	/ERROR: ADD K2525 TO K5252 FAILED
02273	741400		SZL	
02274	740040	H949	HALT	/ERROR: LINK NOT A 0 /TEST ADD 525252, AC=252525, L=0 /L=0
02275	744000		CLL	/L=0
02276	204535		LAC K2525	/AC=252525
02277	304534		ADD K5252	/525252
02300	740001		CMA	/AC=0
02301	740200		SZA	

02302	740040	H950	HALT	/ERROR: ADD K5252 TO K2525 FAILED
02303	741400		SZL	
02304	740040	H951	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M400K, ADD K400K, L=0
02305	754000		CLA:CLL	/AC=0, L=0
02306	304503		ADD M400K	/AC=377777
02307	304452		ADD K400K	/400000
02310	740001		CMA	/AC=0
02311	740200		SZA	
02312	740040	H952A	HALT	/ERROR: INCLUSIVE ADD FAILURE
02313	741400		SZL	
02314	740040	H952B	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M200K, ADD K200K, L=0
02315	754000		CLA:CLL	/AC=0, L=0
02316	304504		ADD M200K	/AC=577777
02317	304453		ADD K200K	/200000
02320	740001		CMA	/AC=0
02321	740200		SZA	
02322	740040	H952C	HALT	/ERROR: INCLUSIVE ADD FAILURE
02323	741400		SZL	
02324	740040	H952D	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M100K, K100K, L=0
02325	754000		CLA:CLL	/AC=0, L=0
02326	304505		ADD M100K	/AC=677777
02327	304454		ADD K100K	/100000
02330	740001		CMA	/AC=0
02331	740200		SZA	
02332	740040	H952E	HALT	/ERROR: INCLUSIVE ADD FAILURE
02333	741400		SZL	
02334	740040	H952F	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M40K, ADD K40K, L=0
02335	754000		CLA:CLL	/AC=0, L=0
02336	304506		ADD M40K	/AC=737777
02337	304455		ADD K40K	/040000
02340	740001		CMA	/AC=0
02341	740200		SZA	
02342	740040	H952G	HALT	/ERROR: INCLUSIVE ADD FAILURE
02343	741400		SZL	
02344	740040	H952H	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M20K, ADD K20K, L=0
02345	754000		CLA:CLL	/AC=0, L=0
02346	304507		ADD M20K	/AC=757777
02347	304456		ADD K20K	/020000
02350	740001		CMA	/AC=0
02351	740200		SZA	
02352	740040	H952I	HALT	/ERROR: INCLUSIVE ADD FAILURE
02353	741400		SZL	
02354	740040	H952J	HALT	/ERROR: LINK NOT A 0
				/TEST ADD M10K, ADD K10K, L=0
02355	754000		CLA:CLL	/AC=0, L=0
02356	304510		ADD M10K	/AC=767777
02357	304457		ADD K10K	/010000
02360	740001		CMA	/AC=0
02361	740200		SZA	
02362	740040	H952K	HALT	/ERROR: INCLUSIVE ADD FAILURE

02363	741400		SZL	
02364	740040	H952L	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M4K, ADD K4K, L=0
02365	754000		CLA:CLL	/AC=0, L=0
02366	304511		ADD M4K	/AC=773777
02367	304460		ADD K4K	/004000
02370	740001		CMA	/AC=0
02371	740200		SZA	
02372	740040	H952M	HALT	/ERROR; INCLUSIVE ADD FAILURE
02373	741400		SZL	
02374	740040	H952N	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M2K, ADD K2K, L=0
02375	754000		CLA:CLL	/AC=0, L=0
02376	304512		ADD M2K	/AC=775777
02377	304461		ADD K2K	/002000
02400	740001		CMA	/AC=0
02401	740200		SZA	
02402	740040	H9520	HALT	/ERROR; INCLUSIVE ADD FAILURE
02403	741400		SZL	
02404	740040	H952P	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M1K, ADD K1K, L=0
02405	754000		CLA:CLL	/AC=0, L=0
02406	304513		ADD M1K	/AC=776777
02407	304462		ADD K1K	/001000
02410	740001		CMA	/AC=0
02411	740200		SZA	
02412	740040	H952Q	HALT	/ERROR; INCLUSIVE ADD FAILURE
02413	741400		SZL	
02414	740040	H952R	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M400, ADD K400, L=0
02415	754000		CLA:CLL	/AC=0, L=0
02416	304514		ADD M400	/AC=777377
02417	304463		ADD K400	/000400
02420	740001		CMA	/AC=0
02421	740200		SZA	
02422	740040	H952S	HALT	/ERROR; INCLUSIVE ADD FAILURE
02423	741400		SZL	
02424	740040	H952T	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M200, ADD K200, L=0
02425	754000		CLA:CLL	/AC=0, L=0
02426	304515		ADD M200	/AC=777577
02427	304464		ADD K200	/000200
02430	740001		CMA	/AC=0
02431	740200		SZA	
02432	740040	H952U	HALT	/ERROR; INCLUSIVE ADD FAILURE
02433	741400		SZL	
02434	740040	H952V	HALT	/ERROR; LINK NOT A 0
				/TEST ADD M100, ADD K100, L=0
02435	754000		CLA:CLL	/AC=0, L=0
02436	304516		ADD M100	/AC=777677
02437	304465		ADD K100	/000100
02440	740001		CMA	/AC=0
02441	740200		SZA	
02442	740040	H952W	HALT	/ERROR; INCLUSIVE ADD FAILURE
02443	741400		SZL	

02444	740040	H952X	HALT	/ERROR; LINK NOT A 0
02445	754000		CLA:CLL	/TEST ADD M40, ADD K40, L=0
02446	304517		ADD M40	/AC=0, L=0
02447	304466		ADD K40	/AC=777737
02450	740001		CMA	/000040
02451	740200		SZA	/AC=0
02452	740040	H952Y	HALT	/ERROR; INCLUSIVE ADD FAILURE
02453	741400		SZL	
02454	740040	H952Z	HALT	/ERROR; LINK NOT A 0
02455	754000		CLA:CLL	/TEST ADD M20, ADD K20, L=0
02456	304520		ADD M20	/AC=0, L=0
02457	304467		ADD K20	/AC = 777757
02460	740001		CMA	/000020
02461	740200		SZA	/AC=0
02462	740040	H952AA	HALT	/ERROR; INCLUSIVE ADD FAILURE
02463	741400		SZL	
02464	740040	H952BB	HALT	/ERROR; LINK NOT A 0
02465	754000		CLA:CLL	/TEST ADD M10, ADD K10, L=0
02466	304521		ADD M10	/AC=0, L=0
02467	304470		ADD K10	/AC=777767
02470	740001		CMA	/000010
02471	740200		SZA	/AC=0
02472	740040	H952CC	HALT	/ERROR, INCLUSIVE ADD FAILURE
02473	741400		SZL	
02474	740040	H952DD	HALT	/ERROR; LINK NOT A 0
02475	754000		CLA:CLL	/TEST ADD M4, ADD K4, L=0
02476	304522		ADD M4	/AC=0, L=0
02477	304471		ADD K4	/AC=777773
02500	740001		CMA	/000004
02501	740200		SZA	/AC=0
02502	740040	H952EE	HALT	/ERROR; INCLUSIVE ADD FAILURE
02503	741400		SZL	
02504	740040	H952FF	HALT	/ERROR; LINK NOT A 0
02505	754000		CLA:CLL	/TEST ADD M2, ADD K2, L=0
02506	304523		ADD M2	/AC=0, L=0
02507	304472		ADD K2	/AC=777775
02510	740001		CMA	/000002
02511	740200		SZA	/AC=0
02512	740040	H952GG	HALT	/ERROR; INCLUSIVE ADD FAILURE
02513	741400		SZL	
02514	740040	H952HH	HALT	/ERROR; LINK NOT A 0
02515	754000		CLA:CLL	/TEST ADD M1, ADD K1, L=0
02516	304524		ADD M1	/AC=0, L=0
02517	304473		ADD K1	/AC=777776
02520	740001		CMA	/000001
02521	740200		SZA	/AC=0
02522	740040	H952II	HALT	/ERROR; INCLUSIVE ADD FAILURE
02523	741400		SZL	
02524	740040	H952JJ	HALT	/ERROR; LINK NOT A 0

02525	754000		CLA:CLL	/TEST ADD K400K, ADD M400K, L=0
02526	304452		ADD K400K	/AC=0, L=0
02527	304503		ADD M400K	/AC=400000
02530	740001		CMA	/377777
02531	740200		SZA	/AC=0
02532	740040	H953A	HALT	/ERROR: INCLUSIVE ADD FAILURE
02533	741400		SZL	
02534	740040	H953B	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K200K, ADD M200K, L=0
02535	754000		CLA:CLL	/AC=0, L=0
02536	304453		ADD K200K	/AC=200000
02537	304504		ADD M200K	/577777
02540	740001		CMA	/AC=0
02541	740200		SZA	
02542	740040	H953C	HALT	/ERROR: INCLUSIVE ADD FAILURE
02543	741400		SZL	
02544	740040	H953D	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K100K, ADD M100K, L=0
02545	754000		CLA:CLL	/AC=0, L=0
02546	304454		ADD K100K	/AC=100000
02547	304505		ADD M100K	/677777
02550	740001		CMA	/AC=0
02551	740200		SZA	
02552	740040	H953E	HALT	/ERROR: INCLUSIVE ADD FAILURE
02553	741400		SZL	
02554	740040	H953F	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K40K, ADD M40K, L=0
02555	754000		CLA:CLL	/AC=0, L=0
02556	304455		ADD K40K	/AC=040000
02557	304506		ADD M40K	/737777
02560	740001		CMA	/AC=0
02561	740200		SZA	
02562	740040	H953G	HALT	/ERROR: INCLUSIVE ADD FAILURE
02563	741400		SZL	
02564	740040	H953H	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K20K, M20K, L=0
02565	754000		CLA:CLL	/AC=0, L=0
02566	304456		ADD K20K	/AC=020000
02567	304507		ADD M20K	/757777
02570	740001		CMA	/AC=0
02571	740200		SZA	
02572	740040	H953I	HALT	/ERROR: INCLUSIVE ADD FAILURE
02573	741400		SZL	
02574	740040	H953J	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K10K, ADD M10K, L=0
02575	754000		CLA:CLL	/AC=0, L=0
02576	304457		ADD K10K	/AC=010000
02577	304510		ADD M10K	/767777
02600	740001		CMA	/AC=0
02601	740200		SZA	
02602	740040	H953K	HALT	/ERROR: INCLUSIVE ADD FAILURE
02603	741400		SZL	
02604	740040	H953L	HALT	/ERROR: LINK NOT A 0
				/TEST ADD K4K, ADD M4K, L=0

02605	754000	CLA:CLL	/AC=0, L=0
02606	304460	ADD K4K	/AC=004000
02607	304511	ADD M4K	/773777
02610	740001	CMA	/AC=0
02611	740200	SZA	
02612	740040	H953M HALT	/ERROR; INCLUSIVE ADD FAILURE
02613	741400	SZL	
02614	740040	H953N HALT	/ERROR; LINK NOT A 0
			/TEST ADD K2K, ADD M2K, L=0
02615	754000	CLA:CLL	/AC=0, L=0
02616	304461	ADD K2K	/AC=002000
02617	304512	ADD M2K	/775777
02620	740001	CMA	/AC=0
02621	740200	SZA	
02622	740040	H953O HALT	/ERROR; INCLUSIVE ADD FAILURE
02623	741400	SZL	
02624	740040	H953P HALT	/ERROR; LINK NOT A 0
			/TEST ADD K1K, ADD M1K, L=0
02625	754000	CLA:CLL	/AC=0, L=0
02626	304462	ADD K1K	/AC=001000
02627	304513	ADD M1K	/776777
02630	740001	CMA	/AC=0
02631	740200	SZA	
02632	740040	H953Q HALT	/ERROR; INCLUSIVE ADD FAILURE
02633	741400	SZL	
02634	740040	H953R HALT	/ERROR; LINK NOT A 0
			/TEST ADD K400, ADD M400, L=0
02635	754000	CLA:CLL	/AC=0, L=0
02636	304463	ADD K400	/AC=000400
02637	304514	ADD M400	/777377
02640	740001	CMA	/AC=0
02641	740200	SZA	
02642	740040	H953S HALT	/ERROR; INCLUSIVE ADD FAILURE
02643	741400	SZL	
02644	740040	H953T HALT	/ERROR; LINK NOT A 0
			/TEST ADD K200, ADD M200, L=0
02645	754000	CLA:CLL	/AC=0, L=0
02646	304464	ADD K200	/AC=000200
02647	304515	ADD M200	/777577
02650	740001	CMA	/AC=0
02651	740200	SZA	
02652	740040	H953U HALT	/ERROR; INCLUSIVE ADD FAILURE
02653	741400	SZL	
02654	740040	H953V HALT	/ERROR; LINK NOT A 0
			/TEST ADD K100, ADD M100, L=0
02655	754000	CLA:CLL	/AC=0, L=0
02656	304465	ADD K100	/AC=000100
02657	304516	ADD M100	/777677
02660	740001	CMA	/AC=0
02661	740200	SZA	
02662	740040	H953W HALT	/ERROR; INCLUSIVE ADD FAILURE
02663	741400	SZL	
02664	740040	H953X HALT	/ERROR; LINK NOT A 0
			/TEST ADD K40, ADD M40, L=0
02665	754000	CLA:CLL	/AC=0, L=0

02666	304466		ADD K40	/AC=000040
02667	304517		ADD M40	/777737
02670	740001		CMA	/AC=0
02671	740200		SZA	
02672	740040	H953Y	HALT	/ERROR; INCLUSIVE ADD FAILURE
02673	741400		SZL	
02674	740040	H953Z	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K20, ADD M20, L=0
02675	754000		CLA:CLL	/AC=0, L=0
02676	304467		ADD K20	/AC=000020
02677	304520		ADD M20	/777757
02700	740001		CMA	/AC=0
02701	740200		SZA	
02702	740040	H953AA	HALT	/ERROR; INCLUSIVE ADD FAILURE
02703	741400		SZL	
02704	740040	H953BB	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K10, ADD M10, L=0
02705	754000		CLA:CLL	/AC=0, L=0
02706	304470		ADD K10	/AC=000010
02707	304521		ADD M10	/777767
02710	740001		CMA	/AC=0
02711	740200		SZA	
02712	740040	H953CC	HALT	/ERROR; INCLUSIVE ADD FAILURE
02713	741400		SZL	
02714	740040	H953DD	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K4, ADD M4, L=0
02715	754000		CLA:CLL	/AC=0, L=0
02716	304471		ADD K4	/AC=000004
02717	304522		ADD M4	/777773
02720	740001		CMA	/AC=0
02721	740200		SZA	
02722	740040	H953EE	HALT	/ERROR; INCLUSIVE ADD FAILURE
02723	741400		SZL	
02724	740040	H953FF	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K2, ADD M2, L=0
02725	754000		CLA:CLL	/AC=0, L=0
02726	304472		ADD K2	/AC=000002
02727	304523		ADD M2	/777775
02730	740001		CMA	/AC=0
02731	740200		SZA	
02732	740040	H953GG	HALT	/ERROR; INCLUSIVE ADD FAILURE
02733	741400		SZL	
02734	740040	H953HH	HALT	/ERROR; LINK NOT A 0
				/TEST ADD K1, ADD M1, L=0
02735	754000		CLA:CLL	/AC=0, L=0
02736	304473		ADD K1	/AC=000001
02737	304524		ADD M1	/777776
02740	740001		CMA	/AC=0
02741	740200		SZA	
02742	740040	H953II	HALT	/ERROR; INCLUSIVE ADD FAILURE
02743	741400		SZL	
02744	740040	H953JJ	HALT	/ERROR; LINK NOT A 0
		/PDP-15	INSTRUCTION TEST PART 1 - TAPE 5	/TEST ADD K7S, AC=K400K, L=0
02745	754001		CLA:CMA:CLL	/AC=ONES, L=0

02746	204452		LAC K400K	/AC=400K
02747	304502		ADD K7S	/ONES
02750	504502		AND K7S	/AC=400K
02751	244452		XOR K400K	
02752	740200		SZA	/AC=0
02753	740040	H954	HALT	/ERROR; ADD-0 TO K400K FAILED
02754	741400		SZL	
02755	740040	H955	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED /TEST ADD K200K, AC=K200K, L=0
02756	754001		CLA!CMA!CLL	/AC=ONES, L=0
02757	204453		LAC K200K	/AC=200K
02760	304453		ADD K200K	/ONES
02761	504502		AND K7S	/AC=400K
02762	244452		XOR K400K	
02763	740200		SZA	/AC=0
02764	740040	H956	HALT	/ERROR; ADD K200K TO K200K FAILED
02765	740400		SNL	
02766	740040	H957	HALT	/ERROR; LINK NOT A ONE, CARRY FAILED /TEST ADD K7S, AC=K100K, L=1
02767	754003		CLA!CMA!CLL!CML	/AC=ONES, L=1
02770	204454		LAC K100K	/AC=100K
02771	304502		ADD K7S	/ONES
02772	504502		AND K7S	/AC=100K
02773	244454		XOR K100K	
02774	740200		SZA	/AC=0
02775	740040	H958	HALT	/ERROR; ADD-0 TO K100K FAILED
02776	740400		SNL	
02777	740040	H959	HALT	/ERROR; LINK NOT A ONE, LINK RESET /TEST ADD K7S, AC=K40K, L=0
03000	754001		CLA!CMA!CLL	/AC=ONES, L=0
03001	204455		LAC K40K	/AC=40K
03002	304502		ADD K7S	/ONES
03003	504502		AND K7S	/AC=40K
03004	244455		XOR K40K	
03005	740200		SZA	/AC=0
03006	740040	H960	HALT	/ERROR; ADD-0 TO K40K FAILED
03007	741400		SZL	
03010	740040	H961	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED /TEST ADD K7S, AC=K20K, L=0
03011	754001		CLA!CMA!CLL	/AC=ONES, L=0
03012	204456		LAC K20K	/AC=20K
03013	304502		ADD K7S	/ONES
03014	504502		AND K7S	/AC=20K
03015	244456		XOR K20K	
03016	740200		SZA	/AC=0
03017	740040	H962	HALT	/ERROR; ADD-0 TO K20K FAILED
03020	741400		SZL	
03021	740040	H963	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED /TEST AND K7S, AC=K10K, L=0
03022	754001		CLA!CMA!CLL	/AC=ONES, L=0
03023	204457		LAC K10K	/AC=10K
03024	304502		ADD K7S	/ONES
03025	504502		AND K7S	/AC=10K
03026	244457		XOR K10K	
03027	740200		SZA	/AC=0

03030	740040	H964	HALT	/ERROR; ADD-0 TO K10K FAILED
03031	741400		SZL	
03032	740040	H965	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K4K, L=0
03033	754001		CLA!CMA!CLL	/AC=ONES, L=0
03034	204460		LAC K4K	/AC=4K
03035	304502		ADD K7S	/ONES
03036	504502		AND K7S	/AC=4K
03037	244460		XOR K4K	
03040	740200		SZA	/AC=0
03041	740040	H966	HALT	/ERROR; ADD-0 TO K4K FAILED
03042	741400		SZL	
03043	740040	H967	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K2K, L=0
03044	754001		CLA!CMA!CLL	/AC=ONES, L=0
03045	204461		LAC K2K	/AC=2K
03046	304502		ADD K7S	/ONES
03047	504502		AND K7S	/AC=2K
03050	244461		XOR K2K	
03051	740200		SZA	/AC=0
03052	740040	H968	HALT	/ERROR; ADD-0 TO K2K FAILED
03053	741400		SZL	
03054	740040	H969	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K1K, L=0
03055	754001		CLA!CMA!CLL	/AC=ONES, L=0
03056	204462		LAC K1K	/AC=1K
03057	304502		ADD K7S	/ONES
03060	504502		AND K7S	/AC=1K
03061	244462		XOR K1K	
03062	740200		SZA	/AC=0
03063	740040	H970	HALT	/ERROR; ADD-0 TO K1K FAILED
03064	741400		SZL	
03065	740040	H971	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K400, L=0
03066	754001		CLA!CMA!CLL	/AC=ONES, L=0
03067	204463		LAC K400	/AC=400
03070	304502		ADD K7S	/ONES
03071	504502		AND K7S	/AC=400
03072	244463		XOR K400	
03073	740200		SZA	/AC=0
03074	740040	H972	HALT	/ERROR; ADD-0 TO K400 FAILED
03075	741400		SZL	
03076	740040	H973	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K200, L=0
03077	754001		CLA!CMA!CLL	/AC=ONES, L=0
03100	204464		LAC K200	/AC=200
03101	304502		ADD K7S	/ONES
03102	504502		AND K7S	/AC=200
03103	244464		XOR K200	
03104	740200		SZA	/AC=0
03105	740040	H974	HALT	/ERROR; ADD-0 TO K200 FAILED
03106	741400		SZL	
03107	740040	H975	HALT	/ERROR; ADD LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K100, L=0
03110	754001		CLA!CMA!CLL	/AC=ONES, L=0

03111	204465		LAC K100	/AC=100
03112	304502		ADD K7S	/ONES
03113	504502		AND K7S	/AC=100
03114	244465		XOR K100	
03115	740200		SZA	/AC=0
03116	740040	H976	HALT	/ERROR: ADD -0 TO K100 FAILED
03117	741400		SZL	
03120	740040	H977	HALT	/ERROR: LINK NOT A ZERO, CARRY FAILED
03121	754001		CLA!CMA!CLL	/TEST ADD K7S, AC=K40K, L=0
03122	204466		LAC K40	/AC=ONES, L=0
03123	304502		ADD K7S	/AC=40
03124	504502		AND K7S	/ONES
03125	244466		XOR K40	/AC=40
03126	740200		SZA	/AC=0
03127	740040	H978	HALT	/ERROR: ADD-0 TO K40 FAILED
03130	741400		SZL	
03131	740040	H979	HALT	/ERROR: LINK NOT A ZERO, CARRY FAILED
03132	754001		CLA!CMA!CLL	/TEST ADD K7S, AC=K20 L=0
03133	204467		LAC K20	/AC=ONES, L=0
03134	304502		ADD K7S	/AC=20
03135	504502		AND K7S	/ONES
03136	244467		XOR K20	/AC=20
03137	740200		SZA	/AC=0
03140	740040	H980	HALT	/ERROR: ADD -0 TO K20 FAILED
03141	741400		SZL	
03142	740040	H981	HALT	/ERROR: LINK NOT A ZERO, CARRY FAILED
03143	754001		CLA!CMA!CLL	/TEST ADD K7S, AC=K10, L=0
03144	204470		LAC K10	/AC=ONES, L=0
03145	304502		ADD K7S	/AC=10
03146	504502		AND K7S	/ONES
03147	244470		XOR K10	/AC=10
03150	740200		SZA	/AC=0
03151	740040	H982	HALT	/ERROR: ADD=0 TO K10 FAILED
03152	741400		SZL	
03153	740040	H983	HALT	/ERROR: LINK NOT A ZERO CARRY FAILED
03154	754001		CLA!CMA!CLL	/TEST ADD K7S, AC=4, L=0
03155	204471		LAC K4	/AC=ONES, L=0
03156	304502		ADD K7S	/AC=4
03157	504502		AND K7S	/ONES
03160	244471		XOR K4	/AC=4
03161	740200		SZA	/AC=0
03162	740040	H984	HALT	/ERROR: ADD -0 TO K4 FAILED
03163	741400		SZL	
03164	740040	H985	HALT	/ERROR: LINK NOT A ZERO, CARRY FAILED
03165	754001		CLA!CMA!CLL	/TEST ADD K7S, AC=K2 L=0
03166	204472		LAC K2	/AC=ONES, L=0
03167	304502		ADD K7S	/AC=2
03170	504502		AND K7S	/ONES
03171	244472		XOR K2	/AC=2
03172	740200		SZA	/AC=0

03173	740040	H986	HALT	/ERROR; ADD-0 TO K2 FAILED
03174	741400		SZL	
03175	740040	H987	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K7S, AC=K1, L=0
03176	754001		CLA!CMA!CLL	/AC=ONES, L=0
03177	204473		LAC K1	/AC=1
03200	304502		ADD K7S	/ONES
03201	504502		AND K7S	/AC=1
03202	244473		XOR K1	
03203	740200		SZA	/AC=0
03204	740040	H988	HALT	/ERROR; ADD-0 TO K1 FAILED
03205	741400		SZL	
03206	740040	H989	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K400K, AC=ONES, L=0
				/L=0
03207	744000		CLL	
03210	777777		LAW 17777	/AC=ONES
03211	504502		AND K7S	/AC=ONES
03212	304452		ADD K400K	/400K
03213	244452		XOR K400K	
03214	740200		SZA	/AC=0
03215	740040	H990	HALT	/ERROR; ADD K400K TO -0 FAILED
03216	741400		SZL	
03217	740040	H991	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
				/TEST ADD K200K, AC=ONES, L=1
				/L=1
03220	744002		CLL!CML	
03221	777777		LAW 17777	/AC=ONES
03222	504502		AND K7S	/AC=ONES
03223	304453		ADD K200K	/200K
03224	244453		XOR K200K	
03225	740200		SZA	/AC=0
03226	740040	H992	HALT	/ERROR; ADD K200K TO -0 FAILED
03227	740400		SNL	
03230	740040	H993	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K100K, AC=ONES, L=1
				/L=1
03231	744002		CLL!CML	
03232	777777		LAW 17777	/AC=ONES
03233	504502		AND K7S	/AC=ONES
03234	304454		ADD K100K	/100K
03235	244454		XOR K100K	
03236	740200		SZA	/AC=0
03237	740040	H994	HALT	/ERROR; ADD K100K TO -0 FAILED
03240	740400		SNL	
03241	740040	H995	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K40K, AC=ONES, L=1
				/L=1
03242	744002		CLL!CML	
03243	777777		LAW 17777	/AC=ONES
03244	504502		AND K7S	/AC=ONES
03245	304455		ADD K40K	/40K
03246	244455		XOR K40K	
03247	740200		SZA	/AC=0
03250	740040	H996	HALT	/ERROR; ADD K40K TO -0 FAILED
03251	740400		SNL	
03252	740040	H997	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K20K, AC=ONES, L=1
03253	744002		CLL!CML	/L=1

03254	777777		LAW 17777	/AC=ONES
03255	504502		AND K7S	/AC=ONES
03256	304456		ADD K20K	/20K
03257	244456		XOR K20K	
03260	740200		SZA	/AC=0
03261	740040	H998	HALT	/ERROR; ADD K20K TO -0 FAILED
03262	740400		SNL	
03263	740040	H999	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K10K, AC=ONES, L=1
03264	744002		CLL:CML	/L=1
03265	777777		LAW 17777	/AC=ONES
03266	504502		AND K7S	/AC=ONES
03267	304457		ADD K10K	/10K
03270	244457		XOR K10K	
03271	740200		SZA	/AC=0
03272	740040	H1000	HALT	/ERROR; ADD K10, TO -0 FAILED
03273	740400		SNL	
03274	740040	H1001	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K4K, AC=ONES, L=1
				/L=1
03275	744002		CLL:CML	/AC=ONES
03276	777777		LAW 17777	/AC=ONES
03277	504502		AND K7S	/AC=ONES
03300	304460		ADD K4K	/4K
03301	244460		XOR K4K	
03302	740200		SZA	/AC=0
03303	740040	H1002	HALT	/ERROR; ADD K4K TO -0 FAILED
03304	740400		SNL	
03305	740040	H1003	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K2K, AC=ONES, L=1
				/L=1
03306	744002		CLL:CML	/AC=ONES
03307	777777		LAW 17777	/AC=ONES
03310	504502		AND K7S	/AC=ONES
03311	304461		ADD K2K	/2K
03312	244461		XOR K2K	
03313	740200		SZA	/AC=0
03314	740040	H1004	HALT	/ERROR; ADD K2K TO -0 FAILED
03315	740400		SNL	
03316	740040	H1005	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K1K, AC=ONES, L=1
				/L=1
03317	744002		CLL:CML	/AC=ONES
03320	777777		LAW 17777	/AC=ONES
03321	504502		AND K7S	/AC=ONES
03322	304462		ADD K1K	/1K
03323	244462		XOR K1K	
03324	740200		SZA	/AC=0
03325	740040	H1006	HALT	/ERROR; ADD K1K TO -0 FAILED
03326	740400		SNL	
03327	740040	H1007	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K400, AC=ONES, L=1
				/L=1
03330	744002		CLL:CML	/AC=ONES
03331	777777		LAW 17777	/AC=ONES
03332	504502		AND K7S	/AC=ONES
03333	304463		ADD K400	/400
03334	244463		XOR K400	
03335	740200		SZA	/AC=0

03336	740040	H1007A	HALT	/ERROR; ADD K400 TO -0 FAILED
03337	740400		SNL	
03340	740040	H1008	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K200, AC=ONES, L=1
03341	744002		CLL:CML	/L=1
03342	777777		LAW 17777	/AC=ONES
03343	504502		AND K7S	/AC=ONES
03344	304464		ADD K200	/200
03345	244464		XOR K200	
03346	740200		SZA	/AC=0
03347	740040	H1009	HALT	/ERROR; ADD K200 TO -0 FAILED
03350	740400		SNL	
03351	740040	H1010	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K100, AC=ONES, L=1
03352	744002		CLL:CML	/L=1
03353	777777		LAW 17777	/AC=ONES
03354	504502		AND K7S	/AC=ONES
03355	304465		ADD K100	/100
03356	244465		XOR K100	
03357	740200		SZA	/AC=0
03360	740040	H1011	HALT	/ERROR; ADD K100 TO -0 FAILED
03361	740400		SNL	
03362	740040	H1012	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K40, AC=ONES, L=1
03363	744002		CLL:CML	/L=1
03364	777777		LAW 17777	/AC=ONES
03365	504502		AND K7S	/AC=ONES
03366	304466		ADD K40	/40
03367	244466		XOR K40	
03370	740200		SZA	/AC=0
03371	740040	H1013	HALT	/ERROR; ADD K40 TO -0 FAILED
03372	740400		SNL	
03373	740040	H1014	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K20, AC=ONES, L=1
03374	744002		CLL:CML	/L=1
03375	777777		LAW 17777	/AC=ONES
03376	504502		AND K7S	/AC=ONES
03377	304467		ADD K20	/20
03400	244467		XOR K20	
03401	740200		SZA	/AC=0
03402	740040	H1015	HALT	/ERROR; ADD K20 TO -0 FAILED
03403	740400		SNL	
03404	740040	H1016	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K10, AC=ONES, L=1
03405	744002		CLL:CML	/L=1
03406	777777		LAW 17777	/AC=ONES
03407	504502		AND K7S	/AC=ONES
03410	304470		ADD K10	
03411	244470		XOR K10	
03412	740200		SZA	/AC=0
03413	740040	H1017	HALT	/ERROR; ADD K10 TO -0 FAILED
03414	740400		SNL	
03415	740040	H1018	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K4, AC=ONES, L=1
03416	744002		CLL:CML	/L=1

03417	777777		LAW 17777	/AC=ONES
03420	504502		AND K7S	/AC=ONES
03421	304471		ADD K4	/4
03422	244471		XOR K4	
03423	740200		SZA	/AC=0
03424	740040	H1019	HALT	/ERROR; ADD K4 T0 -0 FAILED
03425	740400		SNL	
03426	740040	H1020	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K2, AC=ONES, L=1
				/L=1
03427	744002		CLL:CML	
03430	777777		LAW 17777	/AC=ONES
03431	504502		AND K7S	/AC=ONES
03432	304472		ADD K2	/2
03433	244472		XOR K2	
03434	740200		SZA	/AC=0
03435	740040	H1021	HALT	/ERROR; ADD K2 T0-0 FAILED
03436	740400		SNL	
03437	740040	H1022	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K1, AC=ONES, L=1
				/L=1
03440	744002		CLL:CML	
03441	777777		LAW 17777	/AC=ONES
03442	504502		AND K7S	/AC=ONES
03443	304473		ADD K1	/1
03444	244473		XOR K1	
03445	740200		SZA	/AC=0
03446	740040	H1023	HALT	/ERROR; ADD K1 T0-0 FAILED
03447	740400		SNL	
03450	740040	H1024	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD K7S, AC=ONES, L=0
				/L=0
03451	744000		CLL	
03452	204502		LAC K7S	/AC=ONES
03453	304502		ADD K7S	/ONES
03454	740001		CMA	/AC=ONES
03455	740200		SZA	/AC=0
03456	740040	H1025	HALT	/ERROR; ADD K7S TO ALL ONES FAILED
03457	741400		SZL	
03460	740040	H1026	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD 525253, AC=252525, L=1
				/L=1
03461	744002		CLL:CML	
03462	204535		LAC K2525	/AC=252525
03463	304632		ADD K5253	/5252
03464	244473		XOR K1	/000001
03465	740200		SZA	/AC=0
03466	740040	H1027	HALT	/ERROR; ADD K5253 TO K5252 FAILED
03467	740400		SNL	
03470	740040	H1028	HALT	/ERROR; LINK NOT A ONE, LINK RESET
				/TEST ADD 252525, AC=525253 L=0
				/L=0
03471	744000		CLL	
03472	204632		LAC K5253	/AC=525253
03473	304535		ADD K2525	/252525
03474	244473		XOR K1	/000001
03475	740200		SZA	/AC=0
03476	740040	H1029	HALT	/ERROR; ADD K2525 TO K5253 FAILED
03477	741400		SZL	
03500	740040	H1030A	HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED

03501	744000		CLL	/TEST ADD SERIES
03502	304474		ADD K1S	/L=0
03503	304475		ADD K2S	/AC=111111
03504	304476		ADD K3S	/AC=333333
03505	304477		ADD K4S	/AC=666666
03506	304500		ADD K5S	/AC=333333, L=1
03507	304501		ADD K6S	/AC=111111,
03510	304502		ADD K7S	/AC=777777
03511	740001		CMA	/AC=777777,
03512	740200		SZA	/AC=0
03513	740040	H1030B	HALT	/ERROR; ADD SERIES FAILED
03514	740400		SNL	
03515	740040	H1030C	HALT	/ERROR; LINK NOT A 1
				/TEST SAD K0S, AC=K400K
03516	204452		LAC K400K	/AC=400K
03517	544451		SAD K0	/0
03520	740040	H1030D	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=400K
				/TEST SAD K0S, AC=K200K
03521	204453		LAC K200K	/AC=200K
03522	544451		SAD K0	/0
03523	740040	H1030E	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K200K
				/TEST SAD K0S, AC=K100K
03524	204454		LAC K100K	/AC=100K
03525	544451		SAD K0	/0
03526	740040	H1030F	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K100K
				/TEST SAD K0S, AC=K40K
03527	204455		LAC K40K	/AC=40K
03530	544451		SAD K0	/0
03531	740040	H1030G	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=40K
				/TEST SAD K0S, AC=K20K
03532	204456		LAC K20K	/AC=20K
03533	544451		SAD K0	/0
03534	740040	H1030H	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K20K
				/TEST SAD K0S, AC=K10K
03535	204457		LAC K10K	/AC=10K
03536	544451		SAD K0	/0
03537	740040	H1030I	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=10K
				/TEST SAD K0S, AC=K4K
03540	204460		LAC K4K	/AC=4K
03541	544451		SAD K0	/0
03542	740040	H1030J	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K4K
				/TEST SAD K0S, AC=K2K
03543	204461		LAC K2K	/AC=2K
03544	544451		SAD K0	/0
03545	740040	H1030K	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K2K
				/TEST SAD K0S, AC=K1K
03546	204462		LAC K1K	/AC=1K
03547	544451		SAD K0	/0
03550	740040	H1030L	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K1K
				/TEST SAD K0S, AC=K400
03551	204463		LAC K400	/AC=400
03552	544451		SAD K0	/0
03553	740040	H1030M	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K400
				/TEST SAD K0S, AC=K200

03554	204464		LAC K200	/AC=200
03555	544451		SAD K0	/0
03556	740040	H1031	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K200
				/TEST SAD K0S, AC=K100
03557	204465		LAC K100	/AC=100
03560	544451		SAD K0	/0
03561	740040	H1032	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K100
				/TEST SAD K0S, AC=K40
03562	204466		LAC K40	/AC=40
03563	544451		SAD K0	/0
03564	740040	H1033	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K40
				/TEST SAD K0S, AC=K20
03565	204467		LAC K20	/AC=20
03566	544451		SAD K0	/0
03567	740040	H1034	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K20
				/TEST SAD K0S, AC=K10
03570	204470		LAC K10	/AC=10
03571	544451		SAD K0	/0
03572	740040	H1035	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K10
				/TEST SAD K0S, AC=K4
03573	204471		LAC K4	/AC=4
03574	544451		SAD K0	/0
03575	740040	H1035A	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K4
				/TEST SAD K0S, AC=K2
03576	204472		LAC K2	/AC=2
03577	544451		SAD K0	/0
03600	740040	H1036	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K2
				/TEST SAD K0S, AC=K1
03601	204473		LAC K1	/AC=1
03602	544451		SAD K0	/0
03603	740040	H1037	HALT	/ERROR; SAD K0S FAILED TO SKIP, AC=K1
				/TEST SAD K400K, AC=0
03604	750000		CLA	/AC=0
03605	544452		SAD K400K	/400K
03606	740040	H1037A	HALT	/ERROR; SAD K400K FAILED TO SKIP, AC=0
				/TEST SAD K200K, AC=0
03607	750000		CLA	/AC=0
03610	544453		SAD K200K	/200K
03611	740040	H1038	HALT	/ERROR; SAD K200K FAILED TO SKIP, AC=0
				/TEST SAD K100K, AC=0
03612	750000		CLA	/AC=0
03613	544454		SAD K100K	/100K
03614	740040	H1039	HALT	/ERROR; SAD K100K FAILED TO SKIP, AC=0
				/TEST SAD K40K, AC=0
03615	750000		CLA	/AC=0
03616	544455		SAD K40K	/40K
03617	740040	H1040	HALT	/ERROR; SAD K40K FAILED TO SKIP, AC=0
				/TEST SAD K20K, AC=0
			/PDP-15 INSTRUCTION TEST PART 1 - TAPE 6	
03620	750000		CLA	/AC=0
03621	544456		SAD K20K	/20K
03622	740040	H1041	HALT	/ERROR; SAD K10K FAILED TO SKIP, AC=0
				/TEST SAD K10K, AC=0
03623	750000		CLA	/AC=0
03624	544457		SAD K10K	/10K

03625	740040	H1042	HALT	/ERROR; SAD K10, FAILED TO SKIP, AC=0
				/TEST SAD K4K, AC=0
03626	750000		CLA	/AC=0
03627	544460		SAD K4K	/4K
03630	740040	H1043	HALT	/ERROR; SAD K4K FAILED TO SKIP, AC=0
				/TEST SAD K4K, AC=0
03631	750000		CLA	/AC=0
03632	544461		SAD K2K	/2K
03633	740040	H1044	HALT	/ERROR; SAD K2K FAILED TO SKIP, AC=0
				/TEST SADDK1K, AC=0
03634	750000		CLA	/AC=0
03635	544462		SAD K1K	/1K
03636	740040	H1045	HALT	/ERROR; SAD K1K FAILED TO SKIP, AC=0
				/TEST SAD K400, AC=0
03637	750000		CLA	/AC=0
03640	544463		SAD K400	/400
03641	740040	H1045A	HALT	/ERROR; SAD K400 FAILED TO SKIP, AC=0
				/TEST SAD K200, AC=0
03642	750000		CLA	/AC=0
03643	544464		SAD K200	/200
03644	740040	H1046	HALT	/ERROR; SAD K200 FAILED TO SKIP, AC=0
				/TEST SAD K100, AC=0
03645	750000		CLA	/AC=0
03646	544465		SAD K100	/100
03647	740040	H1047	HALT	/ERROR; SAD K100 FAILED TO SKIP, AC=0
				/TEST SAD K40, AC=0
03650	750000		CLA	/AC=0
03651	544466		SAD K40	/40
03652	740040	H1048	HALT	/ERROR; SAD K400 FAILED TO SKIP, AC=0
				/TEST SAD K20, AC=0
03653	750000		CLA	/AC=0
03654	544467		SAD K20	/20
03655	740040	H1049	HALT	/ERROR; SAD K20 FAILED TO SKIP, AC=0
				/TEST SAD K10, AC=0
03656	750000		CLA	/AC=0
03657	544470		SAD K10	/10
03660	740040	H1050	HALT	/ERROR; SAD K10 FAILED TO SKIP, AC=0
				/TEST SAD K4, AC=0
03661	750000		CLA	/AC=0
03662	544471		SAD K4	/4
03663	740040	H1051	HALT	/ERROR; SAD K4 FAILED TO SKIP, AC=0
				/TEST SAD K2, AC=0
03664	750000		CLA	/AC=0
03665	544472		SAD K2	/2
03666	740040	H1052	HALT	/ERROR; SAD K2 FAILED TO SKIP, AC=0
				/TEST SAD K1, AC=0
03667	750000		CLA	/AC=0
03670	544473		SAD K1	/1
03671	740040	H1053	HALT	/ERROR; SAD K1 FAILED TO SKIP, AC=0
				/TEST SAD M400K, AC=ONES
03672	777777		LAW 17777	/AC=ONES
03673	544503		SAD M400K	/377777
03674	740040	H1054	HALT	/ERROR; SAD M400K FAILED TO SKIP, AC=ONES
				/TEST SAD M200K, AC=ONES
03675	777777		LAW 17777	/AC=ONES

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03676	544504		SAD M200K	/577777
03677	740040	H1055	HALT	/ERROR; SAD M200K FAILED TO SKIP, AC=ONES
03700	777777		LAW 17777	/TEST SAD M100K, AC=ONES
03701	544505		SAD M100K	/AC=ONES
03702	740040	H1056	HALT	/677777
03703	777777		LAW 17777	/ERROR; SAD M100K FAILED TO SKIP, AC=ONES
03704	544506		SAD M40K	/TEST SAD M40K, AC=ONES
03705	740040	H1057	HALT	/AC=ONES
03706	777777		LAW 17777	/737777
03707	544507		SAD M20K	/ERROR; SAD M40K FAILED TO SKIP, AC=ONES
03710	740040	H1058	HALT	/TEST SAD M20K, AC=ONES
03711	777777		LAW 17777	/AC=ONES
03712	544510		SAD M10K	/7577777
03713	740040	H1059	HALT	/ERROR; SAD M20K FAILED TO SKIP, AC=ONES
03714	777777		LAW 17777	/TEST SAD M10K, AC=ONES
03715	544511		SAD M4K	/AC=ONES
03716	740040	H1060	HALT	/767777
03717	777777		LAW 17777	/ERROR; SAD M10K FAILED TO SKIP, AC=ONES
03720	544512		SAD M2K	/TEST SAD M4K, AC=ONES
03721	740040	H1061	HALT	/AC=ONES
03722	777777		LAW 17777	/773777
03723	544513		SAD M1K	/ERROR; SAD M4K FAILED TO SKIP, AC=ONES
03724	740040	H1062	HALT	/TEST SAD M2K, AC=ONES
03725	777777		LAW 17777	/AC=ONES
03726	544514		SAD M400	/775777
03727	740040	H1063	HALT	/ERROR; SAD M2K FAILED TO SKIP, AC=ONES
03730	777777		LAW 17777	/TEST SAD M1K, AC=ONES
03731	544515		SAD M200	/AC=ONES
03732	740040	H1064	HALT	/776777
03733	777777		LAW 17777	/ERROR; SAD M1K FAILED TO SKIP, AC=ONES
03734	544516		SAD M100	/TEST SAD M400, AC=ONES
03735	740040	H1065	HALT	/AC=ONES
03736	777777		LAW 17777	/777377
03737	544517		SAD M40	/ERROR; SAD M400 FAILED TO SKIP, AC=ONES
03740	740040	H1066	HALT	/TEST SAD M200, AC=ONES
03741	777777		LAW 17777	/AC=ONES
03742	544520		SAD M20	/777677
03743	740040	H1067	HALT	/ERROR; SAD M100 FAILED TO SKIP, AC=ONES
03744	777777		LAW 17777	/TEST SAD M40, AC=ONES
03745	544521		SAD M10	/AC=ONES
03746	740040	H1068	HALT	/777737
				/ERROR; SAD M40 FAILED TO SKIP, AC=ONES
				/TEST SAD M20, AC=ONES
				/AC=ONES
				/777757
				/ERROR; SAD M20 FAILED TO SKIP, AC=ONES
				/TEST SAD M10, AC=ONES
				/AC=ONES
				/777767
				/ERROR; SAD M10 FAILED TO SKIP AC=ONES
				/TEST SAD M4, AC=ONES

03747	777777		LAW 17777	/AC=ONES
03750	544522		SAD M4	/777773
03751	740040	H1069	HALT	/ERROR; SAD M4 FAILED TO SKIP, AC=ONES
				/TEST SAD M2, AC=ONES
03752	777777		LAW 17777	/AC=ONES
03753	544523		SAD M2	/777775
03754	740040	H1070	HALT	/ERROR; SAD M2 FAILED TO SKIP, AC=ONES
				/TEST SAD M1, AC=ONES
03755	777777		LAW 17777	/AC=ONES
03756	544524		SAD M1	/777776
03757	740040	H1071	HALT	/ERROR; SAD M1 FAILED TO SKIP, AC=ONES
				/TEST SAD K7S, AC=M400K
03760	750000		CLA	/AC=0
03761	344503		TAD M400K	/AC=377777
03762	544502		SAD K7S	/ONES
03763	740040	H1072	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M400K
				/TEST SAD K7S, AC=M200K
03764	750000		CLA	/AC=0
03765	344504		TAD M200K	/AC=577777
03766	544502		SAD K7S	/ONES
03767	740040	H1073	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M200K
				/TEST SAD K7S, AC=M100K
03770	750000		CLA	/AC=0
03771	344505		TAD M100K	/AC=677777
03772	544502		SAD K7S	/ONES
03773	740040	H1074	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M100K
				/TEST SAD K7S, AC=M40K
03774	750000		CLA	/AC=0
03775	344506		TAD M40K	/AC=737777
03776	544502		SAD K7S	/ONES
03777	740040	H1075	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M40K
				/TEST SAD K7S, AC=M20K
04000	750000		CLA	/AC=0
04001	344507		TAD M20K	/AC=757777
04002	544502		SAD K7S	/ONES
04003	740040	H1076	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M20K
				/TEST SAD K7S, AC=M10K
04004	750000		CLA	/AC=0
04005	344510		TAD M10K	/AC=767777
04006	544502		SAD K7S	/ONES
04007	740040	H1078	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M10K
				/TEST SAD K7S, AC=M4K
04010	750000		CLA	/AC=0
04011	344511		TAD M4K	/AC=773777
04012	544502		SAD K7S	/ONES
04013	740040	H1079	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M4K
				/TEST SAD K7S, AC=M2K
04014	750000		CLA	/AC=0
04015	344512		TAD M2K	/AC=775777
04016	544502		SAD K7S	/ONES
04017	740040	H1080	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M2K
				/TEST SAD K7S, AC=M1K
04020	750000		CLA	/AC=0
04021	344513		TAD M1K	/AC=776777
04022	544502		SAD K7S	/ONES

04023	740040	H1081	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M1K /TEST SAD K7S, AC=M400 /AC=0
04024	750000		CLA	
04025	344514		TAD M400	/AC=777377
04026	544502		SAD K7S	/ONES
04027	740040	H1082	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M400 /TEST SAD K7S AC=M200 /AC=0
04030	750000		CLA	
04031	344515		TAD M200	/AC=777577
04032	544502		SAD K7S	/ONES
04033	740040	H1083	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M200 /TEST SAD K7S, AC=M100 /AC=0
04034	750000		CLA	
04035	344516		TAD M100	/AC=777677
04036	544502		SAD K7S	/ONES
04037	740040	H1084	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M100 /TEST SAD K7S, AC=M40
04040	750000		CLA	/AC=0
04041	344517		TAD M40	/AC=777737
04042	544502		SAD K7S	/ONES
04043	740040	H1085	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M40 /TEST SAD K7S, AC=M20 /AC=0
04044	750000		CLA	
04045	344520		TAD M20	/AC=777757
04046	544502		SAD K7S	/ONES
04047	740040	H1086	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M20 /TEST SAD K7S, AC=M10 /AC=0
04050	750000		CLA	
04051	344521		TAD M10	/AC=777767
04052	544502		SAD K7S	/ONES
04053	740040	H1087	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M10 /TEST SAD K7S, AC=M4 /AC=0
04054	750000		CLA	
04055	344522		TAD M4	/AC=777773
04056	544502		SAD K7S	/ONES
04057	740040	H1088	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M4 /TEST SAD K7S, AC=M2 /AC=0
04060	750000		CLA	
04061	344523		TAD M2	/AC=777775
04062	544502		SAD K7S	/ONES
04063	740040	H1089	HALT	/ERROR; SAD K7S FAILED TO SKIP, AC=M2 /TEST SAD K7S, AC=M1 /AC=0
04064	750000		CLA	
04065	344524		TAD M1	/AC=777776
04066	544502		SAD K7S	/ONES
04067	740040	H1090	HALT	/ERROR; SAD K7S FAILED TO SKIP AC=M1 /TEST SAD K400K, AC=K400K
04070	204452		LAC K400K	/AC=400K
04071	544452		SAD K400K	/400K
04072	741000		SKP	
04073	740040	H1091	HALT	/ERROR; SAD K400K SKIPPED, AC=K400K /TEST SAD K200K, AC=K200K
04074	204453		LAC K200K	/AC=200K
04075	544453		SAD K200K	/200K
04076	741000		SKP	

04077	740040	H1092	HALT	/ERROR; SAD K200K SKIPPED, AC=K200K /TEST SAD K100K, AC=100K
04100	204454		LAC K100K	/AC=100K
04101	544454		SAD K100K	/100K
04102	741000		SKP	
04103	740040	H1093	HALT	/ERROR; SAD K100K SKIPPED, AC=K100K /TEST SAD K40K, AC=K40K
04104	204455		LAC K40K	/AC=40K
04105	544455		SAD K40K	/40K
04106	741000		SKP	
04107	740040	H1094	HALT	/ERROR; SAD K40K SKIPPED, AC=K40K /TEST SAD K20K, AC=K20K
04110	204456		LAC K20K	/AC=20K
04111	544456		SAD K20K	/20K
04112	741000		SKP	
04113	740040	H1095	HALT	/ERROR; SAD K10K SKIPPED, AC=K20K /TEST SAD K10K, AC=K10K
04114	204457		LAC K10K	/AC=10K
04115	544457		SAD K10K	/10K
04116	741000		SKP	
04117	740040	H1096	HALT	/ERROR; SAD K10K SKIPPED, AC=K10K /TEST SAD K4K, AC=K4K
04120	204460		LAC K4K	/AC4K
04121	544460		SAD K4K	/4K
04122	741000		SKP	
04123	740040	H1097	HALT	/ERROR; SAD K4K SKIPPED, AC=K4K /TEST SAD K2K, AC=K2K
04124	204461		LAC K2K	/AC=2K
04125	544461		SAD K2K	/2K
04126	741000		SKP	
04127	740040	H1098	HALT	/ERROR; SAD K2K SKIPPED, AC=K2K /TEST SAD K1K, AC=K1K
04130	204462		LAC K1K	/AC=1K
04131	544462		SAD K1K	/1K
04132	741000		SKP	
04133	740040	H1099	HALT	/ERROR; SAD 1K SKIPPED, AC=K1K /TEST SAD K400, AC=K400
04134	204463		LAC K400	/AC=400
04135	544463		SAD K400	/400
04136	741000		SKP	
04137	740040	H1100	HALT	/ERROR; SAD K400 SKIPPED, AC=K400 /TEST SAD K200, AC=K200
04140	204464		LAC K200	/AC=200
04141	544464		SAD K200	/200
04142	741000		SKP	
04143	740040	H1101	HALT	/ERROR; SAD K200 SKIPPED, AC=K200 /TEST SAD K100, AC=K100
04144	204465		LAC K100	/AC=100
04145	544465		SAD K100	/100
04146	741000		SKP	
04147	740040	H1102	HALT	/ERROR; SAD K100 SKIPPED, AC=K2 /TEST SAD K40, AC=K40
04150	204466		LAC K40	/AC=40
04151	544466		SAD K40	/40
04152	741000		SKP	

PAGE	44	15LITA	15LIT1A	
04153	740040	H1103	HALT	/ERROR; SAD K40 SKIPPED, AC=K40 /TEST SAD K20, AC=K20
04154	204467		LAC K20	/AC=20
04155	544467		SAD K20	/20
04156	741000		SKP	
04157	740040	H1104	HALT	/ERROR; SAD K20 SKIPPED, AC=K20 /TEST SAD K10, AC=K10
04160	204470		LAC K10	/AC=10
04161	544470		SAD K10	/10
04162	741000		SKP	
04163	740040	H1105	HALT	/ERROR; SAD K10 SKIPPED, AC=K10 /TEST SAD K4, AC=K4
04164	204471		LAC K4	/AC=4
04165	544471		SAD K4	/4
04166	741000		SKP	
04167	740040	H1106	HALT	/ERROR; SAD K4 SKIPPED, AC=K4 /TEST SAD K2, AC=K2
04170	204472		LAC K2	/AC=2
04171	544472		SAD K2	/2
04172	741000		SKP	
04173	740040	H1107	HALT	/ERROR; SAD K2 SKIPPED, AC=K2 /TEST SAD K1, AC=K1
04174	204473		LAC K1	/AC=1
04175	544473		SAD K1	/1
04176	741000		SKP	
04177	740040	H1108	HALT	/ERROR; SAD K1 SKIPPED, AC=K1 /TEST SAD M400K, AC=M400K L=0
04200	754001		CLA!CMA!CLL	/AC=ONES, L=0
04201	304503		ADD M400K	/AC=377777 L=1
04202	544503		SAD M400K	/377777
04203	741000		SKP	
04204	740040	H1109	HALT	/ERROR; SAD M400K SKIPPED, AC=M400K
04205	741400		SZL	
04206	740040	H1110	HALT	/ERROR; LINK NOT A 0 /TEST SAD M200K, AC=M200K L=0
04207	754001		CLA!CMA!CLL	/AC=ONES, L=0
04210	304504		ADD M200K	/AC=577777 L=1
04211	544504		SAD M200K	/577777
04212	741000		SKP	
04213	740040	H1111	HALT	/ERROR; SAD M200K SKIPPED, AC=M200K
04214	741400		SZL	
04215	740040	H1112	HALT	/ERROR; LINK NOT A 0 /TEST SAD M100K, AC=M100K L=0
04216	754001		CLA!CMA!CLL	/AC=ONES, L=0
04217	304505		ADD M100K	/AC=677777 L=1
04220	544505		SAD M100K	/677777
04221	741000		SKP	
04222	740040	H1113	HALT	/ERROR; SAD M100K SKIPPED, AC=M100K
04223	741400		SZL	
04224	740040	H1114	HALT	/ERROR; LINK NOT A 0 /TEST SAD M40K, AC=M40K L=0
04225	754001		CLA!CMA!CLL	/AC=ONES, L=0
04226	304506		ADD M40K	/AC=737777 L=1
04227	544506		SAD M40K	/737777
04230	741000		SKP	

04231	740040	H1115	HALT	/ERROR; SAD M40K SKIPPED, AC=M40K
04232	741400		SZL	
04233	740040	H1116	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M20K, AC=M20K L=0
04234	754001		CLA!CMA!CLL	/AC=ONES, L=0
04235	304507		ADD M20K	/AC=757777 L=1
04236	544507		SAD M20K	/757777
04237	741000		SKP	
04240	740040	H1117	HALT	/ERROR; SAD M20K SKIPPED, AC=M20K
04241	741400		SZL	
04242	740040	H1118	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M100K, AC=M10K L=0
04243	754001		CLA!CMA!CLL	/AC=ONES, L=0
04244	304510		ADD M10K	/AC=767777 L=1
04245	544510		SAD M10K	/767777
04246	741000		SKP	
04247	740040	H1119	HALT	/ERROR; SAD M10K SKIPPED, AC=M10K
04250	741400		SZL	
04251	740040	H1120	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M4K, AC=M4K L=0
04252	754001		CLA!CMA!CLL	/AC=ONES, L=0
04253	304511		ADD M4K	/AC=773777 L=1
04254	544511		SAD M4K	/773777
04255	741000		SKP	
04256	740040	H1121	HALT	/ERROR; SAD M4K SKIPPED, ACM4K
04257	741400		SZL	
04260	740040	H1122	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M2K, AC=M2K L=0
04261	754001		CLA!CMA!CLL	/AC=ONES, L=0
04262	304512		ADD M2K	/AC=775777 L=1
04263	544512		SAD M2K	/775777
04264	741000		SKP	
04265	740040	H1123	HALT	/ERROR; SAD M2K SKIPPED, AC=M2K
04266	741400		SZL	
04267	740040	H1124	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M1K, AC=M1K L=0
04270	754001		CLA!CMA!CLL	/AC=ONES, L=0
04271	304513		ADD M1K	/AC=776777 L=1
04272	544513		SAD M1K	/776777
04273	741000		SKP	
04274	740040	H1125	HALT	/ERROR; SAD M1K SKIPPED, AC=M1K
04275	741400		SZL	
04276	740040	H1126	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M400, AC=M400 L=0
04277	754001		CLA!CMA!CLL	/AC=ONES, L=0
04300	304514		ADD M400	/AC=777377 L=1
04301	544514		SAD M400	/777377
04302	741000		SKP	
04303	740040	H1127	HALT	/ERROR; SAD M400 SKIPPED, AC=M400
04304	741400		SZL	
04305	740040	H1128	HALT	/ERROR; LINK NOT A 0
				/TEST SAD M200, AC=M200 L=0
04306	754001		CLA!CMA!CLL	/AC=ONES, L=0
04307	304515		ADD M200	/AC=777577 L=1
04310	544515		SAD M200	/777577

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04311	741000			SKP	
04312	740040	H1129		HALT	/ERROR; SAD M200 SKIPPED, AC=M200
04313	741400			SZL	
04314	740040	H1130		HALT	/ERROR; LINK NOT A 0
04315	754001			CLA:CMA:CLL	/TEST SAD M100, AC=M100 L=0
04316	304516			ADD M100	/AC=ONES, L=0
04317	544516			SAD M100	/AC=777677 L=1
04320	741000			SKP	/777677
04321	740040	H1131		HALT	/ERROR; SAD M100 SKIPPED, AC=M100
04322	741400			SZL	
04323	740040	H1132		HALT	/ERROR; LINK NOT A 0
04324	754001			CLA:CMA:CLL	/TEST SAD M40, AC=M40 L=0
04325	304517			ADD M40	/AC=ONES, L=0
04326	544517			SAD M40	/AC=777737 L=1
04327	741000			SKP	/777737
04330	740040	H1133		HALT	/ERROR; SAD M40 SKIPPED, AC=M40
04331	741400			SZL	
04332	740040	H1134		HALT	/ERROR; LINK NOT A 0
04333	754001			CLA:CMA:CLL	/TEST SAD M20, AC=M20 L=0
04334	304520			ADD M20	/AC=ONES, L=0
04335	544520			SAD M20	/AC=777757 L=1
04336	741000			SKP	/777757
04337	740040	H1135		HALT	/ERROR; SAD M20 SKIPPED, AC=M20
04340	741400			SZL	
04341	740040	H1136		HALT	/ERROR; LINK NOT A 0
04342	754001			CLA:CMA:CLL	/TEST SAD M10, AC=M10 L=0
04343	304521			ADD M10	/AC=ONES, L=0
04344	544521			SAD M10	/AC=777767 L=1
04345	741000			SKP	/777767
04346	740040	H1137		HALT	/ERROR; SAD M10 SKIPPED, AC=M10
04347	741400			SZL	
04350	740040	H1138		HALT	/ERROR; LINK NOT A 0
04351	754001			CLA:CMA:CLL	/TEST SAD M4, AC=M4 L=0
04352	304522			ADD M4	/AC=ONES, L=0
04353	544522			SAD M4	/AC=777773 L=1
04354	741000			SKP	/777773
04355	740040	H1139		HALT	/ERROR; SAD M4 SKIPPED, AC=M4
04356	741400			SZL	
04357	740040	H1140		HALT	/ERROR; LINK NOT A 0
04360	754001			CLA:CMA:CLL	/TEST SAD M2, AC=M2 L=0
04361	304523			ADD M2	/AC=ONES, L=0
04362	544523			SAD M2	/AC=777775 L=1
04363	741000			SKP	/777775
04364	740040	H1141		HALT	/ERROR; SAD M2 SKIPPED, AC=M2
04365	741400			SZL	
04366	740040	H1142		HALT	/ERROR; LINK NOT A 0
04367	754001			CLA:CMA:CLL	/TEST SAD M1, AC=M1 L=0
04370	304524			ADD M1	/AC=ONES, L=0
					/AC=777776 L=1

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04371 544524          SAD M1          /777776
04372 741000          SKP
04373 740040    H1143  HALT          /ERROR: SAD M1 SKIPPED, AC=M1
04374 741400          SZL
04375 740040    H1144  HALT          /ERROR: LINK NOT A 0
                                /TEST SAD SERIES
04376 204451          LAC K0          /AC=0
04377 544502          SAD K7S         /ONES
04400 760001          LAW 1          /760001
04401 544502          SAD K7S         /ONES
04402 760002          LAW 2          /760002
04403 544502          SAD K7S         /ONES
04404 760004          LAW 4          /760004
04405 544502          SAD K7S         /ONES
04406 760010          LAW 10         /760010
04407 544502          SAD K7S         /ONES
04410 760020          LAW 20         /760020
04411 544502          SAD K7S         /ONES
04412 760040          LAW 40         /760040
04413 740200          SZA
04414 740040    H1145  HALT          /ERROR: SAD SERIES FAILED, AC NOT 0
                                /TEST SAD-SKP SERIES
04415 750000          CLA          /AC=0
04416 544502          SAD K7S         /ONES
04417 760001          LAW 1          /760001
04420 741000          SKP
04421 760002          LAW 2          /760002
04422 544502          SAD K7S         /ONES
04423 760004          LAW 4          /760004
04424 741000          SKP
04425 760010          LAW 10         /760010
04426 544502          SAD K7S         /ONES
04427 760020          LAW 20         /760020
04430 740200          SZA
04431 740040    H1146  HALT          /ERROR: SAD-SKP SERIES FAILED, AC NOT 0
04432 204636          LAC COUNT
04433 344473          TAD K1
04434 044636          DAC COUNT
04435 740200          SZA
04436 600201          JMP PART1A
04437 750004          LAS          /DON'T RING BELL IF ACS0=1
04440 741100          SPA
04441 600201          JMP PART1A
04442 760207          LAW 207
04443 700406          TLS
04444 700401          TSF
04445 604444          JMP .-1
04446 204602    BEGIN  LAC M777
04447 044636          DAC COUNT
04450 600201          JMP PART1A
/PDR-15 INSTRUCTION TEST PART 1 AND 1A - LAST TAPE
/CONSTANTS
740000  NOP1=NOP
740000  NOP2=NOP
740000  NOP3=NOP

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	740040	HALT=HLT	
04451	000000	K0	0
04452	400000	K400K	400000
04453	200000	K200K	200000
04454	100000	K100K	100000
04455	040000	K40K	40000
04456	020000	K20K	20000
04457	010000	K10K	10000
04460	004000	K4K	4000
04461	002000	K2K	2000
04462	001000	K1K	1000
04463	000400	K400	400
04464	000200	K200	200
04465	000100	K100	100
04466	000040	K40	40
04467	000020	K20	20
04470	000010	K10	10
04471	000004	K4	4
04472	000002	K2	2
04473	000001	K1	1
04474	111111	K1S	111111
04475	222222	K2S	222222
04476	333333	K3S	333333
04477	444444	K4S	444444
04500	555555	K5S	555555
04501	666666	K6S	666666
04502	777777	K7S	777777
04503	377777	M400K	377777
04504	577777	M200K	577777
04505	677777	M100K	677777
04506	737777	M40K	737777
04507	757777	M20K	757777
04510	767777	M10K	767777
04511	773777	M4K	773777
04512	775777	M2K	775777
04513	776777	M1K	776777
04514	777377	M400	777377
04515	777577	M200	777577
04516	777677	M100	777677
04517	777737	M40	777737
04520	777757	M20	777757
04521	777767	M10	777767
04522	777773	M4	777773
04523	777775	M2	777775
04524	777776	M1	777776
04525	377776	M400KM	377776
04526	577776	M200KM	577776
04527	677776	M100KM	677776
04530	737776	M40KM	737776
04531	757776	M20KM	757776
04532	767776	M10KM	767776
04533	773776	M4KM	773776
04534	525252	K5252	525252
04535	252525	K2525	252525
04536	775776	M2KM	775776

04537	776776	M1KM	776776
04540	777376	M400M	777376
04541	777576	M200M	777576
04542	777676	M100M	777676
04543	777736	M40M	777736
04544	777756	M20M	777756
04545	777766	M10M	777766
04546	777772	M4M	777772
04547	777774	M2M	777774
04550	777775	M1M	777775
04551	000003	K3	3
04552	000007	K7	7
04553	000017	K17	17
04554	000037	K37	37
04555	000077	K77	77
04556	000177	K177	177
04557	000377	K377	377
04560	000777	K777	777
04561	001777	K1777	1777
04562	003777	K3777	3777
04563	007777	K7777	7777
04564	017777	K17777	17777
04565	037777	K37777	37777
04566	077777	K77777	77777
04567	177777	M600K	177777
04570	777774	M3	777774
04571	777770	M7	777770
04572	777760	M17	777760
04573	777740	M37	777740
04574	777700	M77	777700
04575	777600	M177	777600
04576	777400	M377	777400
04577	777000	M777	777000
04600	776000	M1777	776000
04601	774000	M3777	774000
04602	770000	M7777	770000
04603	760000	M17777	760000
04604	740000	M37777	740000
04605	700000	M77777	700000
04606	600000	K600K	600000
04607	777000	K777K	777000
04610	000005	K5	5
04611	000011	K11	11
04612	000021	K21	21
04613	000041	K41	41
04614	000101	K101	101
04615	000201	K201	201
04616	000401	K401	401
04617	001001	K1001	1001
04620	002001	K2001	2001
04621	004001	K4001	4001
04622	010001	K10001	10001
04623	740040	KHALT	740040
04624	017760	KCAL0	17760
04625	002021	K2021	2021

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04626	002120	K2120	2120
04627	617760	KCALE	617760
04630	741000	KSKP	SKP
04631	750000	KCLA	CLA
04632	525253	K5253	525253
04633	060000	K60K	60000
04634	625253	K6625	625253
04635	615253	K615	615253
04636	770000	COUNT	770000
	000000		.END

SIZE=04637

NO ERROR LINES

REGIN	04446
CLOF	700004
CLON	700044
CLSF	700001
COUNT	04636
FEM	707702
HALT	740040
H1000	03272
H1001	03274
H1002	03303
H1003	03305
H1004	03314
H1005	03316
H1006	03325
H1007	03327
H1007A	03336
H1008	03340
H1009	03347
H1010	03351
H1011	03360
H1012	03362
H1013	03371
H1014	03373
H1015	03402
H1016	03404
H1017	03413
H1018	03415
H1019	03424
H1020	03426
H1021	03435
H1022	03437
H1023	03446
H1024	03450
H1025	03456
H1026	03460
H1027	03466
H1028	03470
H1029	03476
H1030A	03500
H1030B	03513
H1030C	03515
H1030D	03520
H1030E	03523
H1030F	03526
H1030G	03531
H1030H	03534
H1030I	03537
H1030J	03542
H1030K	03545
H1030L	03550
H1030M	03553
H1031	03556
H1032	03561
H1033	03564
H1034	03567

H1035	03572
H1035A	03575
H1036	03600
H1037	03603
H1037A	03606
H1038	03611
H1039	03614
H1040	03617
H1041	03622
H1042	03625
H1043	03630
H1044	03633
H1045	03636
H1045A	03641
H1046	03644
H1047	03647
H1048	03652
H1049	03655
H1050	03660
H1051	03663
H1052	03666
H1053	03671
H1054	03674
H1055	03677
H1056	03702
H1057	03705
H1058	03710
H1059	03713
H1060	03716
H1061	03721
H1062	03724
H1063	03727
H1064	03732
H1065	03735
H1066	03740
H1067	03743
H1068	03746
H1069	03751
H1070	03754
H1071	03757
H1072	03763
H1073	03767
H1074	03773
H1075	03777
H1076	04003
H1078	04007
H1079	04013
H1080	04017
H1081	04023
H1082	04027
H1083	04033
H1084	04037
H1085	04043
H1086	04047
H1087	04053

H1088 04057
H1089 04063
H1090 04067
H1091 04073
H1092 04077
H1093 04103
H1094 04107
H1095 04113
H1096 04117
H1097 04123
H1098 04127
H1099 04133
H1100 04137
H1101 04143
H1102 04147
H1103 04153
H1104 04157
H1105 04163
H1106 04167
H1107 04173
H1108 04177
H1109 04204
H1110 04206
H1111 04213
H1112 04215
H1113 04222
H1114 04224
H1115 04231
H1116 04233
H1117 04240
H1118 04242
H1119 04247
H1120 04251
H1121 04256
H1122 04260
H1123 04265
H1124 04267
H1125 04274
H1126 04276
H1127 04303
H1128 04305
H1129 04312
H1130 04314
H1131 04321
H1132 04323
H1133 04330
H1134 04332
H1135 04337
H1136 04341
H1137 04346
H1138 04350
H1139 04355
H1140 04357
H1141 04364
H1142 04366

H1143	04373
H1144	04375
H1145	04414
H1146	04431
H699	00204
H700	00206
H701	00212
H702	00220
H703	00226
H704	00234
H705	00242
H706	00250
H707	00256
H708	00264
H709	00272
H710	00300
H711	00306
H712	00314
H713	00322
H714	00330
H715	00336
H716	00344
H717	00352
H718	00360
H719	00366
H720	00373
H721	00400
H722	00405
H723	00412
H724	00417
H725	00424
H726	00431
H727	00436
H728	00443
H729	00450
H730	00455
H731	00462
H732	00467
H733	00474
H734	00501
H735	00506
H736	00513
H737	00520
H738	00525
H739	00532
H740	00537
H741	00544
H742	00551
H743	00556
H744	00563
H745	00570
H746	00575
H747	00602
H748	00607
H749	00614

H750	00621
H751	00626
H752	00633
H753	00640
H754	00645
H755	00652
H756	00660
H757	00662
H758	00670
H759	00672
H760	00700
H761	00702
H762	00710
H763	00712
H764	00720
H765	00722
H766	00730
H767	00732
H768	00740
H769	00742
H770	00750
H771	00752
H772	00760
H773	00762
H774	00770
H775	00772
H776	01000
H777	01002
H778	01010
H779	01012
H780	01020
H781	01022
H782	01030
H783	01032
H784	01040
H785	01042
H786	01050
H787	01052
H788	01060
H789	01062
H790	01070
H791	01072
H792	01100
H793	01102
H794	01110
H795	01112
H796	01120
H797	01122
H798	01130
H799	01132
H800	01140
H801	01142
H802	01150
H803	01152
H804	01160

H805	01162
H806	01170
H807	01172
H808	01200
H809	01202
H810	01210
H811	01212
H812	01220
H813	01222
H814	01230
H815	01232
H816	01240
H817	01242
H818	01250
H819	01252
H820	01260
H821	01262
H822	01270
H823	01272
H824	01300
H825	01302
H826	01310
H827	01312
H828	01320
H829	01326
H830	01334
H831	01342
H832	01350
H833	01356
H834	01364
H835	01372
H836	01400
H837	01406
H838	01413
H839	01415
H840	01422
H841	01427
H842	01434
H843	01441
H844	01446
H845	01453
H846	01460
H847	01465
H848	01472
H849	01477
H850	01504
H851	01511
H852	01516
H853	01523
H854	01530
H855	01535
H856	01542
H857	01547
H858	01554
H859	01556

H860	01563
H861	01565
H862	01572
H863	01574
H864	01601
H865	01603
H866	01610
H867	01612
H868	01617
H869	01621
H870	01626
H871	01630
H872	01635
H873	01637
H874	01644
H875	01646
H876	01653
H877	01655
H878	01662
H879	01664
H880	01671
H881	01673
H882	01700
H883	01702
H884	01707
H885	01711
H886	01716
H887	01720
H888	01725
H889	01727
H890	01734
H891	01736
H892	01743
H893	01745
H894	01753
H895	01755
H896	01763
H897	01765
H898	01773
H899	01775
H900	02003
H901	02005
H902	02013
H903	02015
H904	02023
H905	02025
H906	02033
H907	02035
H908	02043
H909	02045
H910	02053
H911	02055
H912	02063
H913	02065
H914	02073

H915	02075
H916	02103
H917	02105
H918	02113
H919	02115
H920	02123
H921	02125
H922	02133
H923	02135
H924	02143
H925	02145
H926	02153
H927	02155
H930	02163
H931	02165
H932	02172
H933	02174
H934	02202
H935	02204
H936	02212
H937	02214
H938	02222
H939	02224
H940	02232
H941	02234
H942	02242
H943	02244
H944	02252
H945	02254
H946	02262
H947	02264
H948	02272
H949	02274
H950	02302
H951	02304
H952A	02312
H952AA	02462
H952B	02314
H952BB	02464
H952C	02322
H952CC	02472
H952D	02324
H952DD	02474
H952E	02332
H952EE	02502
H952F	02334
H952FF	02504
H952G	02342
H952GG	02512
H952H	02344
H952HH	02514
H952I	02352
H952II	02522
H952J	02354
H952JJ	02524

H952K	02362
H952L	02364
H952M	02372
H952N	02374
H952O	02402
H952P	02404
H952Q	02412
H952R	02414
H952S	02422
H952T	02424
H952U	02432
H952V	02434
H952W	02442
H952X	02444
H952Y	02452
H952Z	02454
H953A	02532
H953AA	02702
H953B	02534
H953BB	02704
H953C	02542
H953CC	02712
H953D	02544
H953DD	02714
H953E	02552
H953EE	02722
H953F	02554
H953FF	02724
H953G	02562
H953GG	02732
H953H	02564
H953HH	02734
H953I	02572
H953II	02742
H953J	02574
H953JJ	02744
H953K	02602
H953L	02604
H953M	02612
H953N	02614
H953O	02622
H953P	02624
H953Q	02632
H953R	02634
H953S	02642
H953T	02644
H953U	02652
H953V	02654
H953W	02662
H953X	02664
H953Y	02672
H953Z	02674
H954	02753
H955	02755
H956	02764

H957	02766
H958	02775
H959	02777
H960	03006
H961	03010
H962	03017
H963	03021
H964	03030
H965	03032
H966	03041
H967	03043
H968	03052
H969	03054
H970	03063
H971	03065
H972	03074
H973	03076
H974	03105
H975	03107
H976	03116
H977	03120
H978	03127
H979	03131
H980	03140
H981	03142
H982	03151
H983	03153
H984	03162
H985	03164
H986	03173
H987	03175
H988	03204
H989	03206
H990	03215
H991	03217
H992	03226
H993	03230
H994	03237
H995	03241
H996	03250
H997	03252
H998	03261
H999	03263
KCALE	04627
KCAL0	04624
KCLA	04631
KHALT	04623
KRB	700312
KSF	700301
KSKP	04630
K0	04451
K1	04473
K1K	04462
K1S	04474
K10	04470

K10K	04457
K100	04465
K100K	04454
K10001	04622
K1001	04617
K101	04614
K11	04611
K17	04553
K177	04556
K1777	04561
K17777	04564
K2	04472
K2K	04461
K2S	04475
K20	04467
K20K	04456
K200	04464
K200K	04453
K2001	04620
K201	04615
K2021	04625
K21	04612
K2120	04626
K2525	04535
K3	04551
K3S	04476
K37	04554
K377	04557
K3777	04562
K37777	04565
K4	04471
K4K	04460
K4S	04477
K40	04466
K40K	04455
K400	04463
K400K	04452
K4001	04621
K401	04616
K41	04613
K5	04610
K5S	04500
K5252	04534
K5253	04632
K6S	04501
K60K	04633
K600K	04606
K615	04635
K6625	04634
K7	04552
K7S	04502
K77	04555
K777	04560
K777K	04607
K7777	04563

K77777	04566
LEM	707704
M1	04524
M1K	04513
M1KM	04537
M1M	04550
M10	04521
M10K	04510
M10KM	04532
M10M	04545
M100	04516
M100K	04505
M100KM	04527
M100M	04542
M17	04572
M177	04575
M1777	04600
M17777	04603
M2	04523
M2K	04512
M2KM	04536
M2M	04547
M20	04520
M20K	04507
M20KM	04531
M20M	04544
M200	04515
M200K	04504
M200KM	04526
M200M	04541
M3	04570
M37	04573
M377	04576
M3777	04601
M37777	04604
M4	04522
M4K	04511
M4KM	04533
M4M	04546
M40	04517
M40K	04506
M40KM	04530
M40M	04543
M400	04514
M400K	04503
M400KM	04525
M400M	04540
M600K	04567
M7	04571
M77	04574
M777	04577
M7777	04602
M77777	04605
NOP1	740000
NOP2	740000

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NOP3	740000
PART1A	00201
PCF	700202
PSA	700204
PSB	700244
PSF	700201
RCF	700102
RRB	700112
RSA	700104
RSB	700144
RSF	700101
TCF	700402
TLS	700406
TSF	700401
.EOT	00000

.EOT	00000
PART1A	00201
H699	00204
H700	00206
H701	00212
H702	00220
H703	00226
H704	00234
H705	00242
H706	00250
H707	00256
H708	00264
H709	00272
H710	00300
H711	00306
H712	00314
H713	00322
H714	00330
H715	00336
H716	00344
H717	00352
H718	00360
H719	00366
H720	00373
H721	00400
H722	00405
H723	00412
H724	00417
H725	00424
H726	00431
H727	00436
H728	00443
H729	00450
H730	00455
H731	00462
H732	00467
H733	00474
H734	00501
H735	00506
H736	00513
H737	00520
H738	00525
H739	00532
H740	00537
H741	00544
H742	00551
H743	00556
H744	00563
H745	00570
H746	00575
H747	00602
H748	00607
H749	00614
H750	00621
H751	00626

H752 00633
H753 00640
H754 00645
H755 00652
H756 00660
H757 00662
H758 00670
H759 00672
H760 00700
H761 00702
H762 00710
H763 00712
H764 00720
H765 00722
H766 00730
H767 00732
H768 00740
H769 00742
H770 00750
H771 00752
H772 00760
H773 00762
H774 00770
H775 00772
H776 01000
H777 01002
H778 01010
H779 01012
H780 01020
H781 01022
H782 01030
H783 01032
H784 01040
H785 01042
H786 01050
H787 01052
H788 01060
H789 01062
H790 01070
H791 01072
H792 01100
H793 01102
H794 01110
H795 01112
H796 01120
H797 01122
H798 01130
H799 01132
H800 01140
H801 01142
H802 01150
H803 01152
H804 01160
H805 01162
H806 01170

H807	01172
H808	01200
H809	01202
H810	01210
H811	01212
H812	01220
H813	01222
H814	01230
H815	01232
H816	01240
H817	01242
H818	01250
H819	01252
H820	01260
H821	01262
H822	01270
H823	01272
H824	01300
H825	01302
H826	01310
H827	01312
H828	01320
H829	01326
H830	01334
H831	01342
H832	01350
H833	01356
H834	01364
H835	01372
H836	01400
H837	01406
H838	01413
H839	01415
H840	01422
H841	01427
H842	01434
H843	01441
H844	01446
H845	01453
H846	01460
H847	01465
H848	01472
H849	01477
H850	01504
H851	01511
H852	01516
H853	01523
H854	01530
H855	01535
H856	01542
H857	01547
H858	01554
H859	01556
H860	01563
H861	01565

H862	01572
H863	01574
H864	01601
H865	01603
H866	01610
H867	01612
H868	01617
H869	01621
H870	01626
H871	01630
H872	01635
H873	01637
H874	01644
H875	01646
H876	01653
H877	01655
H878	01662
H879	01664
H880	01671
H881	01673
H882	01700
H883	01702
H884	01707
H885	01711
H886	01716
H887	01720
H888	01725
H889	01727
H890	01734
H891	01736
H892	01743
H893	01745
H894	01753
H895	01755
H896	01763
H897	01765
H898	01773
H899	01775
H900	02003
H901	02005
H902	02013
H903	02015
H904	02023
H905	02025
H906	02033
H907	02035
H908	02043
H909	02045
H910	02053
H911	02055
H912	02063
H913	02065
H914	02073
H915	02075
H916	02103

H917	02105
H918	02113
H919	02115
H920	02123
H921	02125
H922	02133
H923	02135
H924	02143
H925	02145
H926	02153
H927	02155
H930	02163
H931	02165
H932	02172
H933	02174
H934	02202
H935	02204
H936	02212
H937	02214
H938	02222
H939	02224
H940	02232
H941	02234
H942	02242
H943	02244
H944	02252
H945	02254
H946	02262
H947	02264
H948	02272
H949	02274
H950	02302
H951	02304
H952A	02312
H952B	02314
H952C	02322
H952D	02324
H952E	02332
H952F	02334
H952G	02342
H952H	02344
H952I	02352
H952J	02354
H952K	02362
H952L	02364
H952M	02372
H952N	02374
H952O	02402
H952P	02404
H952Q	02412
H952R	02414
H952S	02422
H952T	02424
H952U	02432
H952V	02434

H952W	02442
H952X	02444
H952Y	02452
H952Z	02454
H952AA	02462
H952BB	02464
H952CC	02472
H952DD	02474
H952EE	02502
H952FF	02504
H952GG	02512
H952HH	02514
H952II	02522
H952JJ	02524
H953A	02532
H953B	02534
H953C	02542
H953D	02544
H953E	02552
H953F	02554
H953G	02562
H953H	02564
H953I	02572
H953J	02574
H953K	02602
H953L	02604
H953M	02612
H953N	02614
H953O	02622
H953P	02624
H953Q	02632
H953R	02634
H953S	02642
H953T	02644
H953U	02652
H953V	02654
H953W	02662
H953X	02664
H953Y	02672
H953Z	02674
H953AA	02702
H953BB	02704
H953CC	02712
H953DD	02714
H953EE	02722
H953FF	02724
H953GG	02732
H953HH	02734
H953II	02742
H953JJ	02744
H954	02753
H955	02755
H956	02764
H957	02766
H958	02775

H959	02777
H960	03006
H961	03010
H962	03017
H963	03021
H964	03030
H965	03032
H966	03041
H967	03043
H968	03052
H969	03054
H970	03063
H971	03065
H972	03074
H973	03076
H974	03105
H975	03107
H976	03116
H977	03120
H978	03127
H979	03131
H980	03140
H981	03142
H982	03151
H983	03153
H984	03162
H985	03164
H986	03173
H987	03175
H988	03204
H989	03206
H990	03215
H991	03217
H992	03226
H993	03230
H994	03237
H995	03241
H996	03250
H997	03252
H998	03261
H999	03263
H1000	03272
H1001	03274
H1002	03303
H1003	03305
H1004	03314
H1005	03316
H1006	03325
H1007	03327
H1007A	03336
H1008	03340
H1009	03347
H1010	03351
H1011	03360
H1012	03362

H1013	03371
H1014	03373
H1015	03402
H1016	03404
H1017	03413
H1018	03415
H1019	03424
H1020	03426
H1021	03435
H1022	03437
H1023	03446
H1024	03450
H1025	03456
H1026	03460
H1027	03466
H1028	03470
H1029	03476
H1030A	03500
H1030B	03513
H1030C	03515
H1030D	03520
H1030E	03523
H1030F	03526
H1030G	03531
H1030H	03534
H1030I	03537
H1030J	03542
H1030K	03545
H1030L	03550
H1030M	03553
H1031	03556
H1032	03561
H1033	03564
H1034	03567
H1035	03572
H1035A	03575
H1036	03600
H1037	03603
H1037A	03606
H1038	03611
H1039	03614
H1040	03617
H1041	03622
H1042	03625
H1043	03630
H1044	03633
H1045	03636
H1045A	03641
H1046	03644
H1047	03647
H1048	03652
H1049	03655
H1050	03660
H1051	03663
H1052	03666

H1053	03671
H1054	03674
H1055	03677
H1056	03702
H1057	03705
H1058	03710
H1059	03713
H1060	03716
H1061	03721
H1062	03724
H1063	03727
H1064	03732
H1065	03735
H1066	03740
H1067	03743
H1068	03746
H1069	03751
H1070	03754
H1071	03757
H1072	03763
H1073	03767
H1074	03773
H1075	03777
H1076	04003
H1078	04007
H1079	04013
H1080	04017
H1081	04023
H1082	04027
H1083	04033
H1084	04037
H1085	04043
H1086	04047
H1087	04053
H1088	04057
H1089	04063
H1090	04067
H1091	04073
H1092	04077
H1093	04103
H1094	04107
H1095	04113
H1096	04117
H1097	04123
H1098	04127
H1099	04133
H1100	04137
H1101	04143
H1102	04147
H1103	04153
H1104	04157
H1105	04163
H1106	04167
H1107	04173
H1108	04177

H1109	04204
H1110	04206
H1111	04213
H1112	04215
H1113	04222
H1114	04224
H1115	04231
H1116	04233
H1117	04240
H1118	04242
H1119	04247
H1120	04251
H1121	04256
H1122	04260
H1123	04265
H1124	04267
H1125	04274
H1126	04276
H1127	04303
H1128	04305
H1129	04312
H1130	04314
H1131	04321
H1132	04323
H1133	04330
H1134	04332
H1135	04337
H1136	04341
H1137	04346
H1138	04350
H1139	04355
H1140	04357
H1141	04364
H1142	04366
H1143	04373
H1144	04375
H1145	04414
H1146	04431
REGIN	04446
K0	04451
K400K	04452
K200K	04453
K100K	04454
K40K	04455
K20K	04456
K10K	04457
K4K	04460
K2K	04461
K1K	04462
K400	04463
K200	04464
K100	04465
K40	04466
K20	04467
K10	04470

K4	04471
K2	04472
K1	04473
K1S	04474
K2S	04475
K3S	04476
K4S	04477
K5S	04500
K6S	04501
K7S	04502
M400K	04503
M200K	04504
M100K	04505
M40K	04506
M20K	04507
M10K	04510
M4K	04511
M2K	04512
M1K	04513
M400	04514
M200	04515
M100	04516
M40	04517
M20	04520
M10	04521
M4	04522
M2	04523
M1	04524
M400KM	04525
M200KM	04526
M100KM	04527
M40KM	04530
M20KM	04531
M10KM	04532
M4KM	04533
K5252	04534
K2525	04535
M2KM	04536
M1KM	04537
M400M	04540
M200M	04541
M100M	04542
M40M	04543
M20M	04544
M10M	04545
M4M	04546
M2M	04547
M1M	04550
K3	04551
K7	04552
K17	04553
K37	04554
K77	04555
K177	04556
K377	04557

K777	04560
K1777	04561
K3777	04562
K7777	04563
K17777	04564
K37777	04565
K77777	04566
M600K	04567
M3	04570
M7	04571
M17	04572
M37	04573
M77	04574
M177	04575
M377	04576
M777	04577
M1777	04600
M3777	04601
M7777	04602
M17777	04603
M37777	04604
M77777	04605
K600K	04606
K777K	04607
K5	04610
K11	04611
K21	04612
K41	04613
K101	04614
K201	04615
K401	04616
K1001	04617
K2001	04620
K4001	04621
K10001	04622
KHALT	04623
KCAL0	04624
K2021	04625
K2120	04626
KCALE	04627
KSKP	04630
KCLA	04631
K5253	04632
K60K	04633
K6625	04634
K615	04635
COUNT	04636
CLSF	700001
CLOF	700004
CLON	700044
RSF	700101
RCF	700102
RSA	700104
RRB	700112
RSB	700144

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PSF	700201
PCF	700202
PSA	700204
PSB	700244
KSF	700301
KRB	700312
TSF	700401
TCF	700402
TLS	700406
EEM	707702
LEM	707704
NOP1	740000
NOP2	740000
NOP3	740000
HALT	740040